SimPowerSystems

For Use with Simulink®

Hydro-Québec

TransÉnergie Technologies

Modeling

Simulation |

Implementation



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SimPowerSystems User's Guide

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What Is SimPowerSystems?

Starting with MathWorks Release 13, the Power System Blockset has been renamed to SimPowerSystems. As a part of the Physical Modeling family, SimPowerSystems and SimMechanics work together with Simulink to model electrical, mechanical, and control systems.

Electrical power systems are combinations of electrical circuits and electromechanical devices like motors and generators. Engineers working in this discipline are constantly improving the performance of the systems. Requirements for drastically increased efficiency have forced power system designers to use power electronic devices and sophisticated control system concepts that tax traditional analysis tools and techniques. Further complicating the analyst's role is the fact that the system is often so nonlinear that the only way to understand it is through simulation.

Land-based power generation from hydroelectric, steam, or other devices is not the only use of power systems. A common attribute of these systems is their use of power electronics and control systems to achieve their performance objectives.

SimPowerSystems was designed to provide a modern design tool that will allow scientists and engineers to rapidly and easily build models that simulate power systems. SimPowerSystems uses the Simulink® environment, allowing a model to be built using simple *click and drag* procedures. Not only can you draw the circuit topology rapidly, but your analysis of the circuit can include its interactions with mechanical, thermal, control, and other disciplines. This is possible because all the electrical parts of the simulation interact with the extensive Simulink modeling library. Since Simulink uses MATLAB® as the computational engine, designers can also use MATLAB toolboxes and Simulink blocksets.

Users can rapidly put SimPowerSystems to work. The libraries contain models of typical power equipment such as transformers, lines, machines, and power electronics. These models are proven ones coming from textbooks, and their validity is based on the experience of the Power Systems Testing and Simulation Laboratory of Hydro-Québec, a large North American utility located in Canada. The capabilities of SimPowerSystems for modeling a typical electrical grid are illustrated in demonstration files. And for users who want to refresh their knowledge of power system theory, there are also self-learning case studies.

Using This Guide

If you are a new user, begin with the first two chapters to learn

- How to build and simulate electrical circuits using the **powerlib** library
- How to interface an electrical circuit with Simulink blocks
- How to analyze the steady-state and frequency response of an electrical circuit
- How to build your own nonlinear models

If you are an experienced blockset user, see these chapters:

- The Release Notes for details on the latest release
- "Tutorial" to learn how to simulate discretized electrical circuits
- "Case Studies" for an overview of some applications of the Power System Blockset and the revised case studies
- "Advanced Topics" to learn how to increase simulation speed

All blockset users should use the "Power System Block Reference" for reference information on blocks, simple demos, and GUI-based tools. For commands, refer to "Power System Command Reference" for a synopsis of the command's syntax, as well as a complete explanation of options and operation.

Product Name The rest of this Release 13 user's guide refers to SimPowerSystems as Power System Blockset.

Units

This manual uses the International System of Units (SI).

Quantity	Unit	Symbol
Time	second	s
Length	meter	m
Mass	kilogram	kg
Energy	joule	J
Current	ampere	A
Voltage	volt	V
Active power	watt	W
Apparent power	volt-ampere	VA
Reactive power	var	var
Impedance	ohm	Ω
Resistance	ohm	Ω
Inductance	henry	Н
Capacitance	farad	F
Flux linkage	volt-second	V. s
Rotation speed	radians per second revolutions per minute	rad/s rpm
Torque	newton-meter	N.m
Inertia	kilogram-meter ²	kg.m ²
Friction factor	newton-meter-second	N.m.s

The manual also uses the per unit (p.u.) system on occasion to define the model parameters.

What Is the Per Unit System?

The per unit system is widely used in the power system industry to express values of voltages, currents, powers, and impedances of various power equipment. It is mainly used for transformers and AC machines.

For a given quantity (voltage, current, power, impedance, torque, etc.) the per unit value is the value related to a base quantity.

base value in p.u. =
$$\frac{\text{quantity expressed in SI units}}{\text{base value}}$$

Generally the following two base values are chosen:

- The base power = nominal power of the equipment
- The base voltage = nominal voltage of the equipment

All other base quantities are derived from these two base quantities. Once the base power and the base voltage are chosen, the base current and the base impedance are determined by the natural laws of electrical circuits.

$$base current = \frac{base power}{base voltage}$$

$$base\ impedance\ =\ \frac{base\ voltage}{base\ current}\ =\ \frac{\left(base\ voltage\right)^2}{base\ power}$$

For a transformer with multiple windings, each having a different nominal voltage, the same base power is used for all windings (nominal power of the transformer). However, according to the above definitions, there are as many base values as windings for voltages, currents, and impedances.

For AC machines, the torque and speed can be also expressed in p.u. The following base quantities are chosen:

- The base speed = synchronous speed
- The base torque = torque corresponding at base power and synchronous speed:

$$base\ torque\ =\ \frac{base\ power\ (3\ phases)\ in\ watts}{base\ speed\ in\ radians/second}$$

Instead of specifying the rotor inertia in kg^*m^2 , you would generally give the inertia constant H defined as

 $H = \frac{\text{kinetic energy stored in the rotor at synchronous speed in joules}}{\text{machine nominal power in VA}}$

$$H = \frac{\frac{1}{2} \times J \cdot w^2}{Pnom}$$

The inertia constant is expressed in seconds. For large machines, this constant is around 3 to 5 seconds. An inertia constant of 3 seconds means that the energy stored in the rotating part could supply the nominal load during 3 seconds. For small machines, H is lower. For example, for a 3 HP motor, it can be between 0.5 and 0.7 seconds.

Example 1: Three-Phase Transformer

Consider, for example, a three-phase two-winding transformer. The following typical parameters could be provided by the manufacturer:

- Nominal power = 300 kVA total for three phases
- Nominal frequency = 60 Hz
- Winding 1: connected in wye, nominal voltage = 25 kV rms line-to-line resistance 0.01 p.u., leakage reactance = 0.02 p.u.
- Winding 2: connected in delta, nominal voltage = 600 V rms line-to-line resistance 0.01 p.u., leakage reactance = 0.02 p.u.
- Magnetizing losses at nominal voltage in % of nominal current: Resistive 1%, Inductive 1%

The base values for each single phase transformer are first calculated:

• For winding 1:

Base power 300 kVA/3 = 100e3 VA/phase Base voltage 25 kV/sqrt(3) = 14434 V rms Base current 100e3/14434 = 6.928 A rms Base impedance $14434/6.928 = 2083 \Omega$

Base resistance $14434/6.928 = 2083 \Omega$ Base inductance $2083/(2\pi^*60) = 5.525 \text{ H}$

• For winding 2:

Base power 300 kVA/3 = 100e3 VA

Base voltage 600 V rms

Base current 100e3/600 = 166.7 A rms

Base impedance $600/166.7 = 3.60 \Omega$ Base resistance $600/166.7 = 3.60 \Omega$

Base inductance $3.60/(2\pi*60) = 0.009549 \text{ H}$

The values of the winding resistances and leakage inductances expressed in SI units are therefore

- For winding 1: R1= $0.01 * 2083 = 20.83 \Omega$; L1= 0.02*5.525 = 0.1105 H
- For winding 2: R2= $0.01 * 3.60 = 0.0360 \Omega$; L2= 0.02*0.009549 = 0.191 mH

For the magnetizing branch, magnetizing losses of 1% resistive and 1% inductive mean a magnetizing resistance Rm of 100 p.u. and a magnetizing inductance Lm of 100 p.u. Therefore, the values expressed in SI units referred to winding 1 are

- $Rm = 100*2083 = 208.3 \text{ k}\Omega$
- Lm = 100*5.525 = 552.5 H

Example 2: Asynchronous Machine

Now consider the three-phase four-pole Asynchronous Machine in SI units provided in the Machines library of **powerlib**. It is rated 3 HP, 220 V rms line-to-line, 60 Hz.

The stator and rotor resistance and inductance referred to stator are

- Rs = 0.435Ω ; Ls = 2 mH
- $Rr = 0.816 \Omega$; Lr = 2 mH

The mutual inductance is Lm = 69.31 mH. The rotor inertia is $J = 0.089 \text{ kg.m}^2$.

The base quantities for one phase are calculated as follows:

Base power 3 HP*746VA/3 = 746 VA/phase

Base voltage 220 V/sqrt(3) = 127.0 V rms

Base current 746/127.0 = 5.874 A rms

Base impedance $127.0/5.874 = 21.62 \Omega$

Base resistance $127.0/5.874 = 21.62 \Omega$

Base inductance $21.62/(2\pi^*60) = 0.05735 \text{ H} = 57.35 \text{ mH}$

Base speed $1800 \text{ rpm} = 1800*(2\pi)/60 = 188.5 \text{ radians}/$

second

Base torque (3-phase) 746*3/188.5 = 11.87 newton-meters

Using the above base values, you can compute the values in per units.

Rs = 0.435 / 21.62 = 0.0201 p.u. Ls = 2 / 57.35 = 0.0349 p.u.

Rr = 0.816 / 21.62 = 0.0377 p.u. Lr = 2 / 57.35 = 0.0349 p.u.

Lm = 69.31/57.35 = 1.208 p.u.

The inertia is calculated from inertia J, synchronous speed, and nominal power.

$$Y = \frac{\frac{1}{2} \times J \cdot w^2}{Pnom} = \frac{\frac{1}{2} \times 0.089 \times (188.5)^2}{3 \times 746} = 0.7065 \text{ seconds}$$

If you open the dialog box of the Asynchronous Machine block in p.u. units provided in the Machines library of **powerlib**, you find that the parameters in p.u. are the ones calculated above.

Base Values for Instantaneous Voltage and Current Waveforms

When displaying instantaneous voltage and current waveforms on graphs or oscilloscopes, you normally consider the peak value of the nominal sinusoidal voltage as 1 p.u. In other words, the base values used for voltage and currents are the rms values given above multiplied by $\sqrt{2}$.

Why Use the Per Unit System Instead of the Standard SI Units?

Here are the main reasons for using the per unit system:

- When values are expressed in p.u., the comparison of electrical quantities with their "normal" values is straightforward.
 - For example, a transient voltage reaching a maximum of 1.42 p.u. indicates immediately that this voltage exceeds the nominal value by 42%.
- The values of impedances expressed in p.u. stay fairly constant whatever the power and voltage ratings.

For example, for all transformers in the 3 kVA to 300 kVA power range, the leakage reactance varies approximately between 0.01 p.u. and 0.03 p.u., whereas the winding resistances vary between 0.01 p.u. and 0.005 p.u., whatever the nominal voltage. For transformers in the 300 kVA to 300 MVA range, the leakage reactance varies approximately between 0.03 p.u. and 0.12 p.u., whereas the winding resistances vary between 0.005 p.u. and 0.002 p.u.

Similarly, for salient pole synchronous machines, the synchronous reactance $X_{\rm d}$ is generally between 0.60 and 1.50 p.u., whereas the sub transient reactance $X_{\rm d}'$ is generally between 0.20 and 0.50 p.u.

It means that if you do not know the parameters for a 10 kVA transformer, you will not make a big mistake by assuming an average value of 0.02 p.u. for leakage reactances and 0.0075 p.u. for winding resistances.

• The calculations using the per unit system are simplified. When all impedances in a multivoltage power system are expressed on a common power base and on the nominal voltages of the different subnetworks, the total impedance in p.u. seen at one bus is obtained by simply adding all impedances in p.u., without taking into consideration the transformer ratios.

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Related Products

The MathWorks provides several products that are especially relevant to the kinds of tasks you can perform with the Power System Blockset. They are listed in the table below. In particular, the Power System Blockset *requires* these products:

- MATLAB 6.5
- Simulink 5.0

For more information about any of these products, see either

- The online documentation for that product, if it is installed or if you are reading the documentation from the CD
- The MathWorks Web site at http://www.mathworks.com; see the "Products" section

Product	Description
DSP Blockset	Design and simulate DSP systems
Fuzzy Logic Toolbox	Design and simulate fuzzy logic systems
μ-Analysis and Synthesis Toolbox	Design multivariable feedback controllers for systems with model uncertainty
Neural Network Toolbox	Design and simulate neural networks
Nonlinear Control Design Blockset	Optimize design parameters in nonlinear control systems
Optimization Toolbox	Solve standard and large-scale optimization problems
Robust Control Toolbox	Design robust multivariable feedback control systems
SimMechanics	Model and simulate mechanical systems

Product	Description
Stateflow [®]	Design and simulate event-driven systems
System Identification Toolbox	Create linear dynamic models from measured input-output data

Typographical Conventions

This manual uses some or all of these conventions.

Item	Convention	Example
Example code	Monospace font	To assign the value 5 to A, enter A = 5
Function names, syntax, filenames, directory/folder names, user input, items in drop-down lists	Monospace font	The cos function finds the cosine of each array element. Syntax line example is MLGetVar ML_var_name
Buttons and keys	Boldface with book title caps	Press the Enter key.
Literal strings (in syntax descriptions in reference chapters)	Monospace bold for literals	<pre>f = freqspace(n,'whole')</pre>
Mathematical expressions	Italics for variables Standard text font for functions, operators, and constants	This vector represents the polynomial $p = x^2 + 2x + 3$.
MATLAB output	Monospace font	MATLAB responds with A = 5
Menu and dialog box titles	Boldface with book title caps	Choose the File Options menu.
New terms and for emphasis	Italics	An <i>array</i> is an ordered collection of information.
Omitted input arguments	() ellipsis denotes all of the input/output arguments from preceding syntaxes.	[c,ia,ib] = union()
String variables (from a finite list)	Monospace italics	<pre>sysc = d2c(sysd,'method')</pre>

Tutorial

To master the Power System Blockset, you must learn how to build and simulate electrical circuits. The Power System Blockset operates in the Simulink environment. Therefore, before starting this training you should be familiar with Simulink. For help on using Simulink, see the Using Simulink guide.

The tutorial is organized into eight sessions. Sessions 1 through 3 are based on a simple power system. Sessions 4 and 5 illustrate power electronics. Session 6 introduces the phasor method, and Session 7 demonstrates three-phase power systems, electrical machinery, and load flow. Session 8 explains how you can create and customize your own nonlinear blocks.

Session 1: Simulating a Simple Circuit (p. 1-2)	Build a simple circuit with Power System blocks and connect it to other Simulink blocks
Session 2: Analyzing a Simple Circuit (p. 1-9)	Use the Powergui block and analyze static and frequency-domain response
Session 3: Simulating Transients (p. 1-18)	Create an electrical subsystem, simulate transients, and discretize simple circuits
Session 4: Introducing Power Electronics (p. 1-24)	Use power electronics and transformers and vary a circuit's initial conditions
Session 5: Simulating Motor Drives (p. 1-31)	Model and discretize simple motors with specialized blocks
Session 6: Introducing the Phasor Simulation Method (p. 1-42)	Use the phasor method to analyze magnitudes and phases in linear circuits
Session 7: Three-Phase Systems and Machines (p. 1-47)	Use electrical machines and three-phase components
Session 8: Building and Customizing Nonlinear Models (p. 1-57)	Model nonlinear systems and create your own blocks to represent them

Session 1: Simulating a Simple Circuit

The Power System Blockset allows you to build and simulate electrical circuits containing linear and nonlinear elements. During Sessions 1 through 3 you will build, analyze, and simulate the following circuit.

In this session you

- Explore the **powerlib** library of the Power System Blockset
- Learn how to build a simple circuit from the **powerlib** library
- Interconnect Simulink blocks with your circuit

The circuit below represents an equivalent power system feeding a 300 km transmission line. The line is compensated by a shunt inductor at its receiving end. A circuit breaker allows energizing and deenergizing of the line. In order to simplify matters, only one of the three phases is represented. The parameters shown in the figure are typical of a 735 kV power system.

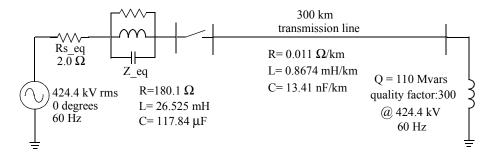


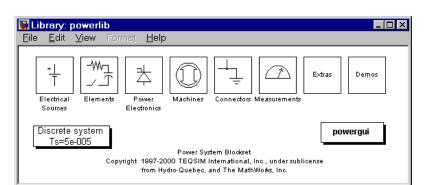
Figure 1-1: Circuit to Be Modeled with the Power System Blockset

Building the Electrical Circuit with powerlib Library

The graphical user interface makes use of the Simulink functionality to interconnect various electrical components. The electrical components are grouped in a special library called **powerlib**.

Open the Power System Blockset library by entering the following command at the MATLAB prompt:

powerlib



This command displays a Simulink window showing icons of different block libraries.

You can open these libraries to produce the windows containing the blocks to be copied into your circuit. Each component is represented by a special icon having one or several inputs and outputs corresponding to the different terminals of the component:

- 1 From the **File** menu of the **powerlib** window, open a new window that will contain your first circuit and save it as circuit1.
- **2** Open the Electrical Sources library and copy the AC Voltage Source block into the **circuit1** window.
- **3** Open the **AC Voltage Source** dialog box by double-clicking the icon and enter the Amplitude, Phase, and Frequency parameters according to the values shown in Figure 1-1.

Note that the amplitude to be specified for a sinusoidal source is its peak value (424.4e3*sqrt(2) volts in our case).

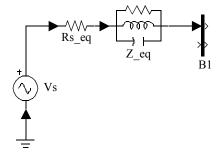
- **4** Change the name of this block from Voltage Source to Vs.
- **5** Copy the Parallel RLC Branch block, which can be found in the Elements library of **powerlib**, set its parameters as shown in Figure 1-1, and name it Z_eq.
- **6** The resistance Rs_eq of the circuit can be obtained from the Parallel RLC Branch block. Duplicate the Parallel RLC Branch block, which is already in

your **circuit1** window, set the R parameter according to Figure 1-1, and set the L and C parameters respectively to infinity (inf) and zero (0).

When the dialog box is closed, you will notice that the L and C components have disappeared so that the icon now shows a single resistor. The same result would have been obtained with the Series RLC Branch block by setting L and C respectively at zero and inf.

- 7 Name this block Rs eq.
- **8** Open the Connectors library of **powerlib** and copy a bus bar.
- **9** Open the **Bus Bar** dialog box, set its parameters at two inputs and two outputs, and name it B1. Also copy the Ground block (select the block with an output connection).

Resize the various components and interconnect blocks by dragging lines from outputs to inputs of appropriate blocks.



In order to complete the circuit of Figure 1-1, you need to add a transmission line and a shunt reactor. You will add the circuit breaker later in Session 3.

The model of a line with uniformly distributed R, L, and C parameters normally consists of a delay equal to the wave propagation time along the line. This model cannot be simulated as a linear system because a delay corresponds to an infinite number of states. However, a good approximation of the line with a finite number of states can be obtained by cascading several PI circuits, each representing a small section of the line.

A PI section consists of a series R-L branch and two shunt C branches. The model accuracy depends on the number of PI sections used for the model. Copy

the PI Section Line block from the Elements library into the **circuit1** window, set its parameters as shown in Figure 1-1, and specify one line section.

The shunt reactor is modeled by a resistor in series with an inductor. You could use a Series RLC Branch block to model the shunt reactor. Set the $\bf R$ and $\bf L$ values corresponding to the active and reactive power specified in Figure 1-1 (Q=110 Mvar; P=110/300=0.37 MW at V=424.4 kV rms and f= 60 Hz).

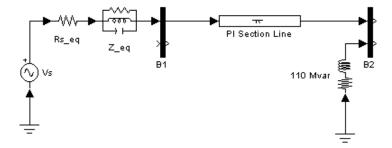
You might find it more convenient to use a Series RLC Load block that allows you to specify directly the active and reactive powers absorbed by the shunt reactor.

Copy the Series RLC Load block, which can be found in the Elements library of **powerlib**. Name this block 110 Mvar. Set its parameters as follows:

```
Vn=424.4e3V; fn=60 Hz; P=110e6/300 W (quality factor=300); QL=110e6 vars; Qc=0
```

Note that, as no reactive capacitive power has been specified, the capacitor disappears on the block icon when the dialog box is closed.

Add a receiving end bus bar B2 by duplicating B1, and interconnect all these new blocks as shown.



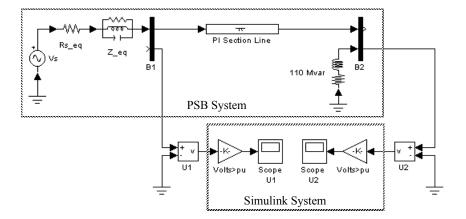
You need a Voltage Measurement block to measure the voltage at bus bar B1. This block can be found in the Measurements library of **powerlib**. Copy it and name it U1. Connect its positive input to the second output of bus bar B1 and its negative input to a new Ground block.

In order to observe the voltage at the bus bar B1 measured by the Voltage Measurement block named U1, a display system is needed. This could be any device found in the Sinks library of Simulink.

Open the Sinks library of Simulink and copy the Scope block in your **circuit1** window. If the scope were connected directly at the output of the voltage measurement, it would display the voltage in volts. However, electrical engineers in power systems are used to working with normalized quantities (per unit system). The voltage is normalized by dividing the value in volts by a base voltage corresponding to the peak value of the system nominal voltage. In this case the scaling factor K is

$$K = \frac{1}{424.4 \times 10^3 \times \sqrt{2}}$$

Copy a Gain block from the Simulink library and set its gain as above. Connect its output to the Scope block and connect the output of the Voltage Measurement block to the Gain block. Duplicate this voltage measurement system at the bus bar B2, as shown below.

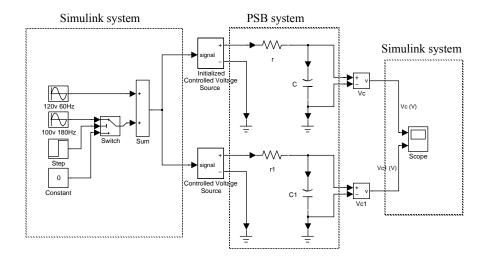


Interfacing the Electrical Circuit with Simulink

The Voltage Measurement block acts as an interface between the Power System Blockset blocks and the Simulink blocks. For the system shown above, the link is done from the electrical system to the Simulink system. The Voltage Measurement blocks converts the measured voltages into Simulink signals.

Note that the Current Measurement block from the Measurements library of **powerlib** can also be used to convert any measured current into a Simulink signal.

The link from Simulink blocks to the electrical system is also possible. For example, you can use the Controlled Voltage Source block to inject a voltage in an electrical circuit. The voltage is then controlled by a Simulink signal.



Simulating Your Circuit

Now you can start the simulation from the **Simulation** menu. As expected, voltage is sinusoidal with peak value of 1 p.u.

While the simulation is running, open the **Vs block** dialog box and modify the amplitude. Observe the effect on the two scopes. You can also modify the frequency and the phase. Remember that you can zoom in on the waveforms in the scope windows by rubber banding (using the left mouse button) around the region of interest.

Note To simulate this circuit, the default integration algorithm (ode45) has been used. However, for most applications of the Power System Blockset your circuits will contain switches and other nonlinear models. In such a case, you must specify a different integration algorithm. This is discussed in "Session 3: Simulating Transients" on page 1-18, where a circuit breaker is added to your circuit.

Session 2: Analyzing a Simple Circuit

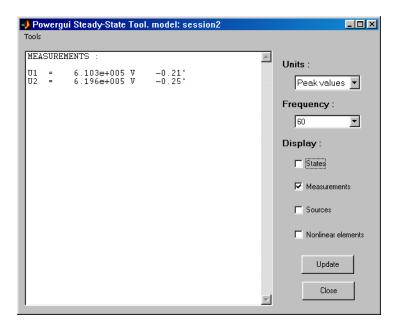
In this session you

- Use the Powergui (graphical user interface) block
- Obtain the steady-state outputs of the system
- Analyze your circuit with the power2sys function
- Analyze an electrical circuit in the frequency domain

Steady-State Analysis

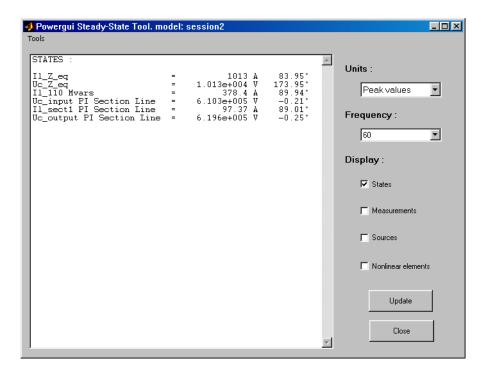
In order to facilitate the steady-state analysis of your circuit, a graphical user interface (GUI) is provided in the **powerlib** library. Copy the Powergui block into your **circuit1** window and double-click the block icon to open it.

This opens the **Steady State** window where the steady-state phasors measured by the two measurement blocks are displayed in polar form.



Each measurement output is identified by a string corresponding to the measurement block name. The magnitudes of the phasors U1 and U2 correspond to the peak value of the sinusoidal voltages.

From the **Steady State** window you can also choose to display the steady-state values of the source voltage or the steady-state values of states by checking either the **Sources** or the **States** check box.



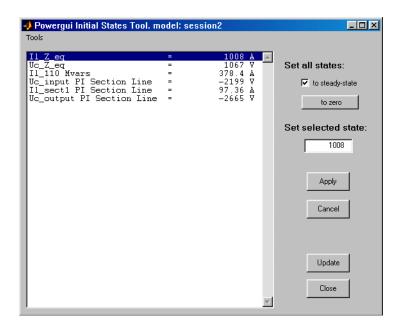
The state variable names contain the name of the block where the inductor or capacitor is found, preceded by the I1_ prefix for inductor currents or the Uc_ prefix for capacitor voltages.

The sign conventions used for the voltages and currents of sources and state variables are determined by the orientation of the blocks:

- Inductor currents flowing in the arrow direction are considered positive.
- Capacitor voltages are $V_{block\ output}$ $V_{block\ input}$.

Note Depending on the exact position of the various blocks in your circuit1 diagram, the state variables might not be ordered in the same way as in the preceding figure.

Now, from the **Tools** menu of the **powergui**, select **Initial Values of State Variables** —> **Display or Modify Initial State Values.** The initial values of the six state variables (three inductor currents and three capacitor voltages) are displayed. These initial values are set in order to start the simulation in steady state.



Frequency Analysis

The Measurements library of **powerlib** contains an Impedance Measurement block that measures the impedance between any two nodes of a circuit. In the following two sections you measure the impedance of your circuit between bus B2 and ground by using two methods:

- Calculation from the state space model
- Automatic measurement using the Impedance Measurement block and the Powergui block

Obtaining the Impedance vs. Frequency from the State-Space Model

To measure the impedance versus frequency at bus bar B2, you will need a current source at bus bar B2 providing a second input to the state-space model. Open the Electrical Sources library and copy the AC Current Source block into your model. Connect this source at bus bar B2, as shown below. Set the current source magnitude to 0 and keep its frequency at 60 Hz. Rearrange the blocks as follows:

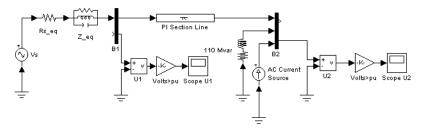


Figure 1-2: AC Current Source at the B2 Bus Bar

Now compute the state-space representation of the model circuit1 with the power2sys function. Enter the following command at the MATLAB prompt.

```
[A,B,C,D,x0,states,inputs,outputs]=power2sys('circuitl');
```

The power2sys function returns the state-space model of your circuit in the four matrices A, B, C, and D. x0 is the vector of initial conditions that you have just displayed with the Powergui block. The names of the state variables, inputs, and outputs are returned in three string matrices.

```
states =
Il_110 Mvars
Uc_input PI Section Line
Il_ sect1 PI Section Line
Uc_output PI Section Line
Il_Z_eq
Uc_Z_eq
```

```
inputs =
U_Vs
I_AC Current Source
outputs =
U_U1
U_U2
```

Note that you could have obtained the names and ordering of the states, inputs and outputs directly from the Powergui block.

Once the state-space model of the system is known, it can be analyzed in the frequency domain. For example, the modes of this circuit can be found from the eigenvalues of matrix A (use the MATLAB eig command).

```
eig(A)

ans =

1.0e+05 *

-2.4972

-0.0001 + 0.0144i <---229 Hz

-0.0001 - 0.0144i

-0.0002 + 0.0056i <---89 Hz

-0.0002 - 0.0056i

-0.0000
```

This system has two oscillatory modes at 89 Hz and 229 Hz. The 89 Hz mode is due to the equivalent source, which is modeled by a single pole equivalent. The 229 Hz mode is the first mode of the line modeled by a single PI section.

If you own the Control System Toolbox, you can compute the impedance of the network as a function of frequency by using the bode function.

In the Laplace domain, the impedance Z2 at bus B2 is defined as the transfer function between the current injected at bus B2 (input 2 of the system) and the voltage measured at bus B2 (output 2 of the system):

$$Z2(s) = \frac{U2(s)}{I2(s)}$$

The impedance at bus B2 for the 0-1500Hz range can be calculated and visualized as follows:

```
freq=0:1500;
w=2π*freq;
[mag1,phase1]=bode(A,B,C,D,2,w);
semilogy(freq,mag1(:,2));
```

Repeat the same process to get the frequency response with a 10 section line model. Open the **PI Section Line** dialog box and change the number of sections from 1 to 10. To calculate the new frequency response and superimpose it upon the one obtained with a single line section, enter the following commands:

```
[A,B,C,D]=power2sys('circuit1');
[mag10,phase10]=bode(A,B,C,D,2,w);
semilogy(freq,mag1(:,2),freq,mag10(:,2));
```

This is the resulting plot.

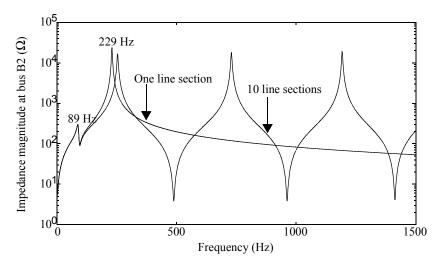


Figure 1-3: Impedance at Bus B2 as Function of Frequency

This graph indicates that the frequency range represented by the single line section model is limited to approximately 150 Hz. For higher frequencies, the 10 line section model is a better approximation.

For a distributed parameter line model the propagation speed is

$$v = \frac{1}{\sqrt{L \cdot C}} = 293,208 \text{km/s}$$

The propagation time for 300 km is therefore T=300/293,208=1.023 ms and the frequency of the first line mode is f1=1/4T=244 Hz. A distributed parameter line would have an infinite number of modes every 244 + n * 488 Hz (n=1,2,3...). The 10 section line model simulates the first 10 modes. The first three line modes can be seen in Figure 1-3. (244Hz, 732Hz, and 1220 Hz).

Obtaining the Impedance vs Frequency from the Impedance Measurement Block and the Powergui Block

The process described above to measure a circuit impedance has been automated in the Power System Blockset. Open the Measurements library of **powerlib**, copy the Impedance Measurement block into your model, and rename it ZB2. The block uses a current source and a voltage measurement to perform the impedance measurement. Connect the two inputs of this block between bus bar B2 and ground as shown.

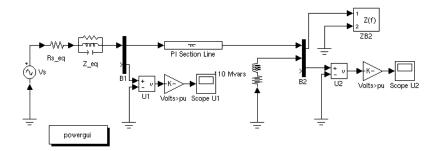
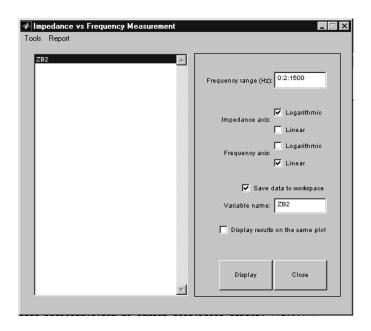


Figure 1-4: Measuring Impedance vs Frequency with the Impedance Measurement Block

Now open the **powergui**. In the **Tools** menu, select **Impedance vs Frequency Measurement**. A new window opens, showing the list of Impedance Measurement blocks available in your circuit.



In your case, only one impedance is measured, and it is identified by ZB2 (the name of the ZB2 block) in the window. Fill in the frequency range by entering 0:2:1500 (zero to 1500 Hz by steps of 2 Hz). Select the logarithmic scale to display Z magnitude. Check the **Save data to workspace** button and enter ZB2 as the variable name that will contain the impedance vs frequency. Click the **Display** button.

When the calculation is finished, a graphic window appears with the magnitude and phase as functions of frequency. The magnitude should be identical to the plot (for one line section) shown in Figure 1-3. If you look in your workspace, you should have a variable named ZB2. It is a two-column matrix containing frequency in column 1 and complex impedance in column 2.

Now open the **Simulation** —> **Simulation parameters** dialog of your circuit1 model. On the **Solver** pane, select the ode23tb integration algorithm. Set the relative tolerance to 1e-4 and keep auto for the other parameters. Set the stop time to 0.05. Open the scopes and start the simulation.

Look at the waveforms of the sending and receiving end voltages on ScopeU1 and ScopeU2. As the state variables have been automatically initialized, the system starts in steady state and sinusoidal waveforms are observed.

Finally open the **powergui**. In the **Tools** menu select **Initial Values of State Variables** —> **Display or Modify Initial State Value** and reset all the states to 0 by selecting the **Reset to zero** button and then the **Apply** button. Restart the simulation and observe the transient when the line is energized from 0.

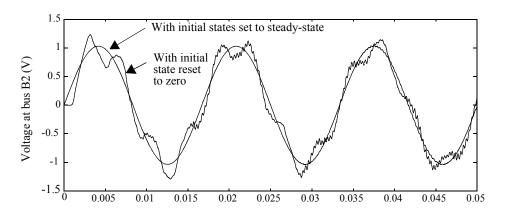


Figure 1-5: Receiving End Voltage U2 with 10 PI Section Line

Session 3: Simulating Transients

In this session you

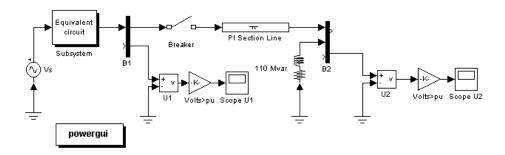
- Learn how to create an electrical subsystem
- Simulate transients with a circuit breaker
- Compare time domain simulation results with different line models
- Learn how to discretize a circuit and compare results obtained with continuous variable time-step algorithm and discrete system

One of the main uses of the Power System Blockset is to simulate transients in electrical circuits. This can be done with either mechanical switches (circuit breakers) or switches using power electronic devices.

First open your circuit1 system and delete the current source connected at bus B2. Save this new system as circuit2. Before connecting a circuit breaker, you need to modify the schematic diagram of circuit2. As with Simulink, the Power System Blockset allows you to group several components into a subsystem. This feature is useful to simplify complex schematic diagrams.

Use this feature to transform the source impedance into a subsystem by

1 Selecting the two blocks identified as Rs_eq and Z_eq by surrounding them with a bounding box (left mouse button) and using the Edit —> Create subsystem menu item. The two blocks now form a new block called Subsystem.



2 Using the **Edit** —> **Mask subsystem** menu item, change the icon of that subsystem. In the **Icon** section of the mask editor, enter the following drawing command:

```
disp('Equivalent\nCircuit')
```

- **3** Use the **Format** —> **Show drop shadow** menu item to get the appearance shown in the figure. You can now double-click the Subsystem block and look at its content.
- **4** Insert a circuit breaker into your circuit in order to simulate a line energization by opening the Elements library of **powerlib**. Copy the Breaker block into your **circuit2** window.

The circuit breaker is a nonlinear element modeled by an ideal switch in series with a resistance. Because of modeling constraints, this resistance cannot be set to 0. However, it can be set to a very small value, say $0.001\,\Omega$, that does not affect the performance of the circuit.

1 Open the Breaker block dialog box and set its parameters as follows:

```
Ron=0.001 \Omega; Initial state=0 (open); Rs=inf; Cs=0; Switching times=[(1/60)/4]
```

- **2** Insert the circuit breaker in series with the sending end of the line, then rearrange the circuit as shown in the previous figure.
- 3 Finally connect a Scope block, from the Sinks library of Simulink, at the output of the Gain block measuring U2. Click the Scope **Parameters** icon and select the **Data history** tab. Select the **Save data to workspace** button and specify a variable name U2 to save the simulation results; then change the **Format** option for U2 to be **Array**. Also, clear the **Limit rows to last** button to display the entire waveform for long simulation times.

You are now ready to simulate your system.

Continuous Variable Time-Step Integration Algorithms

Open the **PI section Line** dialog box and make sure the number of sections is set to 1. Open the **Simulation** —> **Simulation parameters** dialog. As you now

have a system containing switches, you need a stiff integration algorithm to simulate the circuit. In the **Solver** pane, select the variable-step, stiff integration algorithm ode23tb.

Keep the default parameters (relative tolerance set at 1e-3) and set the stop time to 0.02 seconds. Open the scopes and start the simulation. Look at the waveforms of the sending and receiving end voltages on ScopeU1 and ScopeU2.

Once the simulation is complete, copy the variable U2 into U2_1 by entering the following command in the MATLAB window:

```
U2_1=U2;
```

These two variables now contain the waveform obtained with a single PI section line model.

Open the **PI section Line** dialog box and change the number of sections from 1 to 10. Start the simulation. Once the simulation is complete, copy the variable U2 into U2 10.

Before modifying your circuit to use a distributed parameter line model, save your system as circuit 2 10pi. You will have to reuse this circuit later.

Delete the PI section line model and replace it with a single phase Distributed Parameter Line block. Set the number of phases to 1 and use the same **R**, **L**, **C**, and **length** parameters as for the PI section line (see Figure 1-1). Save this system as circuit2_dist.

Restart the simulation and save the U2 voltage in the U2_d variable.

You can now compare the three waveforms obtained with the three line models. Each variable U2_1, U2_10, and U2_d is a two-column matrix where the time is in column 1 and the voltage is in column 2. Plot the three waveforms on the same graph by entering the following command:

```
plot(U2_1(:,1), U2_1(:,2), U2_10(:,1),U2_10(:,2), U2_d(:,1),U2_d(:,2));
```

These waveforms are shown in the next figure. As expected from the frequency analysis performed during Session 2, the single PI model does not respond to frequencies higher than 229 Hz. The 10 PI section model gives a better accuracy, although high-frequency oscillations are introduced by the discretization of the line. You can clearly see in the figure the propagation time delay of 1.03 ms associated with the distributed parameter line.

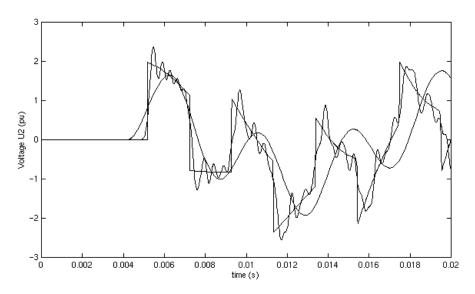


Figure 1-6: Receiving End Voltage Obtained with Three Different Line Models

Discretizing the Electrical System

One important feature of the Power System Blockset, which has been introduced with Version 2.0, is its ability to simulate either with continuous variable time-step integration algorithms or with discrete solvers. For small systems, variable time steps algorithms are usually faster than fixed-time step methods, because the number of integration steps is lower. However, for large systems that contain many states or many nonlinear blocks such as power electronic switches, it is advantageous to discretize the electrical system.

When you discretize your system, the precision of the simulation is controlled by the time step. If you use too large a time step, the precision might not be sufficient. The only way to know if it is acceptable is to repeat the simulation with different time steps and find a compromise for the largest acceptable time step. Usually time steps of 20 μs to 50 μs give good results for simulation of switching transients on 50 Hz or 60 Hz power systems or on systems using line-commutated power electronic devices such as diodes and thyristors. You must reduce the time step for systems using forced-commutated power electronic switches. These devices, the insulated-gate bipolar transistor

(IGBT), the field-effect transistor (FET), and the gate-turn-off thyristor (GTO) are operating at high switching frequencies.

For example, simulating a pulse-width modulated (PWM) inverter operating at 8 kHz would require a time step of at least $1 \mu s$.

You will now learn how to discretize your system and compare simulation results obtained with continuous and discrete systems. Open the circuit2_10pi system that you saved from a previous simulation. This system contains 24 states and one switch. Copy the Discrete System block of the **powerlib** library into your circuit5 system. Open it and set the sample time to 25e-6 s. When you restart the simulation, the power system is discretized using the Tustin method (corresponding to trapezoidal integration) using a 25 µs sample time.

Open the **Simulation** —> **Simulation parameters** —> **Solver** dialog and set the simulation time to 0.2 s. Start the simulation.

Note Once the system is discretized, there are no more continuous states in the electrical system, so you do not need a variable-step integration method to simulate. In the **Simulation** —> **Simulation parameters** —> **Solver** dialog, you could have selected the Fixed-step and discrete (no continuous states) options and specified a fixed step of $50 \, \mu s$.

In order to measure the simulation time, you can restart the simulation by entering the following commands:

```
tic; sim(gcs); toc
```

When the simulation is finished the elapsed time in seconds is displayed in the MATLAB window.

To return to the continuous simulation, open the Discrete System block and set the Sample time to zero. If you compare the simulation times, you will find that the discrete system simulates approximately 3.5 times faster than the continuous system.

In order to compare the precision of the two methods, perform the following three simulations:

1 Simulate a continuous system, with Ts = 0.

- **2** Simulate a discrete system, with $Ts = 25 \mu s$.
- 3 Simulate a discrete system, with $Ts = 50 \mu s$.

For each simulation, save the voltage U2 in a different variable. Use respectively U2c, U2d25, and U2d50. Plot the U2 waveforms on the same graph by entering the following command:

```
plot(U2c(:,1), U2c(:,2), U2d25(:,1),U2d25(:,2), U2d50(:,1),U2d(50:,2))
```

Using the zoom button of the graphic window, zoom in on the 4 - 12 ms region. You will see differences on the high frequency transients. The 25 μs compares reasonably well with the continuous simulation. However, increasing the time step to 50 μs produces appreciable errors. The 25 μs time step would therefore be acceptable for this circuit, while obtaining a gain of 3.5 on simulation speed.

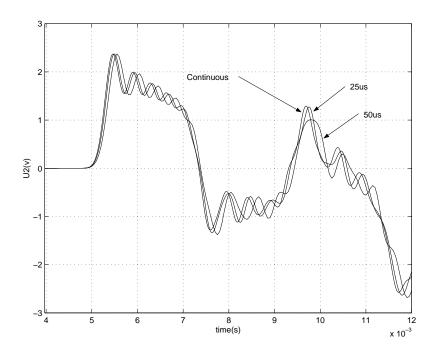


Figure 1-7: Comparison of Simulation Results for Continuous and Discrete Systems

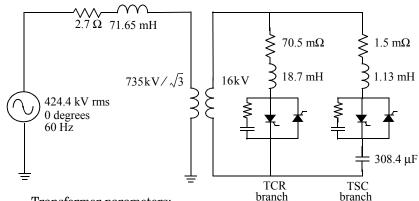
Session 4: Introducing Power Electronics

In this session you

- Learn how to use power electronics components
- Learn how to use transformers
- Change initial conditions of a circuit

The Power System Blockset has been designed to simulate power electronic devices. In this session, you build a simple circuit using thyristors.

Consider the circuit shown below. It represents one phase of a static var compensator (SVC) used on a 735 kV transmission network. On the secondary of the 735 kV /16 kV transformer, two variable susceptance branches are connected in parallel: one thyristor controlled reactor (TCR) branch and one thyristor switched capacitor (TSC) branch.



Transformer parameters:

Nominal power 110 MVA

Primary: Rated voltage 424.4 kV rms; leakage reactance = 0.15 p.u.; resistance = 0.002 p.u.

Secondary: Rated voltage 16 kV rms; leakage reactance = 0 p.u.;

Thyristor parameters:

Ron = 1 m Ω ; Vf = 14*0.8 V (14 thyristors in series)

Figure 1-8: One Phase of a TCR/TSC Static Var Compensator

The TCR and TSC branches are both controlled by a valve consisting of two thyristor strings connected in antiparallel. An RC snubber circuit is connected across each valve. The TSC branch is switched on/off, thus providing discrete step variation of the SVC capacitive current. The TCR branch is phase controlled in order to obtain a continuous variation of the net SVC reactive current.

You will now build two circuits illustrating the operation of the TCR and the TSC branches.

Simulation of the TCR Branch

- 1 Open a new window and save it as circuit3.
- **2** Open the Power Electronics library and copy the Thyristor block into your circuit3 model.
- **3** Open the **Thyristor** menu and set the parameters as follows:

```
Ron=1e-3; Lon=0; Vf=14*0.8; Rs=500; Cs=0.15e-6
```

Notice that the snubber circuit is integral to the **Thyristor** dialog box.

- **4** Rename this block Th1 and duplicate it.
- **5** Connect this new thyristor Th2 in antiparallel with Th1, as shown in Figure 1-9.

As the snubber circuit has already been specified with Th1, the snubber of Th2 must be eliminated.

6 Open the Th2 dialog box and set the snubber parameters to

```
Rs = Inf; Cs = 0.
```

Notice that the snubber disappears on the Th2 icon.

The linear transformer is located in the Elements library. Copy it, rename it to TrA, and open its dialog box. Set its nominal power, frequency, and winding parameters (winding 1 = primary; winding 2 = secondary) as shown in Figure 1-8.

Note that the leakage reactance and resistance of each winding have to be specified directly in per unit quantities. As there is no tertiary winding, enter 0 in the field corresponding to winding 3. Notice that winding 3 disappears on the TrA block.

Finally, set the magnetizing branch parameters Rm and Xm at [500, 500]. These values correspond to 0.2% resistive and inductive currents as specified in Figure 1-8.

Add a voltage source, series RL elements, and a ground block. Set the parameters as shown in Figure 1-8. Add a current measurement to measure the primary current. By using appropriate connectors, you should be able to interconnect the circuit as shown in Figure 1-9.

Notice that the Thyristor blocks have an output identified by the letter m. This output returns a Simulink vectorized signal containing the thyristor current (Iak) and voltage (Vak). Connect a Demultiplexer block with two outputs at the m output of Th1. Then connect the two multiplexer outputs to a dual trace scope that you rename Scope_Th1. (To create a second input to your scope, in the **Scope properties** —> **General** menu item, set the number of axes to 2.) Label the two connection lines Ith1 and Vth1. These labels are automatically displayed on the top of each trace.

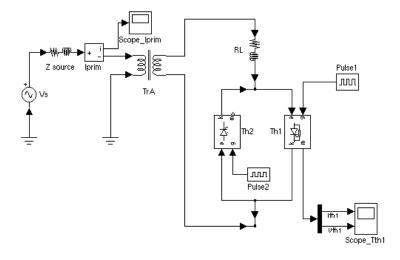


Figure 1-9: Simulation of the TCR Branch

You can now model the synchronized pulse generators firing thyristors Th1 and Th2. Copy two Simulink pulse generators into your system, name them Pulse1 and Pulse2, and connect them to the gates of Th1 and Th2.

Now you have to define the timing of the Th1 and Th2 pulses. At every cycle a pulse has to be sent to each thyristor α degrees after the zero crossing of the thyristor commutation voltage. Set the pulse1 and pulse2 parameters as follows:

```
Period : 1/60 s

Duty cycle: 1% (3.6 degrees pulses)

Amplitude :1

Start time : 1/60+T for Pulse1; 1/60+1/120+T for Pulse2
```

The pulses sent to Th1 are delayed by 180 degrees with respect to pulses sent to Th2. The delay T is used to specify the α firing angle. In order to get a 120 degrees firing angle, specify T in the workspace by entering

```
T=1/60/3;
```

Now open the **Simulation** —> **Simulation parameters** dialog. Select the ode23tb integration algorithm. Keep the default parameters but set the relative tolerance to 1e-4 and the stop time to 0.1. Start the simulation. The results are shown in Figure 1-10.

Note You could also choose to discretize your system. Try for example 50 μs sample time. The simulation results should compare well with the continuous system.

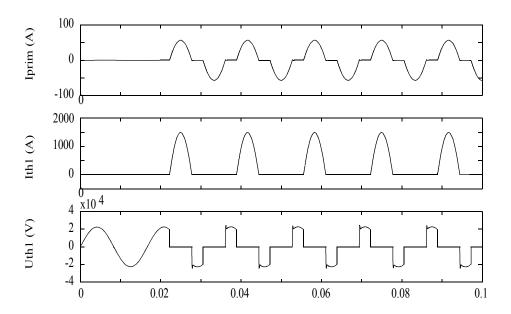


Figure 1-10: TCR Simulation Results

Simulation of the TSC Branch

You can now modify your circuit3 system and change the TCR branch to a TSC branch. Save circuit3 as a new system and name it circuit4.

Connect a capacitor in series with the RL inductor and Th1/Th2 valve as shown in Figure 1-11. Change the **R**, **L**, and **C** parameters as shown in Figure 1-8. Connect a voltmeter and scope to monitor the voltage across the capacitor.

Contrary to the TCR branch, which was fired by a synchronous pulse generator, a continuous firing signal is now applied to the two thyristors. Delete the two pulse generators. Copy a Step block from the Simulink library and connect its output at both gates of Th1 and Th2. Set its step time at 1/60/4 (energizing at the first positive peak of the source voltage). Your circuit should now be similar to the one shown here.

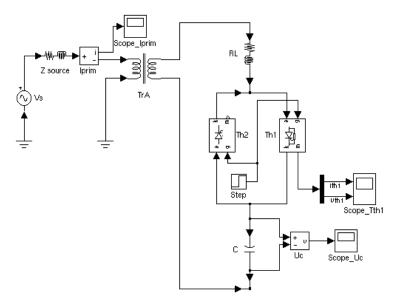


Figure 1-11: Simulation of the TSC Branch

Open the three scopes and start the simulation.

As the capacitor is energized from zero, you can observe a low damping transient at 200 Hz, superimposed with the 60 Hz component in the capacitor voltage and primary current. During normal TSC operation, the capacitor has an initial voltage left since the last valve opening. In order to minimize the closing transient with a charged capacitor, the thyristors of the TSC branch must be fired when the source voltage is at maximum value and with the correct polarity. The initial capacitor voltage corresponds to the steady-state voltage obtained when the thyristor switch is closed. The capacitor voltage is 17.67 kVrms when the valve is conducting. At the closing time, the capacitor must be charged at the peak voltage.

$$Uc = 17670 \times \sqrt{2} = 24989 V$$

You can now use the Powergui block to change the capacitor initial voltage. Open the **powergui**. In the **Tools** menu, select **Initial Values of State**Variables —> Display or Set Initial Conditions. A list of all the state variables with their default initial values appears. The value of the initial

voltage across the capacitor C (variable Uc_C) should be -0.3141 V. This voltage is not exactly zero because the snubber allows circulation of a small current when both thyristors are blocked. Now select the Uc_C state variable and enter 24989 in the upper right field. Then click the **Apply** button in order to make this change effective.

Start the simulation. As expected the transient component of capacitor voltage and current has disappeared. The voltages obtained with and without initial voltage are compared in this plot.

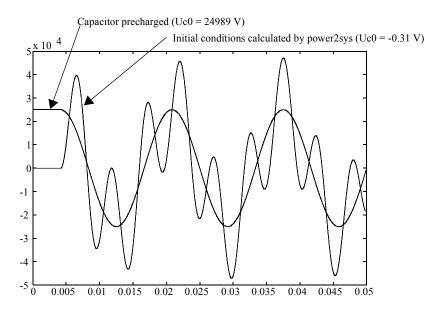


Figure 1-12: Transient Capacitor Voltage With and Without Initial Charge

Session 5: Simulating Motor Drives

In this session you

- Use electrical machines and power electronics to simulate a simple motor drive
- Learn how to use the Universal Bridge block
- Discretize your model and compare variable-step and fixed-step simulation methods
- Learn how to use the Multimeter block

Variable speed control of AC electrical machines makes use of forced-commutated electronic switches such as IGBTs, MOSFETs, and GTOs. Asynchronous machines fed by pulse width modulation (PWM) inverters are now gradually replacing the DC motors and thyristor bridges. With PWM associated with modern control techniques such as field-oriented control or direct torque control, it is now possible to obtain the same flexibility in speed and torque control as with DC machines. In this session you build a simple open loop DC drive controlling an asynchronous machine. A more elaborate example of a PWM drive is presented in the Case Studies chapter. The Power System Blockset circuit to simulate is shown in Figure 1-13. It uses blocks of the Machines and Power Electronics libraries.

The Machines library contains four of the most commonly used three-phase machines: simplified and complete synchronous machines, asynchronous machine, and permanent magnet synchronous machine. Each machine can be used either in generator or motor mode. Combined with linear and nonlinear elements such as transformers, lines, loads, breakers, etc., they can be used to simulate electromechanical transients in an electrical network. They can also be combined with power electronic devices to simulate drives.

The Power Electronics library contains blocks allowing you to simulate diodes, thyristors, GTO thyristors, MOSFETs, and IGBT devices. You could interconnect several blocks together to build a three-phase bridge. For example, an IGBT inverter bridge would require six IGBTs and six antiparallel diodes.

In order to facilitate implementation of bridges, the Universal Bridge block automatically performs these interconnections for you.

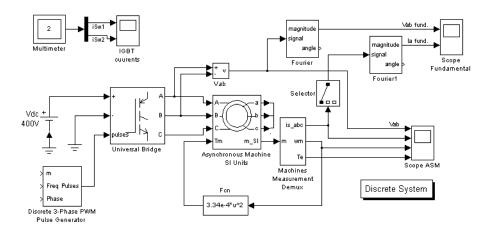


Figure 1-13: Circuit 5: PWM Control of an Induction Motor

Building and Simulating the PWM Motor Drive

Assembling and Configuring the Motor Blocks

In the first steps, you copy and set up the motor blocks.

- 1 Open a new window and save it as circuit5.
- 2 Open the Power Electronics library and copy the Universal Bridge block into your circuit5 model.
- **3** Open the Universal Bridge dialog and set its parameters as follows:

Power Electronic device = IGBT/Diodes; Port configuration= ABC as output terminals; Snubber Rs=1e5 W Cs=inf; Ron=1e-3 Ω ; Tail: Tf=1e-6s; Tt=1e-6 s

Notice that the snubber circuit is integral to the Universal Bridge dialog box. As the Cs capacitor value of the snubber is set to Inf (short-circuit), we are using a purely resistive snubber. Generally, IGBT bridges do not use snubbers; however, because each nonlinear element in the Power System Blockset is modeled as a current source, you have to provide a parallel path

across each IGBT in order to allow connection to an inductive circuit (stator of the asynchronous machine). The high resistance value of the snubber does not affect the circuit performance.

- **4** Open the Machines library. Copy the Asynchronous Machine SI Units block as well as the Machines Measurement Demux block into your circuit5 model.
- **5** Open the Asynchronous Machine dialog and look at its parameters. The parameters are set for a 3 HP, 220 V, 60 Hz, two pairs of poles machine. Its nominal speed is therefore slightly lower than the synchronous speed of 1800 rpm, or $w_s = 188.5 \text{ rad/s}$.
- **6** Notice that the three rotor terminals a, b, and c are accessible. During normal motor operation these terminals should be short-circuited together. Open the Connectors library. Copy the vertical Bus Bar block with two inputs and one output into your circuit5 model.
- **7** Open the Bus Bar dialog and change the number of inputs to three and the number of outputs to zero. Resize the block vertically and connect its three inputs to the three rotor terminals as shown in Figure 1-13.
- 8 Open the Machines Measurement Demux block menu. When this block is connected at the ASM measurement output, it allows you to access specific internal signals of the ASM. Clear all signals except the following three signals: is_abc (three stator currents), wm (rotor speed), and Te (electromagnetic torque).

Loading and Driving the Motor

You now implement the torque-speed characteristic of the motor load. Assume a quadratic torque-speed characteristic (fan or pump type load). The torque T is then proportional to the square of the speed ω .

$$T = k \times \omega^2$$

The nominal torque of the motor is

$$T_n = \frac{3 \times 746}{188.5} = 11.87Nm$$

Therefore, the constant k should be

$$k = \frac{T_n}{\omega_s^2} = \frac{11.87}{188.5^2} = 3.34 \times 10^{-4}$$

Open the Functions & Tables library of Simulink and copy the Fcn block into your circuit5 model. Open the block menu and enter the expression of torque as a function of speed.

- 2 Connect the input of the Simulink Fcn block to the speed output of the Machines Measurement Demux block labeled wm and its output to the torque input of the motor labeled Tm.
- **3** Open the Electrical Sources library and copy the DC Voltage Source block into your circuit5 model. Open the block menu and set the voltage to 400 V.
- **4** Open the Measurements library and copy a Voltage Measurement block into your circuit5 model. Change the block name to Vab.
- **5** Using ground blocks of the Connectors library, complete the power elements and voltage sensor interconnections as shown in Figure 1-13.

Controlling the Inverter Bridge with a Pulse Generator

In order to control your inverter bridge, you need a pulse generator. Such a generator is available in the Extras library of **powerlib**.

- 1 Open the Extras/Discrete Control blocks library and copy the Discrete 3-Phase PWM Generator block into your circuit5 model. Connect its Pulses output to the Pulses input of the Universal Bridge block.
- **2** Open the Discrete 3-Phase PWM Pulse Generator block dialog and set the parameters as follows.

Generator Mode = 3-arm bridge (6pulses); Carrier frequency = 1080 Hz; Sample time = 10e-6 s; Internal generation of modulating signal = checked; Modulation index m = 0.9; Frequency of output voltage = 60 Hz; Phase of output voltage = 0 degrees

3 Use the **Edit** —> **Look Under Mask** menu item of your model window to see how the PWM is implemented. This control system is made entirely with Simulink blocks. The block has been discretized so that the pulses will change at multiples of the specified time step. A time step of $10 \,\mu s$ corresponds to +/- 0.54% of the switching period at $1080 \,Hz$.

One common method of generating the PWM pulses uses comparison of the output voltage to synthesize (60 Hz in our case) with a triangular wave at the switching frequency (1080 Hz in our case). This is the method that is implemented in the Discrete 3-Phase PWM Pulse Generator block. The line-to-line rms output voltage is a function of the DC input voltage and of the modulation index m as given by the following equation:

$$V_{LLrms} = \frac{m}{2} \times \frac{\sqrt{3}}{\sqrt{2}} Vdc = m \times 0.612 \times VDC$$

Therefore, a DC voltage of 400 V and a modulation factor of 0.90 yield the 220 Vrms output line-to-line voltage, which is the nominal voltage of the asynchronous motor.

4 You now add blocks measuring the fundamental component (60 Hz) embedded in the chopped Vab voltage and in the phase A current. Open the Extras/Measurements library of **powerlib** and copy the Fourier block into your circuit5 model.

Open the Fourier block dialog and check that the parameters are set as follows:

Fundamental frequency f1= 60 Hz; Harmonic number= 1;

Connect this block to the output of the Vab voltage sensor.

5 Duplicate the Fourier block. In order to measure the phase A current, you need to select the first element of the is_abc output of the ASM Measurement Demux block.

Copy a Selector block from the Signals & Systems library of Simulink.

Open its menu and set **Element** to 1. Connect the Selector output to the second Fourier block and its input to the is_abc output of the Machines Measurement Demux block as shown in Figure 1-13.

6 Finally, add scopes to your model. Copy one Scope block into your circuit. This scope is used to display the instantaneous motor voltage, currents, speed, and electromagnetic torque. In the **Scope Properties** —> **General** menu of the scope, set the following parameters:

Number of axes=4; Time range =0.05 s; Tick labels: bottom axis only

Connect the four inputs and label the four connection lines as shown in Figure 1-13. When you start the simulation, these labels are displayed on top of each trace.

7 Duplicate the four-input Scope and change its number of inputs to 2. This scope is used to display the fundamental component of Vab voltage and Ia current. Connect the two inputs to the outputs of the Fourier blocks. Label the two connection lines as shown in Figure 1-13.

You are now ready to simulate the motor starting.

Simulating the PWM Motor Drive with Continuous Integration Algorithm

Open the **Simulation** —> **Simulation parameters** menu. Select the ode23tb integration algorithm. Set the relative tolerance to 1e-4, the absolute tolerance and the Max step size to auto, and the stop time to 1 s. Start the simulation. The simulation results are shown in Figure 1-14.

The motor starts and reaches its steady-state speed of 181 rad/s (1728 rpm) after $0.5~\rm s$. At starting, the magnitude of the 60 Hz current reaches 90 A peak (64 A rms) whereas its steady-state value is $10.5~\rm A$ (7.4 A rms). As expected, the magnitude of the 60 Hz voltage contained in the chopped wave stays at

$$220 \times \sqrt{2} = 311V$$

Also notice strong oscillations of the electromagnetic torque at starting. If you zoom in on the torque in steady state, you should observe a noisy signal with a mean value of 11.9 N.m, corresponding to the load torque at nominal speed.

If you zoom in on the three motor currents, you can see that all the harmonics (multiples of the 1080 Hz switching frequency) are filtered by the stator inductance, so that the 60 Hz component is dominant.

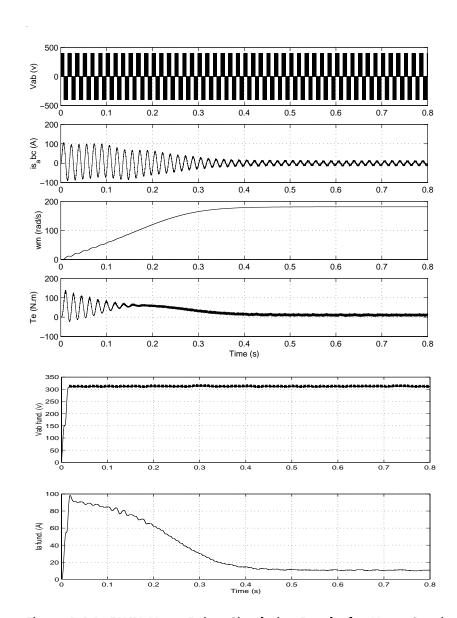


Figure 1-14: PWM Motor Drive; Simulation Results for Motor Starting at Full Voltage

Using the Multimeter Block

You probably have noticed that the Universal Bridge block is not a conventional subsystem where all the six individual switches are accessible. If you want to measure the switch voltages and currents you must use the Multimeter block, which gives access to the bridge internal signals.

- 1 Open the Universal Bridge dialog and set the **Measurement** parameter to Device currents.
- **2** Copy the Multimeter block from the Measurements library into your circuit5 circuit. Double-click the Multimeter block. A window showing the six switch currents appears.
- **3** Select the two currents of the bridge arm connected to phase A. They are identified as
 - iSw1: Universal BridgeiSw2: Universal Bridge
- **4** Click **OK**. The number of signals (2) is displayed in the multimeter icon.
- **5** Using a Demux block, send the two multimeter output signals to a two-trace scope and label the two connection lines as shown in Figure 1-13 (Trace 1: iSw1 Trace 2: iSw2).
- **6** Restart the simulation. The waveforms obtained for the first 20 ms are shown in this plot.

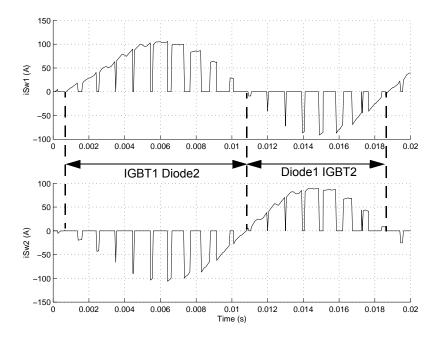


Figure 1-15: Currents in IGBT/Diode Switches 1 and 2

As expected, the currents in switches 1 and 2 are complementary. A positive current indicates a current flowing in the IGBT, whereas a negative current indicates a current in the antiparallel diode.

Note Multimeter block use is not restrained to the Universal Bridge block. All the elements of the Electrical Sources and Elements libraries have a Measurement parameter where you can select voltages, currents, and saturable transformer fluxes. A judicious use of the Multimeter block reduces the number of current and voltage sensors in your circuit, making it easier to follow.

Discretizing the PWM Motor Drive

You probably have noticed that the simulation using a variable-step integration algorithm is relatively long. Depending on your computer, it might take some minutes to simulate one second. In order to shorten the simulation time, you can discretize your circuit and simulate at fixed simulation time steps.

Copy the Discrete System block of the **powerlib** library into your circuit5 system. Open it and set the sample time to 10e-6 s. When you restart the simulation, the power system, including the asynchronous machine, will be discretized at a $10~\mu s$ sample time.

As there are no more continuous states in the electrical system, you do not need a variable-step integration method to solve this system. In the **Simulation** —> **Simulation parameters** —> **Solver** dialog pane, you could select the Fixed-step and discrete (no continuous states) options. However, as your system contains two blocks that contain continuous states (Fourier blocks), you still need continuous integration. In the **Simulation**—> **Simulation Parameters** —> **Solver** dialog pane, select the Fixed-step and ode1 (Euler) options and specify a fixed step of 10 µs.

Start the simulation. Observe that the simulation is now approximately three times faster than with the continuous system. Results compare well with the continuous system.

Session 6: Introducing the Phasor Simulation Method

In this session you will

- Apply the phasor simulation method to a simple linear circuit
- Learn advantages and limitations of this method

Up to now you have used two methods to simulate electrical circuits:

- Simulation at variable time steps using the continuous Simulink solvers
- Simulation at fixed time steps using a discretized system

This section explains how to use a third simulation method: the phasor solution method. This technique is newly introduced in Version 2.3.

When to Use the Phasor Solution

The phasor solution method is mainly used to study electromechanical oscillations of power systems consisting of large generators and motors. An example of application of this method is presented in the next session to simulate a multimachine system. However, this technique is not restricted to the study of transient stability of machines. It can be applied to any linear system.

If, in a linear circuit, you are interested only in the changes in magnitude and phase of all voltages and currents when switches are closed or opened, you do not need to solve all differential equations (state-space model) resulting from the interaction of R, L, and C elements. You can instead solve a much simpler set of algebraic equations relating the voltage and current phasors. This is what the phasor solution method does. As its name implies, this method computes voltages and currents as phasors. Phasors are complex numbers representing sinusoidal voltages and currents at a particular frequency. They can be expressed either in Cartesian coordinates (real and imaginary) or in polar coordinates (amplitude and phase). As the electrical states are ignored, the phasor solution method does not require a particular solver to solve the electrical part of your system. The simulation is therefore much faster to execute. You must keep in mind however that this faster solution technique gives the solution only at one particular frequency.

Phasor Simulation of a Circuit Transient

You now apply the phasor solution method to a simple linear circuit. Open the Demos library of **powerlib.** Open the Simple Demos library and select the demo named "Transient Analysis". A system named psbtransient opens.

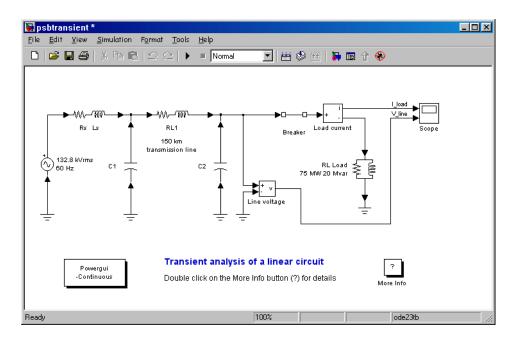


Figure 1-16: Simple Linear Circuit Built in Power System Blockset

This circuit is a simplified model of a 60 Hz, 230 kV three-phase power system where only one phase is represented. The equivalent source is modeled by a voltage source (230 kV rms / sqrt(3) or 132.8 kV rms, 60 Hz) in series with its internal impedance (Rs Ls). The source feeds a RL load through a 150 km transmission line modeled by a single PI section (RL1 branch and two shunt capacitances, C1 and C2). A circuit breaker is used to switch the load (75 MW, 20 Mvar) at the receiving end of the transmission line. Two measurement blocks are used to monitor the load voltage and current.

The Powergui block at the lower right corner indicates that the model is continuous. Start the simulation and observe transients in voltage and current

waveforms when the load is first switched off at $t=0.0333\ s$ (2 cycles) and switched on again at $t=0.1167\ s$ (7 cycles).

Invoking the Phasor Solution in the Powergui Block

You now simulate the same circuit using the phasor simulation method. This option is accessible through the Powergui block. Open this block and select **Phasor simulation.** You must also specify the frequency used to solve the algebraic network equations. A default value of 60 Hz should already be entered in the **Frequency** menu. Close the Powergui and notice that the word Phasors now appears on the Powergui icon, indicating that the Powergui now applies this method to simulate your circuit.

Restart the simulation. The magnitudes of the 60 Hz voltage and current are now displayed on the scope. Waveforms obtained from the continuous simulation and the phasor simulation are superimposed in this plot.

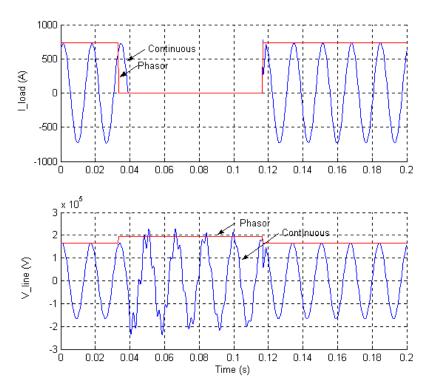


Figure 1-17: Waveforms Obtained with the Continuous and Phasor Simulation Methods

Note that with continuous simulation, the opening of circuit breaker occurs at the next zero crossing of current following the opening order; whereas for the phasor simulation, this opening is instantaneous. This is because there is no concept of zero crossing in the phasor simulation.

Selecting Phasor Signal Measurement Formats

If you now double-click the voltage measurement block or the current measurement block, you see that a menu allows you to output phasor signals in four different formats: Complex, Real-Imag, Magnitude-Angle, or just Magnitude (default choice). If you select Magnitude-Angle, both magnitude and angle (in degrees) are multiplexed at the output of the measurement block. You might need to demultiplex these two signals to send them on separate traces of

the scope. Note that the oscilloscope does not accept complex signals. You should instead use the Real-Imag format.

The Complex format allows the use of complex operations and processing of phasors without separating real and imaginary parts. Suppose for example that you need to compute the power consumption of the load (active power P and reactive power Q). The complex power \overline{S} is obtained from the voltage and current phasors as

$$S = P + jQ = \frac{1}{2} \cdot \overline{V} \cdot \overline{I}^*$$

where \overline{I}^* is the conjugate of the current phasor. The 1/2 factor is required to convert magnitudes of voltage and current from peak values to rms values.

Select the Complex format for both current and voltage and, using blocks from the Simulink Math library, implement the power measurement as shown.

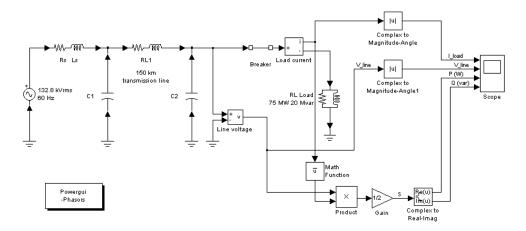


Figure 1-18: Power Computation Using Complex Voltage and Current

The Complex to Magnitude blocks are now required to convert complex phasors to magnitudes before sending them to the scope.

The power computation system you just implemented is already built in the Power System Blockset: the Active and Reactive Power (Phasor Type) block is available in the Extras library under the Phasor collection of blocks.

Session 7: Three-Phase Systems and Machines

In this session you

- Learn how to use electrical machines
- Use the three-phase library and three-phase transformers
- Initialize machines to start simulation in steady state and use the **Machine**Load Flow option of the powergui

You now use three types of machines of the Electrical Machines library: simplified synchronous machine, detailed synchronous machine, and asynchronous machine. These machines will be interconnected with linear and nonlinear elements such as transformers, loads, and breakers to study transient stability of a diesel generator uninterruptible power supply.

Three-Phase Network with Electrical Machines

During this session you simulate the three-machines system shown in this single line diagram.

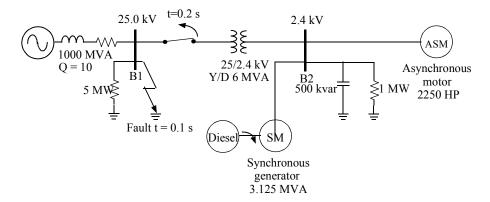


Figure 1-19: Diesel Generator and Asynchronous Motor on Distribution Network

This system consists of a plant (bus B2), simulated by a resistive and motor load (ASM) fed at 2400 V from a distribution 25 kV network through a 6 MVA, 25/2.4 kV transformer, and from an emergency synchronous generator/diesel engine unit (SM).

A 500 kvar capacitor bank is used for power factor correction at the $2.4~\rm kV$ bus. The 25 kV network is modeled by a simple R-L equivalent source (short-circuit level 1000 MVA, quality factor X/R = 10) and a 5 MW load. The asynchronous motor is rated 2250 HP, $2.4~\rm kV$, and the synchronous machine is rated $3.125~\rm MVA$, $2.4~\rm kV$.

Initially, the motor develops a mechanical power of 2000 HP and the diesel generator delivers $500 \, \mathrm{kW}$ of active power. The synchronous machine controls the 2400V bus B2 voltage at 1.0 p.u. and generates $500 \, \mathrm{kW}$ of active power. At $t = 0.1 \, \mathrm{s}$, a three-phase to ground fault occurs on the $25 \, \mathrm{kV}$ system, causing the opening of the $25 \, \mathrm{kV}$ circuit breaker at $t = 0.2 \, \mathrm{s}$, and a sudden increase of the generator loading. During the transient period following the fault and islanding of the Motor/Generator system, the synchronous machine excitation system and the diesel speed governor react to maintain the voltage and speed at a constant value.

This system has already been built in the Power System Blockset. Open the Demos library of **powerlib** and double-click the demo called Three-Phase Machines and Load Flow. A system named psbmachines opens.

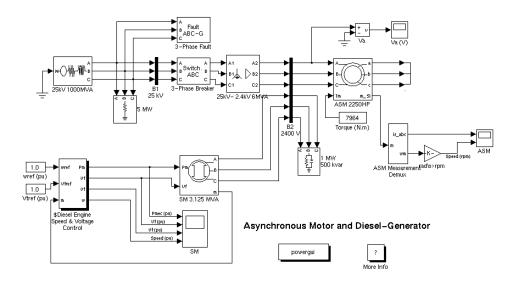


Figure 1-20: Power System of Figure 1-19 Built with the Power System Blockset

The Synchronous Machine (SM) block is using standard parameters, whereas the Asynchronous machine (ASM) block is using S.I. parameters.

The other three-phase elements such as the inductive voltage source, the Y grounded/Delta transformer, and the loads are masked blocks built with standard single-phase Power System Blockset blocks. They are available in the Extra/Three-Phase library of **powerlib**. The 3-Phase Fault and the 3-Phase Breaker blocks are also available in the same library. If you open their dialog boxes, you see how the switching times are specified. Special measurement blocks provided in the Machines library are used to demultiplex the SM and ASM machine outputs.

The SM voltage and speed outputs are used as feedback inputs to a Simulink control system that contains the diesel engine and governor block as well as an excitation block. The excitation system is the standard block provided in the Machines library. The SM parameters as well as the diesel engine and governor models were taken from reference [1].

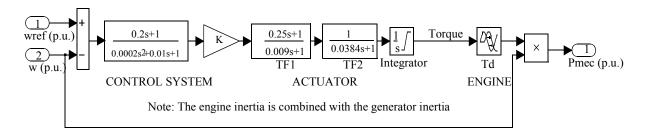


Figure 1-21: Diesel Engine and Governor System

If you simulate this system for the first time, you normally do not know what the initial conditions are for the SM and ASM to start in steady state.

These initial conditions are

- SM block: Initial values of speed deviation (usually 0%), rotor angle, magnitudes and phases of currents in stator windings, and initial field voltage required to obtain the desired terminal voltage under the specified load flow.
- ASM block: Initial values of slip, rotor angle, magnitudes and phases of currents in stator windings.

Open the dialog box of the Synchronous Machine and Asynchronous Machine blocks. All initial conditions should be set at 0, except for the initial SM field voltage and ASM slip, which are set at 1 p.u. Open the three scopes monitoring the SM and ASM speeds as well as the ASM stator currents. Start the simulation and observe the first 100 ms before fault is applied.

As the simulation starts, you will notice that the three ASM currents start from 0 and contain a slowly decaying DC component. The machine speeds take a much longer time to stabilize because of the inertia of the motor/load and diesel/generator systems. In our example, the ASM even starts to rotate in the wrong direction because the motor starting torque is lower than the applied load torque. Stop the simulation.

Load Flow and Machine Initialization

In order to start the simulation in steady state with sinusoidal currents and constant speeds, all the machine states must be initialized properly. This is a difficult task to perform manually, even for a simple system. In the next section you learn how to use the **Load Flow** option of the **powergui** to perform a load flow and initialize the machines.

Double-click the **powergui.** In the **Tools** menu, select the **Load Flow and Machine Initialization** button. A new window appears. In the upper right window you have a list of the machines appearing in your system. Select the SM 3.125 MVA machine. Note that for the **Bus Type**, you have a menu allowing you to choose either PV Generator or Swing Generator.

For synchronous machines you normally specify the desired terminal voltage and the active power that you want to generate (positive power for generator mode) or absorb (negative power for motor mode). This is possible as long as you have a *swing* (or *slack*) bus that generates or absorbs the excess power required to balance the active powers throughout the network.

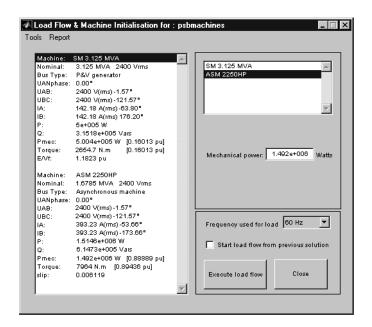
The swing bus can be either a voltage source or any other synchronous machine. If you do not have any voltage source in your system, you must declare one of the machines as a swing machine. In the next section you will make a load flow with the $25~\rm kV$ voltage source connected to bus B1 used as a swing bus.

Load Flow Without a Swing Machine

In the **Load Flow** window, your **SM Bus Type** should be already initialized as PV generator indicating that the load flow is performed with the machine controlling its active power and terminal voltage. By default, the desired Terminal Voltage is initialized at the nominal machine voltage (2400 Vrms). Keep it unchanged and set the Active Power to 500e3 (500 kW). Now select the ASM 2250 HP machine in the upper right window. The only parameter that is needed is the **Mechanical power** developed by the motor. Enter 2000*746 (2000 HP). You now perform the load flow with the following parameters.

```
SM:Terminal voltage = 2400 Vrms; Active Power = 500kW ASM:Mechanical Power = 2000*746 W (2000 HP)
```

Select the **Execute load flow** button. Once the load flow is solved, the phasors of AB and BC machine voltages as well as currents flowing in phases A and B are updated, as shown in the next figure.



The SM active and reactive powers, mechanical power, and field voltage are displayed.

```
SM:P=500 kW; Q =315 kvar;
```

```
Pmec=500.4 kW (or 500/3125=0.1601 pu)
Field voltage Ef=1.182 pu
```

The ASM active and reactive powers absorbed by the motor, slip, and torque are also displayed.

```
ASM:P=1.515MW; Q =615 kvar; Pmec=1.492 MW (2000 HP) Slip=0.006119; Torque=7964 N.m
```

Close the **Load Flow** window.

The ASM torque value (7964 N.m) should be already entered in the Constant block connected at the ASM torque input. If you now open the **SM** and **ASM** dialog boxes you can see the updated initial conditions. If you open the **powergui**, you can see updated values of the measurement outputs. You can also click the **Nonlinear** button to obtain voltages and currents of the nonlinear blocks. For example, you should find that the magnitude of the Phase A voltage across the fault breaker (named Uc_3phase fault/Breaker1) is 20.40 kV, corresponding to a 24.985 kV rms phase-phase voltage.

In order to start the simulation in steady state, the states of the Governor & Diesel Engine and the Excitation blocks should also be initialized according to the values calculated by the load flow. Open the Governor & Diesel Engine subsystem, which is inside the Diesel Engine Speed and Voltage Control subsystem. The initial mechanical power has already been set to 0.1601 p.u. Open the Excitation block and notice that the initial terminal voltage and field voltage have been set respectively to 1.0 and 1.182 p.u.

Note that the load flow automatically initializes the machine blocks but not the associated control blocks. Therefore, if you perform a new load flow, you should change the initial values in the control blocks.

Open the four scopes displaying the terminal voltage, field voltage, mechanical power, and speed of the synchronous machine, as well as the scope displaying the asynchronous motor speed. Start the simulation. The simulation results are shown in the following figure.

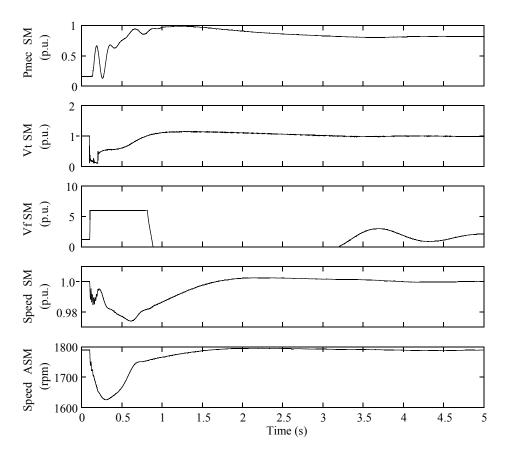


Figure 1-22: Simulation Results

Observe that during the fault the terminal voltage drops to about 0.2 p.u. and the excitation voltage hits the limit of 6 p.u. After fault clearing and islanding, the SM mechanical power quickly increases from its initial value of 0.16 p.u. to 1 p.u. and stabilizes at the final value of 0.80 p.u. required by the resistive and motor load (1.0 MW resistive load + 1.51 MW motor load = 2.51 MW = 2.51/3.125 = 0.80 p.u.). After 3 seconds the terminal voltage stabilizes close to its reference value of 1.0 p.u. The motor speed temporarily decreases from 1789 rpm down to 1625 rpm, then recovers close to its normal value after 2 seconds.

If you increase the fault duration to 12 cycles by changing the breaker opening time to 0.3 s, you will notice that the system collapses. The ASM speed slows down to zero after 2 seconds.

Load Flow with a Swing Machine

In this section you make a load flow with two machine types: a *PV generator* and a *Swing generator*. In your **psbmachines** window, delete the inductive source and replace it with the Simplified Synchronous Machine block in p.u. that you find in the Machines library. Rename it SSM 1000MVA and save this new system in your working directory as psbmachine2. Open the **SSM** 1000MVA dialog box and enter the following parameters.

First line: 3 wires Y

Second line: Vn(V), Pn(VA) fn(Hz): [1000e6 25e3 60]

Third line: H(s) Kd() p () [inf 0 2]

As you specify an infinite inertia, the speed and therefore the frequency of the machine are kept constant.

Fourth line: R(p.u.) X(p.u.): [0.1 1.0]

(Notice how easily you can specify an inductive short circuit level of 1000 MVA and a quality factor of 10 with the per unit system.)

Fifth line: Leave all initial conditions at 0.

When there is no voltage source imposing a reference angle for voltages, you must choose one of the synchronous machines as a reference. In a load flow program, this reference is called the *swing bus*. The swing bus absorbs or generates the power needed to balance the active power generated by the other machines and the power dissipated in loads as well as losses in all elements.

Open the **powergui**. In the **Tools** menu, select **Load Flow and Machine Initialization**. Leave the SM **Bus Type** as PV Generator and change the SSM **Bus Type** to Swing Generator. Specify the load flow by entering the following parameters:

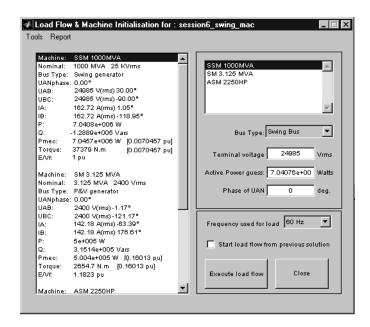
```
SM: Terminal Voltage =2400 Vrms; Active Power =500e3 W; ASM: Mechanical power= 2000*746 W (2000 HP)
```

For the SSM swing machine you only have to specify the requested terminal voltage (magnitude and phase). The active power is unknown. However, you

can specify an active power that is used as an initial guess and help load flow convergence. Specify the following parameters:

```
SSM: Terminal Voltage = 24985 Vrms (Voltage obtained at bus B1 from the previous load flow); Phase of UAN voltage =0 degrees; Active Power = 0 W
```

Click the **Execute load Flow** button. Once the load flow is solved the following solution is displayed. Use the scroll bar of the left window to look at the solution for each of the three machines.



The active and reactive electrical powers, mechanical power, and field voltage are displayed for the SSM block.

```
P=500 kW; Q=315 kvar;
Pmec=500.4 MW (or 500/3125=0.1601 pu);
Field voltage Ef=1.182 pu
```

The active and reactive electrical powers, mechanical power, and internal voltage of the SM block are

```
P=7.041MW;Q=-129 kvar;
```

```
Pmec=7.046MW (or 7.046/1000=0.007046 pu); E=1.0 pu
```

The active and reactive powers absorbed by the motor, slip, and torque of the ASM block are also displayed.

```
P=1.515MW; Q=615 kvar; Pmec=1.492 MW (2000 HP) Slip=0.006119; Torque=7964 N.m
```

As expected, the solution obtained is exactly the same as the one obtained with the R-L voltage source. The active power delivered by the swing bus is 7.04 MW (6.0 MW resistive load + 1.51 MW load - 0.5 MW generated by SM = 7.01 MW, the difference (0.03 MW) corresponding to losses in the transformer).

Connect at inputs 1 and 2 of the SSM block two Constant blocks specifying respectively the required mechanical power (0.007046 p.u.) and its internal voltage (1.0 p.u.). Restart the simulation. You should get the same waveforms as those of Figure 1-22.

Reference

[1] Yeager, K.E., and J.R.Willis, "Modeling of Emergency Diesel Generators in an 800 Megawatt Nuclear Power Plant," *IEEE Transactions on Energy Conversion*, Vol. 8, No. 3, September, 1993.

Session 8: Building and Customizing Nonlinear Models

The Power System Blockset provides a wide collection of nonlinear models. It can happen, however, that you need to interface your own nonlinear model with the standard models provided in the **powerlib** library. This model could be a simple nonlinear resistance simulating an arc or a varistor, a saturable inductor, a new type of motor, etc.

In the following section you learn how to build such a nonlinear model. We will use as examples a simple saturable inductance and a nonlinear resistance.

Modeling a Nonlinear Inductance

Consider an inductor of 2 henries designed to operate at a nominal voltage, Vnom = 120 V rms, and a nominal frequency, fnom = 60 Hz. From zero to 120 V rms the inductor has a constant inductance, L=2 H. When voltage exceeds its nominal voltage, the inductor saturates and its inductance is reduced to Lsat = 0.5 H. The nonlinear flux-current characteristic is plotted in the next figure. Flux and current scales are in per units. The nominal voltage and nominal current are chosen as base values for the per-unit system.

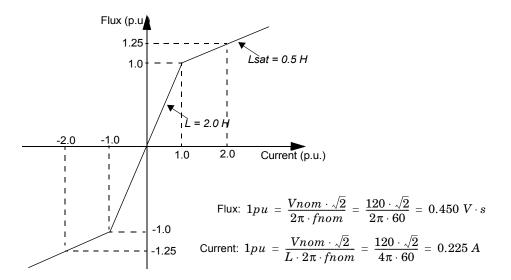


Figure 1-23: Flux-Current Characteristic of the Nonlinear Inductance

The current i flowing in the inductor is a nonlinear function of flux linkage ψ that, in turn, is a function of v appearing across its terminals. These relations are given by the following equations:

$$v=L\cdot rac{di}{dt}=rac{d\psi}{dt}$$
 or $\psi=\int v\cdot dt$ where
$$i=rac{\psi}{L(\psi)}$$

The model of the nonlinear inductance can therefore be implemented as a controlled current source, where current i is a nonlinear function of voltage v, as shown.

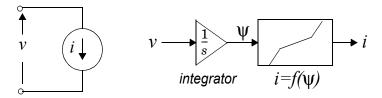


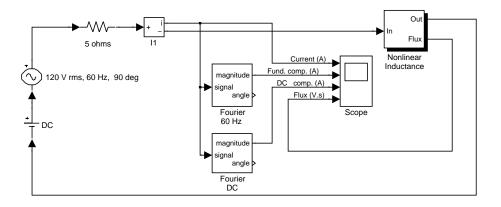
Figure 1-24: Model of a Nonlinear Inductance

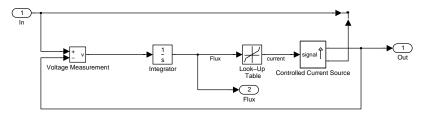
Figure 1-25 shows a circuit using a 2 H nonlinear inductance. The nonlinear inductance is connected in series with two voltage sources (an AC Voltage Source block of 120 volts rms, 60 Hz, and a DC Voltage Source block) and a 5 ohm resistor.

All the elements used to build the nonlinear model have been grouped in a subsystem named Nonlinear Inductance. The inductor terminals are labeled In and Out. Notice that a second output returning the flux has been added to the subsystem. This Simulink output can be used to observe the flux by connecting it to a Simulink Scope block.

The nonlinear model uses two **powerlib** blocks and two Simulink blocks. The two **powerlib** blocks are a Voltage Measurement block to read the voltage at the inductance terminals and a Controlled Current Source block. The direction of the arrow of the current source is oriented from input to output according to the model shown in Figure 1-24.

The two Simulink blocks are an Integrator block computing the flux from the voltage input and a Look-Up Table block implementing the saturation characteristic $i = f(\psi)$ described by Figure 1-23.





Nonlinear Inductance subsystem

Figure 1-25: Implementation of a Nonlinear Inductance

Two Fourier blocks from the Measurements library of **powerlib_extras** are used to analyze the fundamental component and the DC component of the current.

Using blocks of the **powerlib** and Simulink libraries, build the circuit of Figure 1-25. To implement the $i = f(\psi)$ relation, specify the following vectors in the Look-Up Table block.

```
Vector of input values (flux): 
 [-1.25 -1 1 1.25 ] *(120*sqrt(2)/(2\pi*60)) 
 Vector of output values (current): 
 [-2 -1 1 2]*(120*sqrt(2)/(4\pi*60)) 
 Save your circuit as circuit7.
```

Set the following parameters for the two sources.

AC source: Peak amplitude = 120*sqrt(2) ; Phase = 90 degrees; Frequency = 60 Hz
DC source: Amplitude = 0 V

Adjust the simulation time to 1.5 s and select the ode33tb integration algorithm with default parameters. Start the simulation.

As expected, the current and the flux are sinusoidal. Their peak values correspond to the nominal values.

$$Peak \cdot Current = rac{120 \cdot \sqrt{2}}{2 \cdot 2\pi \cdot 60} = 0.225 \, A$$

$$Peak \cdot Flux = rac{120 \cdot \sqrt{2}}{2\pi \cdot 60} = 0.450 \, V \cdot s$$

Current and flux waveforms are shown.

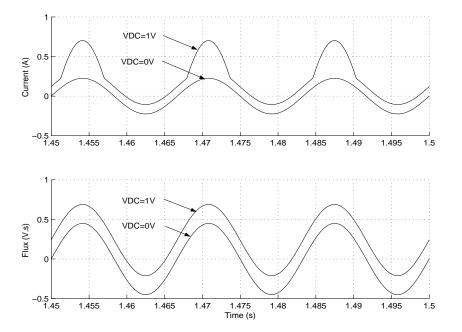


Figure 1-26: Current and Flux Waveforms Obtained with VDC = 0 V and VDC = 1 V

Now change the DC voltage to 1 V and restart the simulation. Observe that the current is distorted. The 1 V DC voltage is now integrated, causing a flux offset, which makes the flux enter into the nonlinear region of the flux-current characteristic (ψ > 0.450 V.s) As a result of this flux saturation, the current contains harmonics. Zoom on the last three cycles of the simulation. The peak value of the current now reaches 0.70 A and the fundamental component has increased to 0.368 A. As expected, the DC component of the current is 1 V/0.5 Ω = 0.2. The current and flux waveforms obtained with and without saturation are superimposed in Figure 1-26.

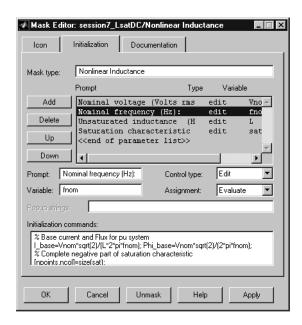
Customizing Your Nonlinear Model

Up to now, you have used a nonlinear model with fixed parameters. If you plan to use this block in other circuits with different parameters (for example, an inductance with a different voltage rating or a saturation characteristic defined with more than two segments), you will find it more convenient to enter the block parameters in a dialog box, rather than modifying individual blocks of your subsystem.

In the following section, you learn how to use the Simulink masking facility to create a dialog box, an icon, and documentation for your model. For more details, refer to the Using Simulink guide.

Block Initialization

Select the Nonlinear Inductance subsystem and in the **Edit** menu, select **Mask subsystem**. The **Mask Editor** window appears.



Select the Initialization tab. In the Mask type field, enter

Nonlinear Inductance

The parameters that you have to specify are the nominal voltage, the inductance in the linear region, and the flux-current characteristic (flux and current vectors in p.u.).

Click **Add**. In the **Prompt** field, enter

Nominal voltage (Volts rms) :

In the **Variable** field, enter the variable name associated with that field.

Vnom

Repeat the preceding steps to define the dialog boxes and associated variables listed below.

```
Nominal frequency (Hz): fnom
```

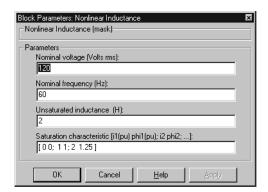
```
Unsaturated inductance (H):
L
Saturation characteristic [i1(pu) phi1(pu); i2 phi2; ...]:
sat
```

In the **Initialization commands** section, enter the following MATLAB commands. This code prepares the two vectors Current_vect and Flux_vect to be used in the Look-Up Table block of the model.

```
% Define base current and Flux for p.u. system
I base=Vnom*sqrt(2)/(L*2\pi*fnom);
Phi base=Vnom*sqrt(2)/(2\pi*fnom);
% Check first two points of the saturation characteristic
if ~all(all(sat(1:2,:)==[0 0; 1 1])),
    h=errordlg('The first two points of the characteristic must
be [0 0; 1 1]', 'Error');
    uiwait(h);
end
% Complete negative part of saturation characteristic
[npoints, ncol] = size(sat);
sat1=[sat ; -sat(2:npoints,:)];
sat1=sort(sat1);
% Current vector (A) and flux vector (V.s)
Current vect=sat1(:,1)*I base;
Flux vect=sat1(:,2)*Phi base;
```

As the saturation characteristic is specified only in the first quadrant, three lines of code are added to complete the negative part of the saturation characteristic. Notice also how the validity of the first segment of the saturation characteristic is verified. This segment must be defined by two points [0 0; 1 1] specifying a 1 p.u. inductance (nominal value) for the first segment.

Click the **OK** button to close the **Mask Editor** window. Double-click the icon of your masked block. Its dialog opens with all fields empty. Enter the values as shown here.



Before you can use the masked block, you must apply the two internal variables defined in the initialization section of the Look-Up Table block. Select your block and, in the **Edit** menu, select **Look under mask**.

The Nonlinear Inductance subsystem opens. Open the Look-Up Table block dialog box and enter the following variable names in the two fields:

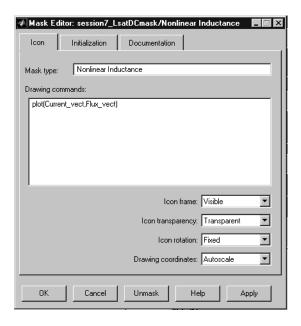
```
Vector of input values (flux): Flux_vect
Vector of output values (current):Current_vect
```

Close the Nonlinear Inductance subsystem and start the simulation. You should get the same waveforms as shown in Figure 1-26.

Block Icon

In this section you learn how to customize your block's icon and make it more attractive.

Select your block and, in the **Edit** menu, select **Edit mask**. The **Mask Editor** window opens. Select the **Icon** tab.



In the **Drawing commands** section, you can specify any drawing that will appear in your block icon by using the plot function. You can, for example, plot the flux-current characteristic of your inductance.

Remember that the currents and fluxes of the nonlinear characteristic are stored respectively in the Current_vect and Flux_vect internal variables of the masked block. Enter the following command in the **Drawing commands** section.

```
plot(Current vect,Flux vect)
```

Click **Apply** and notice that the saturation characteristic is displayed on the icon. Notice also that the input and output names have disappeared.

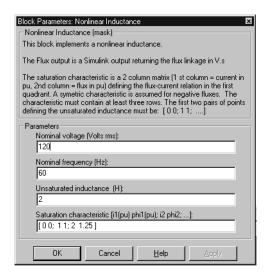
To make them visible, in the **Icon transparency** pop-up menu, select **Transparent**. Click **OK** to close the **Mask Editor** window.

Block Documentation

In this section, you add documentation to your block dialog box. Select your block and, in the **Edit** menu, select **Edit Mask**. The **Mask Editor** window opens.



Select the **Documentation** tab and enter in the **Block description** the text shown in the dialog box of the next figure. Then, click **OK** to close the **Mask Editor** window. The next time you double-click your block, this description appears on the dialog box of the block.



Modeling a Nonlinear Resistance

The technique for modeling a nonlinear resistance is similar to the one used for the nonlinear inductance.

We use as an example a metal-oxide varistor (MOV) having the following V-I characteristic defined by the equation

$$i = I_o \cdot \left(\frac{v}{V_o}\right)^{\alpha}$$

where

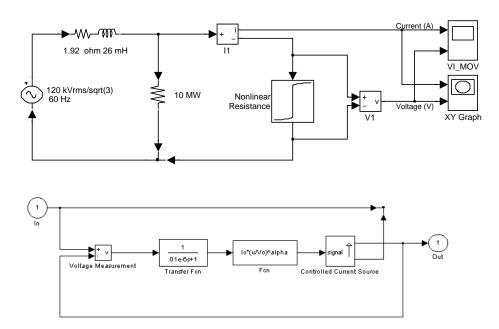
v, i = Instantaneous voltage and current

Vo = Protection voltage

Io = Reference current used to specify the protection voltage

 α = Exponent defining the nonlinear characteristic (typically between 10 and 50)

The following figure shows an application of such a nonlinear resistance to simulate a MOV used to protect equipment on a 120 kV network. In order to keep the circuit simple, only one phase of the circuit is represented.



Nonlinear Resistance subsystem

Figure 1-27: Nonlinear Resistance Applied on a 120 kV Network

Using blocks of the **powerlib** and Simulink libraries, build this circuit. Group all components used to model the nonlinear model in a subsystem named Nonlinear Resistance. Use an X-Y Graph block to plot the V-I characteristic of the Nonlinear Resistance subsystem.

Notice that the model does not use a Look-Up Table block as in the case of the nonlinear inductance model. As the analytical expression of current as function of voltage is known, the nonlinear I(V) characteristic is implemented directly with a Fcn block from the Fcn & Tables library of Simulink.

This purely resistive model contains no states. It produces an algebraic loop in the state-space representation of the circuit, as shown in the next figure. See Chapter 4, "Power System Block Reference," for more details on how the Power System Blockset works.

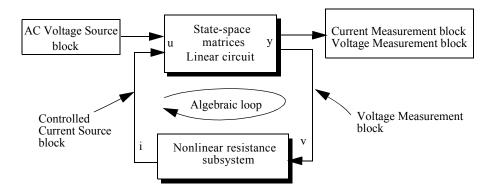


Figure 1-28: Algebraic Loop Introduced by the Nonlinear Resistance Model

Although Simulink is able to solve algebraic loops, this could result in slow simulation times. It is therefore recommended that you break the loop with a block that will not change the nonlinear characteristic. We have introduced a first-order transfer function H(s) = 1/(1+Ts) in the system, using a fast time constant (T = 0.01 μ s).

Use the technique explained for the nonlinear inductance block to mask and customize your nonlinear resistance block as shown.

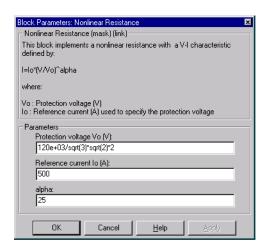


Figure 1-29: Dialog Box of the Nonlinear Resistance Block

Open the dialog box of your new masked block and enter the parameters shown in Figure 1-29. Notice that the protection voltage Vo is set at 2 p.u. of the nominal system voltage. Adjust the source voltage at 2.3 p.u. by entering the following peak amplitude:

120e3/sqrt(3)*sqrt(2)*2.3

Save your circuit as circuit8.

Using the ode23tb integration algorithm, simulate your circuit8 system for 0.1 s. The results are shown below.

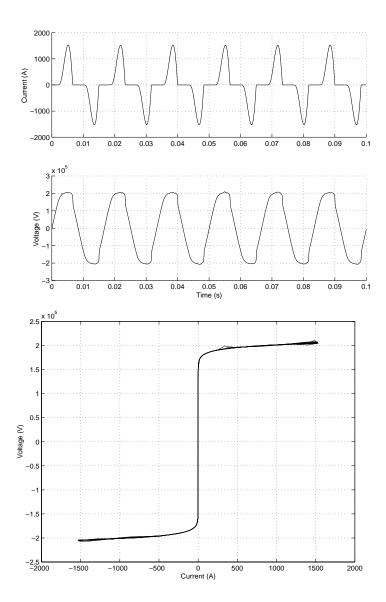


Figure 1-30: Current and Voltage Waveforms and V-I Characteristic Plotted by the X-Y Graph Block

Creating Your Own Library

Simulink gives you the possibility of creating your own library of Power System Blockset blocks. To create a library, in the **File** menu choose **New Library**. A new Simulink window named **Library:untitled** opens. Now copy the Nonlinear Inductance block of your circuit7 system and the Nonlinear Resistance block of your circuit8 system into that library. Save this library as **my_PSBlibrary**. Next time you develop a new model, you can add it to your personal library. You can also organize your library in different sublibraries according to their functions, as is done in the **powerlib** library.

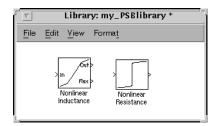


Figure 1-31: Nonlinear Inductance and Resistance Blocks in my_PSBlibrary

One advantage of using a library is that all blocks that you copy from that library are referenced to the library. In other words, if you make a correction in your library block, the correction is automatically applied to all circuits using that block.

Connecting Your Model with Other Nonlinear Blocks

You now learn how to avoid error messages that can appear with nonlinear blocks when they are simulated by a current source. Obviously, a current source cannot be connected in series with an inductor, another current source, or a an open circuit. Such circuit topologies are forbidden in the Power System Blockset.

Similarly, if your nonlinear model uses a Controlled Voltage Source block, this model could not be short-circuited or connected across a capacitor.

Suppose, for example, that you want to study the inrush current in a nonlinear inductance when it is energized on a voltage source. Using blocks from **powerlib** library and **my_PSBlibrary**, you can build the circuit shown here. Change the Breaker block parameters as follows.

External control:not checked Switching times:[1/60]

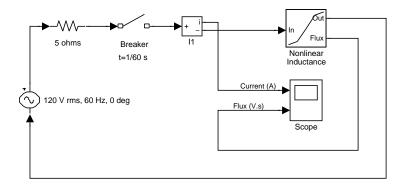


Figure 1-32: Circuit Topology Causing an Error

If you try to simulate this circuit, you get the following error message.



This topology is forbidden because two nonlinear elements simulated by current sources are connected in series: the Breaker block and the Nonlinear Inductance block. To be able to simulate this circuit you must provide a current path around one of the two nonlinear blocks. You could, for example, connect a large resistance, say 1 $M\Omega$, across the Breaker block or the Inductance block.

In our case, it is more convenient to choose the Breaker block because a series RC snubber circuit is provided with the model. Open the Breaker block dialog box and specify the following snubber parameters:

```
Snubber resistance Rs (Ohms) : 1e6
Snubber capacitance Cs (F) : inf
```

Notice that in order to get a purely resistive snubber you have to use an infinite capacitance.

Note Using an inductive source impedance (R-L series) instead of a purely resistive impedance would have produced another error message, because the current source modeling the nonlinear inductance would have been in series with an inductance, even with a resistive snubber connected across the breaker. In such a case, you could add either a parallel resistance across the source impedance or a large shunt resistance connected between one breaker terminal and the source neutral terminal.

Make sure that the phase angle of the voltage source is zero. Use the ode23tb integration algorithm and simulate the circuit for 1 second. Voltage and current waveforms are shown here.

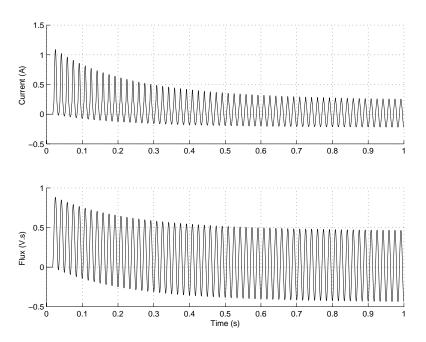


Figure 1-33: Current and Flux Waveforms When Energizing the Nonlinear Inductance with Maximum Flux Offset

Figure 1-33 shows that energizing the inductor at a zero crossing of voltage results in a maximum flux offset and saturation.

Case Studies

These case studies provide detailed, realistic examples of how to use Power System Blockset.

Series-Compensated Transmission Network (p. 2-2)	Study of subsynchronous resonance in AC power transmission
Chopper-Fed DC Motor Drive (p. 2-19)	Study of a DC motor drive with armature voltage controlled by a GTO thyristor chopper
Synchronous Machine and Regulators (p. 2-30)	Study of a nonlinear controller regulating a hydraulic turbine and synchronous alternator connected to an infinite bus
Variable-Frequency Induction Motor Drive (p. 2-38)	Study of a variable-frequency AC motor drive in which a pulse-width-modulated inverter drives an induction motor in variable-voltage, variable-speed operation
HVDC System (p. 2-51)	Study of a high-voltage DC transmission link and perturbations to analyze system response

Series-Compensated Transmission Network

The example described in this section illustrates phenomena related to subsynchronous resonance in a series-compensated AC transmission network.

Description of the Transmission Network

The single diagram shown here represents a three-phase, 60 Hz, 735 kV power system transmitting power from a power plant consisting of six 350 MVA generators to an equivalent network through a 600 km transmission line. The transmission line is split into two 300 km lines connected between buses B1, B2, and B3.

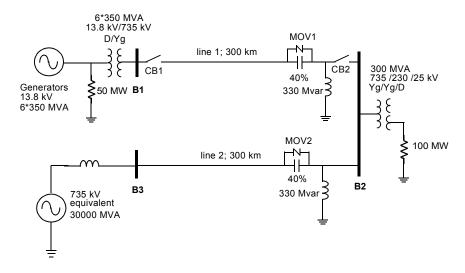


Figure 2-1: Series and Shunt Compensated Network

In order to increase the transmission capacity, each line is series compensated by capacitors representing 40% of the line reactance. Both lines are also shunt compensated by a 330 Mvar shunt reactance. The shunt and series compensation equipment is located at the B2 substation where a 300 MVA-735/230 kV transformer feeds a 230 kV-250 MW load through a 25 kV tertiary winding.

Each series compensation bank is protected by metal-oxide varistors (MOV1 and MOV2). The two circuit breakers of line 1 are shown as CB1 and CB2.

This network is available in the psb3phseriescomp model. Load this model and save it in your working directory as case1 in order to allow further modifications to the original system.

Compare the circuit modeled in Power System Blockset (Figure 2-2) with the schematic diagram of Figure 2-1. The generators are simulated with a Simplified Synchronous Machine block. A Three-Phase Transformer (Two-Windings) block and a Three-Phase Transformer (Three-Windings) block are used to model the two transformers. Saturation is implemented on the transformer connected at bus B2.

B1 and B3 blocks are Three-Phase V-I Measurement blocks taken from the Measurements library. B2 is a similar block that has been modified to accommodate two three-phase inputs and one three-phase output. These blocks are reformatted and given a black background color to give them the appearance of bus bars. They output the three line-to-ground voltages multiplexed on output 4 and the three line currents multiplexed on output 5. Open the dialog boxes of B1 and B2. See how the blocks are programmed to output voltages in p.u. and current in p.u./100 MVA.

The fault is applied on line 1, on the line side of the capacitor bank. Open the dialog boxes of the 3-Phase Fault block and of the 3-Phase breakers CB1 and CB2. See how the initial breaker status and switching times are specified. A line-to-ground fault is applied on phase A at t=1 cycle. The two circuit breakers that are initially closed are then open at t=5 cycles, simulating a fault detection and opening time of 4 cycles. The fault is eliminated at t=6 cycles, one cycle after the line opening.

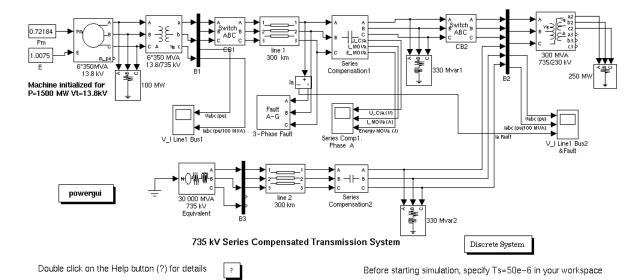


Figure 2-2: Series-Compensated Network (psb3phseriescomp.mdl)

Series Compensation 1 Subsystem

Now, open the Series Compensation1 subsystem of the psb3phseriescomp model. The three-phase module consists of three identical subsystems, one for each phase. A note indicates how the capacitance value and the MOV protection level are calculated. Open the Series Compensation1/ Phase A subsystem. You can see the details of the connections of the series capacitor and the Surge Arrester block (MOV block). The transmission line is 40% series compensated by a 62.8 μF capacitor. The capacitor is protected by the MOV block. If you open the dialog box of the MOV block, you will notice that it consists of 60 columns and that its protection level (specified at a reference current of 500 A/column or 30 kA total) is set at 298.7 kV. This voltage corresponds to 2.5 times the nominal capacitor voltage obtained at a nominal current of 2 kA rms.

A gap is also connected in parallel with the MOV block. The gap is fired when the energy absorbed by the surge arrester exceeds a critical value of 30 MJ. In order to limit the rate of rise of capacitor current when the gap is fired, a damping RL circuit is connected in series. Open the Energy & Gap firing

subsystem. It shows how the energy dissipated in the MOV is calculated by integrating the power (product of the MOV voltage and current).

When the energy exceeds the 30 MJ threshold, a closing order is sent to the breaker block simulating the gap.

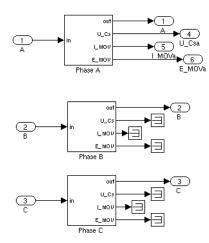
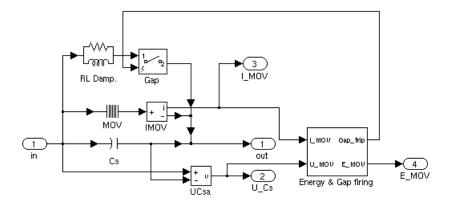
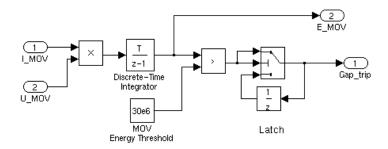


Figure 2-3: Series Compensation Module

Series Compensation 1/Phase A Subsystem:



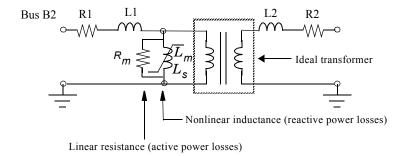
Series Compensation 1/Phase A Subsystem/Energy & Gap firing:



Three-Phase Saturable Transformer Model

Open the 300~MVA~735/230~kV~Transformer dialog box and notice that the current-flux saturation characteristic is set at

These data are the current and flux values at points 1, 2, and 3 of the piecewise linear approximation to the flux linkage curve shown here:



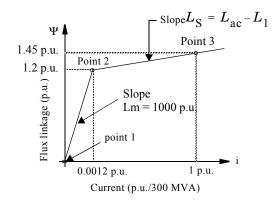


Figure 2-4: Saturable Transformer Model

The flux-current characteristic is approximated by the two segments shown in the graph here. The saturation knee point is 1.2 p.u. The first segment corresponds to the magnetizing characteristic in the linear region (for fluxes below 1.2 p.u.). At 1 p.u. voltage, the inductive magnetizing current is 0.0010/1.0 = 0.001 p.u., corresponding to 0.1% reactive power losses.

The iron core losses (active power losses) are specified by the magnetization resistance Rm = 1000 p.u., corresponding to 0.1% losses at nominal voltage.

The slope of the saturation characteristic in the saturated region is 0.25 p.u. Therefore, taking into account the primary leakage reactance (L1 = 0.15 p.u.), the air core reactance of the transformer seen from the primary winding is 0.4 p.u./300 MVA.

Setting the Initial Load Flow and Obtaining Steady State

Before performing transient tests you must initialize your model for the desired load flow. Use the load flow utility of the **powergui** to obtain an active power flow of 1500 MW out of the machine with a terminal voltage of 1 p.u. (13.8 kV).

Open the **powergui** and select **Load Flow and Machine Initialization**. A new window appears. In the upper right window you have the name of the only machine present in your system. Its **Bus Type** should be **PV Generator** and the desired **Terminal Voltage** should already be set to the nominal voltage of 13800 V. In the **Active Power** field, enter 1500e6 as the desired output power. Click the **Execute load flow** button. Once the load flow is solved, the phasors of AB and BC machine voltages as well as currents flowing in phases A and B are updated in the left window. The required mechanical power to drive the machine is displayed in watts and in p.u., and the required excitation voltage E is displayed in p.u.

```
Pmec :1.5159e9 W [0.72184 p.u.]
E/Vf : 1.0075 p.u.
```

Notice that constant blocks containing these two values are already connected to the Pm and E inputs of the machine block. If you open the machine dialog box, you see that the machine initial conditions (initial speed deviation dw = 0; internal angle theta, current magnitudes, and phase angles) are automatically transferred in the last line.

Once the load flow is performed, you can obtain the corresponding voltage and current measurements at the different buses. In the **powergui** select **Steady State Voltages and Currents**. You can observe, for example, the phasors for phase A voltages at buses B1, B2, and B3 and the current entering line 1 at bus B1.

```
B1/Va : 6.088e5 V ; 18.22 degrees
B2/Va : 6.223e5 V ; 9.26 degrees
B3/Va : 6.064e5 V ; 2.04 degrees
B1/Ia : 1560 A ; 30.50 degrees
```

The active power flow for phase A entering line 1 is therefore

$$P_a = V_a \cdot I_a \cdot \cos(\varphi_a) = \frac{608.8 \text{ kV}}{\sqrt{2}} \cdot \frac{1.56 \text{ kA}}{\sqrt{2}} \cos(30.50 - 18.22) = 464 \text{ MW}$$

corresponding to a total of 464 * 3 = 1392 MW for the three phases.

Transient Performance for a Line Fault

In order to speed up the simulation, you need to discretize the network. The sample time is specified in the Powergui block as a variable Ts. This sample time Ts is also used in the integrator block of the MOV energy calculator controlling the gap.

In the MATLAB window, define the variable

```
Ts = 50e-6;
```

Ensure that the simulation parameters are set as follows:

```
Stop time : 0.2
Solver options Type: Fixed-step; discrete (no continuous state)
Fixed step size: Ts
```

Line-to-Ground Fault Applied on Line 1

Ensure that the fault breaker is programmed for a line-to-ground fault on phase A. Start the simulation and observe the waveforms on the three scopes. These waveforms are shown here:

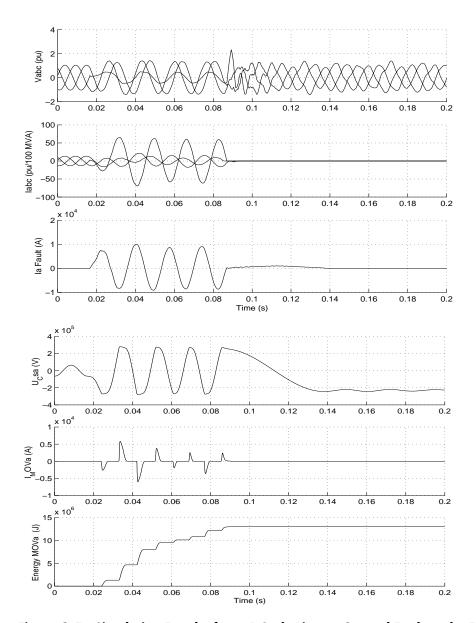


Figure 2-5: Simulation Results for a 4 Cycle Line-to-Ground Fault at the End of Line 1

The simulation starts in steady state. At the t=1 cycle, a line-to-ground fault is applied and the fault current reaches 10 kA (trace 3). During the fault, the MOV conducts at every half cycle (trace 5) and the energy dissipated in the MOV (trace 6) builds up to 13 MJ. At t=5 cycles the line protection relays open breakers CB1 and CB2 (see the three line currents on trace 2) and the energy stays constant at 13 MJ. As the maximum energy does not exceed the 30 MJ threshold level, the gap is not fired. At the breaker opening, the fault current drops to a small value and the line and series capacitance starts to discharge through the fault and the shunt reactance. The fault current extinguishes at the first zero crossing after the opening order given to the fault breaker (t=6 cycles). Then the series capacitor stops discharging and its voltage oscillates around 220 kV (trace 4).

Three-Phase-to-Ground Fault Applied on Line 1

Open the **3-Phase Fault** block dialog box. Select the Phase B Fault and Phase C Fault, so that you now have a three-phase-to-ground fault.

Restart the simulation. The resulting waveforms are shown.

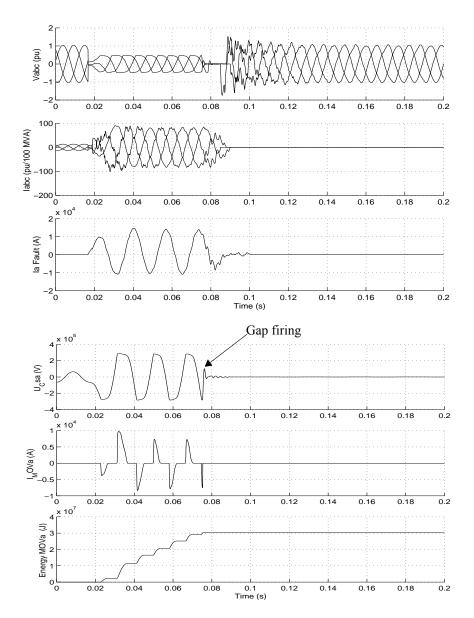


Figure 2-6: Simulation Results for a 4-Cycle Three-Phase-to-Ground Fault at the End of Line ${\bf 1}$

Note that during the fault the energy dissipated in the MOV (trace 6) builds up faster than in the case of a line-to-ground fault. The energy reaches the 30 MJ threshold level after three cycles, one cycle before the opening of the line breakers. As a result, the gap is fired and the capacitor voltage (trace 4) quickly discharges to zero through the damping circuit.

Frequency Analysis

One particular characteristic of series-compensated systems is the existence of subsynchronous modes (poles and zeros of the system impedance below the fundamental frequency). Dangerous resonances can occur if the mechanical torsion modes of turbine/generator shafts are in the vicinity of the zeros of the system impedance. Also, high subsynchronous voltages due to impedance poles at subsynchronous frequencies will drive transformers into saturation. The transformer saturation due to subsynchronous voltages is illustrated at the end of this case study. The torque amplification on a thermal machine is illustrated in another demonstration (see the psbthermal model).

Now measure the positive-sequence impedance versus frequency seen from bus B2.

Session 2 of the Tutorial chapter already explained how the Impedance Measurement block allows you to compute the impedance of a linear system from its state-space model. However, your case1 model contains several nonlinear blocks (machine and saturation of transformers). If you connect the Impedance Measurement block to your system, all nonlinear blocks are ignored. This is correct for the transformer, but you get the impedance of the system with the machine disconnected. Before measuring the impedance, you must therefore replace the machine block with an equivalent linear block having the same impedance.

Delete the Simplified Synchronous Machine block from your case1 model and replace it with the 3-Phase Source block from the Electrical Sources library. Open the block dialog box and set the parameters as follows in order to get the same impedance value (L=0.22~p.u./(6*350~MVA)) Quality factor = 15).

```
Phase-to-phase rms voltage :13.8e3
Phase angle of phase A: 0
Frequency (Hz) : 60
Internal connection Yg
Specify impedance using short-circuit level
3-phase short-circuit level: 6*350e6
```

Base voltage: 13.8e3

X/R ratio: 15

Save your modified model as case1Zf.

Open the Measurements library of **powerlib** and copy the Impedance Measurement block into your model. This block is used to perform the impedance measurement. Connect the two inputs of this block between phase A and phase B of the B2 bus. Measuring the impedance between two phases gives two times the positive-sequence impedance. Therefore you must apply a factor of 1/2 to the impedance in order to obtain the correct impedance value. Open the dialog box and set the multiplication factor to 0.5.

In the **powergui** select **Impedance vs Frequency Measurement**. A new window opens, showing your Impedance Measurement block name. Fill in the frequency range by entering 0:500. Select the linear scales to display Z magnitude vs. frequency plot. Check the **Save data to workspace** button and enter Zcase1 as the variable name to contain the impedance vs. frequency. Click the **Display** button.

When the calculation is finished, the magnitude and phase as a function of frequency are displayed in the two graphs on the window. If you look in your workspace, you should have a variable named Zcase1. It is a two-column matrix containing frequency in column 1 and complex impedance in column 2.

The impedance as a function of frequency (magnitude and phase) is shown here:

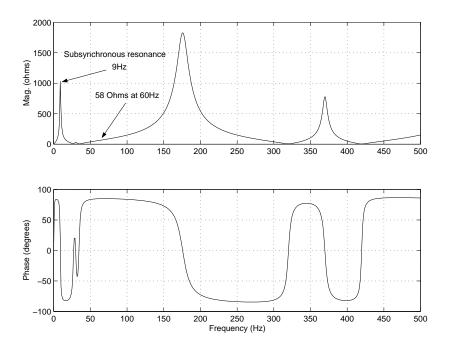


Figure 2-7: Impedance vs. Frequency Seen from Bus B2

You can observe three main modes: 9 Hz, 175 Hz, and 370 Hz. The 9 Hz mode is mainly due to a parallel resonance of the series capacitor with the shunt inductors. The 175 Hz and 370 Hz modes are due to the 600 km distributed parameter line. These three modes are likely to be excited at fault clearing.

If you zoom in on the impedance in the 60 Hz region, you can find the system's short-circuit level at bus B2. You should find a value of 58 Ω at 60 Hz, corresponding to a three-phase short-circuit power of $(735~\text{kV})^2$ / 58 = 9314~MVA.

Transient Performance for a Fault at Bus B2

The configuration of the substation circuit breakers normally allows clearing a fault at the bus without losing the lines or the transformers. You now modify your case1 model in order to perform a three-cycle, three-phase-to-ground fault at bus B2:

- 1 Disconnect the 3-Phase Fault block and reconnect it so that the fault is now applied on bus B2.
- **2** Open the **3-Phase Fault** dialog box and make the following modifications:

```
Phase A, Phase B, Phase C, Ground Faults : all selected Transition times : [2/60 \ 5/60] Transition status [1, 0, 1...]: (0/1)
```

You have now programmed a three-phase-to-ground fault applied at t=1 cycle.

3 Open the dialog boxes of circuit breakers CB1 and CB2 and make the following modifications:

```
Switching of Phase A: not selected
Switching of Phase B: not selected
Switching of Phase C: not selected
```

The circuit breakers are not switched anymore. They stay at their initial state (closed).

- **4** Insert a Selector block (from the Simulink Signals & Systems library) in the Vabc output of bus B2 connected to the scope. Set the **Elements** parameter to 1. This allows you to see the phase A voltage clearly on the scope.
- **5** You now add blocks to read the flux and the magnetization current of the saturable transformer connected at bus B2.

Copy the Multimeter block from the Measurements library into your case1 model. Open the **Transformer** dialog box. In the **Measurements** pop-up menu, select **Flux and magnetization Current**. Open the Multimeter block. Verify that you have six signals available. Select flux and magnetization current on phase A, and click **OK**.

- **6** You now have two signals available at the output of the Multimeter block. Use a Demux block to send these two signals on a two-trace scope.
- 7 In the Simulation —> Simulation parameters dialog, change the stop time to 0.5. This longer simulation time allows you to observe the expected low-frequency modes (9 Hz). Start the simulation.

Waveforms of interest are plotted here:

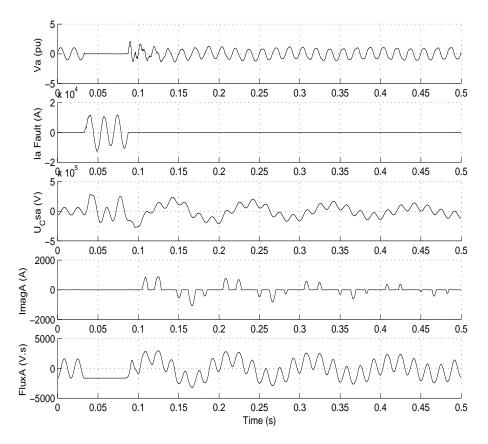


Figure 2-8: Simulation Results for a 3-Cycle 3-Phase-to-Ground Fault at Bus B2

The 9 Hz subsynchronous mode excited at fault clearing is clearly seen on the phase A voltage at bus B2 (trace 1) and capacitor voltage (trace 3). The 9 Hz voltage component appearing at bus B2 drives the transformer into saturation, as shown on the transformer magnetizing current (trace 4). The flux in phase A of the transformer is plotted on trace 5. At fault application the voltage at transformer terminals drops to zero and the flux stays constant during the fault.

At fault clearing, when the voltage recovers, the transformer is driven into saturation as a result of the flux offset created by the 60 Hz and 9 Hz voltage

components. The pulses of the transformer magnetizing current appear when the flux exceeds its saturation level. This current contains a 60 Hz reactive component modulated at 9 Hz.

Chopper-Fed DC Motor Drive

The example described in this section illustrates application of Power System Blockset to the operation of a DC motor drive in which the armature voltage is controlled by a GTO thyristor chopper.

The objective of this example is to demonstrate the use of electrical blocks, in combination with Simulink blocks, in the simulation of an electromechanical system with a control system. The electrical part of the DC motor drive, including the DC source, the DC motor, and the chopper, is built using blocks from the Elements, Machines, and Power Electronics libraries. The DC Machine block models both electrical and mechanical dynamics. The load torque-speed characteristic and the control system are built using Simulink blocks.

Description of the Drive System

A simplified diagram of the drive system is shown in the next figure. The DC motor is fed by the DC source through a chopper that consists of the GTO thyristor, Th1, and the free-wheeling diode D1. The DC motor drives a mechanical load that is characterized by the inertia J, friction coefficient B, and load torque T_L (which can be a function of the motor speed).

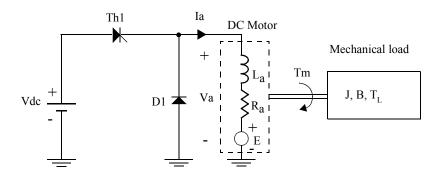


Figure 2-9: Chopper-Fed DC Motor Drive

In this diagram, the DC motor is represented by its equivalent circuit consisting of inductor L_a and resistor R_a in series with the counter electromotive force (emf) E.

The backreaction EMF is proportional to the motor speed

$$E = K_E \omega$$

where K_E is the motor voltage constant and ω is the motor speed.

In a separately excited DC machine, the motor voltage constant KE is proportional to the field current $I_{\mathbf{f}}$

$$K_E = L_{af}I_f$$

where L_{af} is the field-armature mutual inductance.

The torque developed by the DC motor is proportional to the armature current $I_{\mathbf{a}}$:

$$T_m = K_T I_a$$

where K_T is the motor torque constant.

The DC motor torque constant is equal to the voltage constant.

$$K_T = K_E$$

Thyristor Th1 is triggered by a pulse width modulated (PWM) signal to control the average motor voltage. Theoretical waveforms illustrating the chopper operation are shown here:

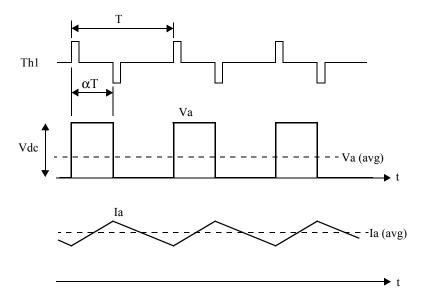


Figure 2-10: Waveforms Illustrating the Chopper Operation

The average armature voltage is a direct function of the chopper duty cycle α .

$$V_a(avg) = \alpha V_{dc}$$

Note that this relation is valid only when the armature current is continuous. In steady state, the armature average current is equal to

$$I_a(avg) = \frac{V_a(avg) - E}{R_a}$$

The peak-to-peak current ripple is

$$\Delta i = \frac{Vdc}{Ra} \frac{(1 - e^{-\alpha r} + e^{-r} - e^{-(1 - \alpha)r})}{1 - e^{-r}}$$

where α is the duty cycle and r is the ratio between the chopper period and the DC motor electrical time constant.

$$r = \frac{T}{(L_a/R_a)}$$

In this case study, a variable-speed DC motor drive using a cascade control configuration is considered. Here is a block diagram of this drive:

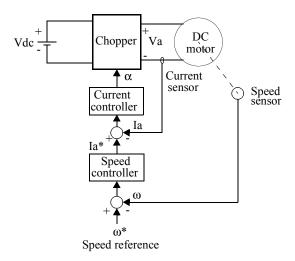


Figure 2-11: Variable-Speed DC Motor Drive

The motor torque is controlled by the armature current I_a , which is regulated by a current control loop. The motor speed is controlled by an external loop, which provides the current reference I_a^* for the current control loop.

Modeling the DC Drive

Open the psbdrive model and save this model as case2.mdl in your working directory so that you can make further modifications without altering the original file.

The drive system diagram is built with blocks from the **powerlib** library combined with Simulink blocks. Voltage Measurement and Current

Measurement blocks are used as the interface between the two block types. The system diagram is shown here:

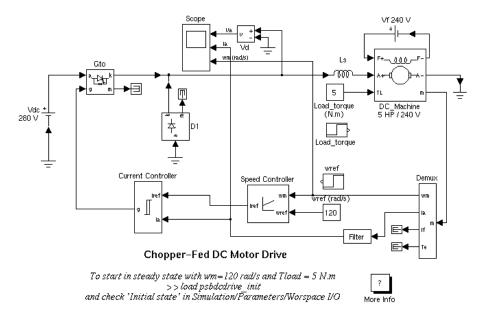
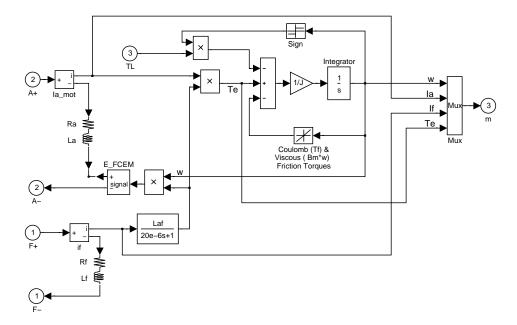


Figure 2-12: DC Motor Drive Using Power System Blockset (psbdcdrive.mdl)

The DC motor represented by the DC Machine block is modeled in two separate parts: electrical and mechanical. To view the Simulink model of the DC motor, click the DC Machine block and use the **Look under mask** item in the **Edit** menu.



The armature circuit is represented by an RL circuit in series with a controlled voltage source, the value of which is $K_E\omega$.

The field circuit is represented by an RL circuit.

The mechanical part is represented by Simulink blocks, which implement the following equation:

$$T_m = J \frac{d\omega}{dt} + B\omega + \operatorname{sgn} T_L$$

Set the DC machine parameters to the desired values by using the dialog box of the DC Machine block.

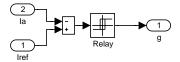
You implement the load torque-speed characteristic using a Simulink Fcn block.

The motor used in this case study is a separately excited 5 HP/240 V DC motor having the following parameters: Ra = 0.5 Ω , La = 10 mH, KE = 1.23 V/(rad/s), KT = 1.23 N.m/A.

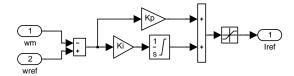
A 10 mH inductor (Ls) is connected in series with the DC motor to smooth out the armature current. The constant excitation is implemented by the connection of a DC Voltage Source block to the field winding.

The required trigger signal for the GTO thyristor is generated by a hysteresis current controller, which forces the motor current to follow the reference within +h/2 and -h/2 limits (h is the hysteresis band).

The current controller is a masked block that contains



The speed control loop uses a proportional-integral controller, which is implemented by Simulink blocks.



Simulation of the DC Drive

Run the simulation by selecting **Start** from the **Simulation** menu in Simulink. Set the simulation parameters in the **Simulation parameters** dialog as follows.

Simulation time: Start Time:0, Stop time: 1.2 Solver Type: Variable-step ode23tb (stiff/TR-BDF2) Max Step Size: auto

Initial Step Size: auto Relative Tolerance: 1e-3 Absolute Tolerance: 1e-3

The motor voltage, current waveforms, and motor speed are displayed on three axes of the scope connected to the variables V_d , I_a , and ω .

Once the simulation is completed, you can return to the MATLAB Command Window to examine the results with more detail by using the plot function.

Drive Starting

This test simulates the starting transient of the DC drive. The inertia of the mechanical load is small in order to bring out the details of the chopper commutation. The speed reference is stepped from 0 to 120 rad/s at $t=0.0\,\mathrm{s}$ and you can observe the DC motor speed and current.

The transient responses for the starting of the DC motor drive are shown in Figure 2-13.

Note that you can save the final system state vector xFinal by selecting the Workspace I/O —> Save to workspace —> Final state checkbox in the Simulation parameters dialog. It can be used as the initial state in a subsequent simulation so that the simulation can start under steady-state conditions.

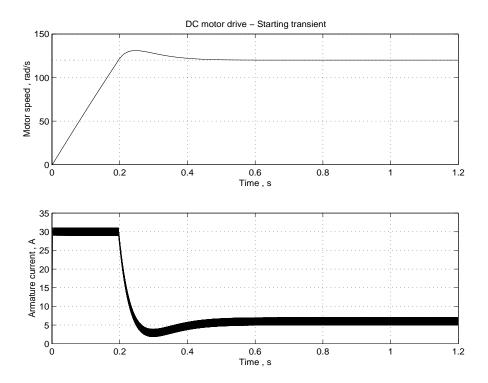


Figure 2-13: Starting the DC Motor Drive

Steady-State Voltage and Current Waveforms

When the steady state is attained, you can stop the simulation and plot the current and voltage waveforms using the variables V_a and I_a sent back to the MATLAB workspace by the scope.

The DC motor current and voltage waveforms obtained at the end of the starting test are shown here:

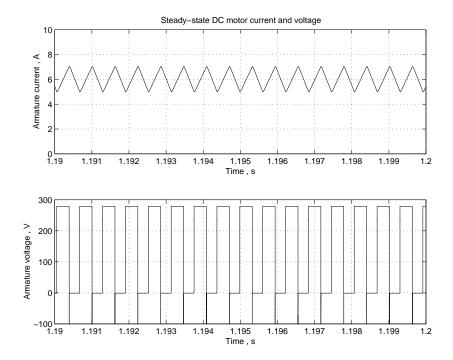


Figure 2-14: Steady-State Motor Current and Voltage Waveforms

Speed Regulation Dynamic Performance

You can study the drive dynamic performance, (speed regulation performance versus reference and load torque changes), by applying two successive changing operating conditions to the DC drive: a step change in speed reference and a step change in load torque.

Click the Torque Selection block to activate the load torque steps from 5 N.m to 25 N.m at t = 1.2 s. The speed reference steps from 120 rad/s to 160 rad/s at t = 0.4 s can be achieved by a Step block. The final state vector obtained with the previous simulation can be used as the initial condition so that the creates the xInitial variable. Select the **Workspace I/O** —> **Load from workspace** —> **Initial state** check box in the **Simulation parameters** window and restart the simulation.

The response of the DC motor drive to successive changes in speed reference and load torque is plotted here:

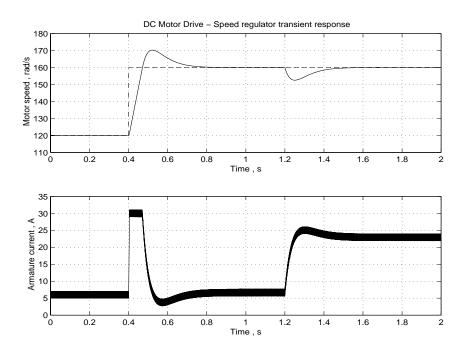


Figure 2-15: Dynamic Transient of the DC Motor Drive

References

[1] Leonhard, W., Control of Electrical Drives, Springer-Verlag, Berlin, 1996.

Synchronous Machine and Regulators

This case study investigates the application of a multiinput, multioutput nonlinear controller to a system consisting of a hydraulic turbine and a synchronous generator connected to an infinite bus. The complete system is modeled using Power System Blockset and Simulink blocks.

The objective of this case study is to demonstrate the use of the Synchronous Machine block connected to a complex control system implemented with Simulink blocks. The controller is based on a feedback linearization scheme. Its main goals are to control the rotor angle as well as the terminal voltage, to improve the stability properties, and to obtain good dynamic response. Simulation results will show that the nonlinear controller is able to replace the standard linear controllers and give better performance.

Traditionally, stabilization of power systems was ensured by linear regulators such as the automatic voltage regulator (AVR), the speed governor, or the power system stabilizer (PSS). These compensators assume a linearized model of the power system around an operating point.

The demand for improved performance has created the need to operate power systems closer to the limits and therefore well outside the linear domain. Nonlinearities then begin to have a significant effect, especially after important disturbances that lead to a large variation of the operating point.

This case study allows you to step through the design of a nonlinear controller that takes into account all the nonlinearities of the model. The objectives of the controller are to regulate both the terminal voltage and the internal power angle. The control inputs are the field excitation voltage and the gate opening of the turbine.

Mathematical Model

The model considered is a single machine infinite bus (SMIB) system, as shown in the next figure. The machine is a synchronous generator driven by a hydraulic turbine.

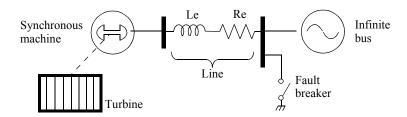


Figure 2-16: Diagram of Case Study

The dynamic equations of the machine that are used to derive the linear feedback controller are for the three-phase Synchronous Machine block and the Hydraulic Turbine and Governor block (see Chapter 4, "Power System Block Reference"). Because the synchronous machine is connected to an infinite bus, the dq terminal voltages v_d and v_q are constrained by the load equations. In the Park-transformed coordinates (rotor reference frame), v_d and v_q are expressed as

$$\begin{bmatrix} v_d \\ v_q \end{bmatrix} = R_e \begin{bmatrix} i_d \\ i_q \end{bmatrix} + L_e \begin{bmatrix} i_d \\ i_q \end{bmatrix} - \omega L_e \begin{bmatrix} i_q \\ -i_d \end{bmatrix} + V^{\infty} \begin{bmatrix} \cos(\delta - a) \\ -\sin(\delta - a) \end{bmatrix}$$

This equation can be combined with the complete model of the SMIB system in the nonlinear state-space form

$$\dot{x} = F(x) + G(x)u$$

F(x) and G(x) are given by

The explicit expressions of the coefficients A and g can be derived from the equations found in the "Power System Block Reference" under the Synchronous Machine block description. The other terms of the state-space equation are x, the vector of state variables, and u, the vector of control inputs. They are defined as follows:

$$x = \left[i_d \ i_q \ i_{fd} \ i_{kd} \ i_{kq} \ \delta \ \omega \ q \ G\right]^T$$
$$u = \left[v_{fd} \ u_G\right]^T$$

The currents i_d , i_q and voltages v_d , v_q are the projection of the actual line currents and terminal voltages on the direct and quadrature axes (dq frame). i_{fd} and v_{fd} represent the field current and voltage. i_{kq} and i_{kd} represent the damper windings currents, and ω the angular speed of the machine. δ is the electrical angle measured from a synchronously rotating frame. G and G are

respectively the opening of the gate and the flow rate of the turbine. Finally, \mathbf{u}_G is the voltage applied to the gate servomotor.

Feedback Linearization Design

The input-output feedback linearization technique consists in the exact cancellation of the nonlinearities of the system in order to obtain a linear relationship between inputs and outputs in a closed loop. The nonlinear control law is deduced by successively differentiating each of the outputs until at least one input appears. Consider the first output as the terminal voltage V_t :

$$y_1 = V_t = \sqrt{V_d^2 + V_q^2}$$

The terminal voltage is a complicated function of the state variables in which the control input V_{fd} appears explicitly with a multiplying factor of a very small order of magnitude. Ignore this direct dependence between V_t and V_{fd} and compute the derivative of output y_1 .

$$\begin{split} \frac{dy}{dt} &= \alpha_1(x) + \beta_{11}(x)u_1 + \beta_{12}(x)u_2, \quad \text{where} \\ \alpha_1(x) &= \frac{\partial V_t}{\partial x} \cdot F(x) \\ &= \frac{1}{2V_t} \bigg(2V_d \frac{\partial V}{\partial x} d + 2V_q \frac{\partial V}{\partial x} q \bigg) F(x) \\ \beta_{11}(x) &= \frac{\partial V_t}{\partial x} \cdot G_1(x) \\ &= \frac{1}{2V_t} \bigg(2V_d \frac{\partial V}{\partial x} d + 2V_q \frac{\partial V}{\partial x} q \bigg) G_1(x) \\ \beta_{12}(x) &= \frac{\partial V_t}{\partial x} \cdot G_2(x) \\ &= 0 \end{split}$$

The second output y_2 is the angle δ that has to be differentiated three times before the inputs appear. This yields

$$\begin{split} \frac{d^{3}y_{2}}{dt^{3}} &= \omega_{r} \frac{\partial F_{7}}{\partial x} \cdot F(x) + \omega_{r} \frac{\partial F_{7}}{\partial x} G_{1}(x)u_{1} + \omega_{r} \frac{\partial F_{7}}{\partial x} G_{2}(x)u_{2} \\ &= \alpha_{2}(x) + \beta_{21}(x)u_{1} + \beta_{22}(x)u_{2} \end{split}$$

Combining the equations of the outputs, the following input-output nonlinear system is obtained:

$$\begin{bmatrix} y_1^{(1)} \\ y_1^{(3)} \\ y_2^{(3)} \end{bmatrix} = \begin{bmatrix} \alpha_1(x) \\ \alpha_2(x) \end{bmatrix} + \begin{bmatrix} \beta_{11}(x) & 0 \\ \beta_{21}(x) & \beta_{22}(x) \end{bmatrix} \begin{bmatrix} u_1 \\ u_2 \end{bmatrix}$$

and the nonlinear control law is easily deduced.

$$\begin{bmatrix} u_1 \\ u_2 \end{bmatrix} = \begin{bmatrix} \beta_{11}(x) & 0 \\ \beta_{21}(x) & \beta_{22}(x) \end{bmatrix}^{-1} \begin{bmatrix} -\begin{bmatrix} \alpha_1(x) \\ \alpha_2(x) \end{bmatrix} + \begin{bmatrix} v_1 \\ v_2 \end{bmatrix} \end{bmatrix}$$

This yields, in a closed loop, the exactly linearized input-output system

$$\begin{bmatrix} y_1^{(1)} \\ y_2^{(3)} \end{bmatrix} = \begin{bmatrix} v_1 \\ v_2 \end{bmatrix}$$

Once the system has been linearized, any linear control design can be applied to regulate the outputs. Here, the pole placement method was chosen and the following linear control law is proposed.

$$\begin{split} v_1 &= k_{11}(V_t - V_{tref}) \\ v_2 &= k_{21}(\delta - \delta_{ref}) + k_{22}(\dot{\delta} - \dot{\delta}_{ref}) + k_{23}(\ddot{\delta} - \ddot{\delta}_{ref}) + k_{24}(x_9 - x_{9ref}) \end{split}$$

The last term in the equation of v_2 is introduced in order to stabilize the internal dynamics. These dynamics come about because the original nonlinear system is a ninth-order system while the linearized system is a fourth-order system. This is called *partial linearization*, and you must ensure that the remaining dynamics are asymptotically stable. A complete treatment of this question can be found in reference [1].

Simulation Results

The performance of the nonlinear controller is tested on the nonlinear turbine-generator system. The controller and turbine are simulated using Simulink blocks, while the generator is represented by the Synchronous Machine block from the **powerlib** library. A three-phase short-circuit is simulated on the load bus bar and the fault is cleared after 100 ms. The performance of the nonlinear controller is analyzed.

Open the psbregulator model. Before running the simulation, make sure that the simulation parameters are set as follows.

```
Solver: ode23tb; Maximum order: 5
Stop time: 1.0
Max step size: auto; Initial step size: auto
Relative tolerance: 1e-3; Absolute tolerance: auto
Workspace I/O: Load initial states: init_regulator.mat
```

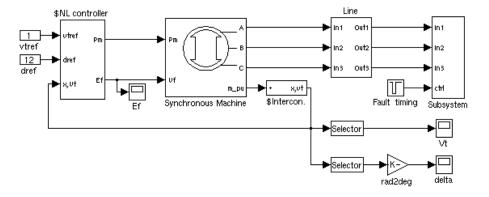


Figure 2-17: Simulink Diagram of Case Study (psbregulator.mdl)

Because of nonlinearities present in this system, computation of initial conditions is not carried out. Instead, a long simulation (10 s) is executed and the final states are saved in file data_regulator.mat.

These final states are used as initial states in this case study. The simulation consequently starts in steady state. At $t=0.1\,\mathrm{s}$, the fault is suddenly applied, and it is removed after 100 ms (6 cycles). The postfault transient is then observed.

The nonlinear controller calls a MATLAB initialization function to compute the gains before the simulation. Although this process has been automated to take into account the parameters in the dialog boxes of the various blocks, it is not recommended that you change any value in any block.

If you decide to change some values, a long simulation must be run and the final states must be in a file called <code>init_regulator.mat</code>. The next figure shows the response of the generator's terminal voltage, load angle, and the control effort of the regulator. You can observe how the stabilization of V_t is obtained in less than 0.25 seconds with this controller. The load angle takes longer to stabilize, because the time constant of the mechanical part of the system is much larger than the electrical time constants. If you want to compare results with classical regulators, replace the nonlinear controller with the same excitation system and Hydraulic Turbine and Governor block used in the psbturbine model. You can see that the system takes longer to stabilize than in this case study.

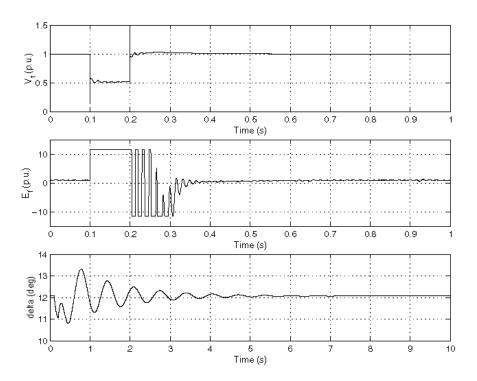


Figure 2-18: Simulation Results Obtained with Case Study

References

[1] Akhrif, O., F.A. Okou, L.A. Dessaint, and R. Champagne, "Application of a Multivariable Feedback Linearization Scheme for Rotor Angle Stability and Voltage Regulation of Power Systems," *IEEE Transactions on Power Systems*, Vol. 14, No. 2, May 1999, pp. 620-628.

Variable-Frequency Induction Motor Drive

This case study presents a variable-frequency AC motor drive in which a pulse width modulated (PWM) inverter is used as a variable-voltage variable-frequency source to drive an induction motor in variable-speed operation.

You model the drive, including the motor, the power converter, and the speed control system, by using Power System Blockset and Simulink blocks. The drive operation is studied for different operating conditions: starting, steady-state, and transients.

The objective of this example is to demonstrate the use of Machines library and Power Electronics library blocks in combination with Simulink blocks in the simulation of a complex electromechanical system operating at high frequency. The electrical part of the AC motor drive, including the PWM inverter, is built using the Universal Bridge block. The induction motor is represented by the Asynchronous Machine block, which models both electric and mechanical dynamics. The control system, including current and speed regulators, is built using Simulink blocks. The interface between electrical and control systems is managed by blocks of the Measurements library.

Description of the Induction Motor Drive

The induction motor requires a variable-frequency three-phase source for variable-speed operation. You can realize this source by using a power converter system consisting of a rectifier connected to an inverter through a DC link.

The next figure shows a block diagram of the power circuit of a typical variable-frequency induction motor drive.

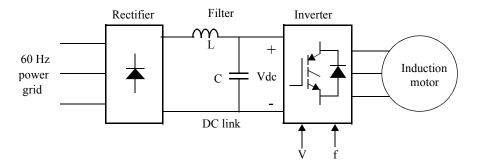


Figure 2-19: Variable-Frequency Induction Motor Drive

The power grid AC voltage is converted into a fixed DC voltage by the rectifier. The harmonics are filtered out by an LC filter to provide a smooth DC voltage, which is then applied to the inverter input.

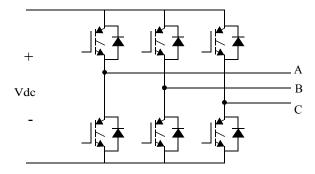


Figure 2-20: Three-Phase IGBT Inverter

The inverter consists essentially of six power switches that can be metal-oxide semiconductor field-effect transistors (MOSFET), gate turn-off thyristors (GTO), or insulated gate bipolar transistors (IGBT), depending on the drive power capacity and the inverter switching frequency (Hz). The preceding figure shows a simplified diagram of a three-phase IGBT inverter.

The inverter converts the DC link voltage into an adjustable three-phase AC voltage. Different control schemes can be used to control the inverter output voltage and frequency. One of the most utilized schemes is pulse width

modulation (PWM) in which you obtain three-phase variable sinusoidal voltage waveforms by modulating the on and off times of the power switches.

In industrial drive applications, the PWM inverter operates as a three-phase variable-frequency, variable-voltage source with fundamental frequency varying from zero to three times the motor nominal frequency.

In some control schemes where a three-phase, variable-frequency current source is required, current control loops are added to force the motor currents to follow an input reference (usually sinusoidal).

You can control the inverter-fed induction motor drive with various schemes depending on the application, desired performance, and controller design complexity. The most utilized schemes are

- Stator V/Hz control
- Stator currents and open loop flux control
- Vector control (field-oriented control)
- Direct torque control (DTC)

A Field-Oriented Variable-Speed Induction Motor Drive

This case study illustrates a variable-speed induction motor drive using field-oriented control. In this control scheme, a dq coordinates reference frame locked to the rotor flux space vector is used to achieve decoupling between the motor flux and torque. They can thus be controlled separately by stator direct-axis current and quadrature-axis current respectively, as in a DC motor. This figure shows a block diagram of a field-oriented induction motor drive:

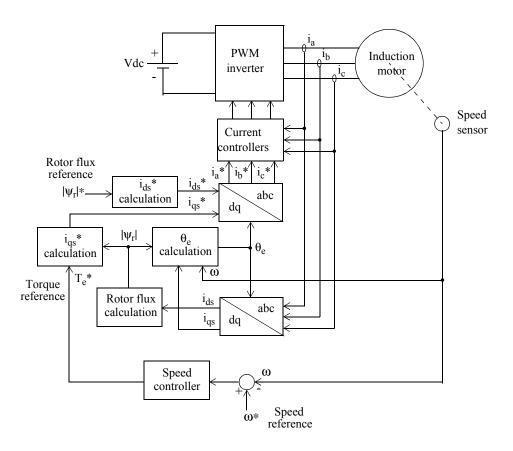


Figure 2-21: Field-Oriented Variable-Frequency Induction Motor Drive

The induction motor is fed by a current-controlled PWM inverter, which operates as a three-phase sinusoidal current source. The motor speed ω is compared to the reference ω^* and the error is processed by the speed controller to produce a torque command Te^* .

As shown below, the rotor flux and torque can be separately controlled by the stator direct-axis current i_{ds} and quadrature-axis current i_{qs} , respectively.

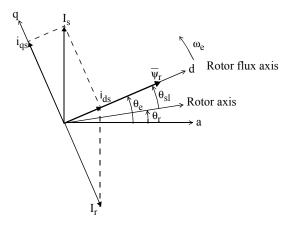


Figure 2-22: Field-Oriented Control Principle

The stator quadrature-axis current reference $i_{qs}{}^{*}$ is calculated from torque reference $T_{e}{}^{*}$ as

$$i_{qs}^{\ *} = \frac{2}{3} \cdot \frac{2}{p} \cdot \frac{L_r}{L_m} \cdot \frac{T_e^{\ *}}{|\Psi_r|_{est}}$$

where L_r is the rotor inductance, L_m is the mutual inductance, and $|\psi_r|_{est}$ is the estimated rotor flux linkage given by

$$\left| \Psi_r \right|_{est} = \frac{L_m i_{ds}}{1 + \tau_r s}$$

where τ_r = $L_r/\,R_r$ is the rotor time constant.

The stator direct-axis current reference i_{ds}^* is obtained from rotor flux reference input $|\psi_r|^*$:

$$i_{ds}^* = \frac{|\Psi_r|^*}{L_m}$$

The rotor flux position θ_e required for coordinates transformation is generated from the rotor speed ω_m and slip frequency ω_{sl} :

$$\theta_e = \int (\omega_m + \omega_{sl}) dt$$

The slip frequency is calculated from the stator reference current $i_{qs}^{}$ and the motor parameters.

$$\omega_{sl} = \frac{L_m}{|\Psi_r|_{est}} \cdot \frac{R_r}{L_r} \cdot i_{qs}^*$$

The i_{qs}^* and i_{ds}^* current references are converted into phase current references i_a^* , i_b^* , i_c^* for the current regulators. The regulators process the measured and reference currents to produce the inverter gating signals.

The role of the speed controller is to keep the motor speed equal to the speed reference input in steady state and to provide a good dynamic during transients. It can be of proportional-integral type.

Modeling the Induction Motor Drive

Open the pshacdrive model and save it as case4.mdl in your working directory so that you can make further modifications without altering the original file.

The next figure shows the psbacdrive model in which blocks from Power System Blockset and Simulink are used to model the induction motor drive.

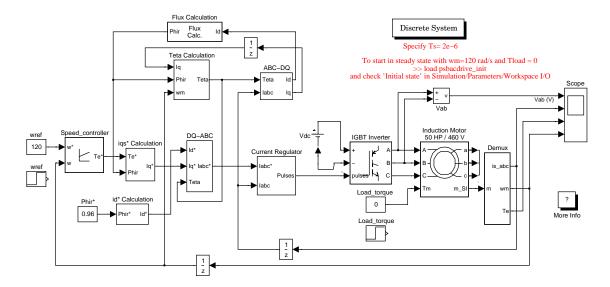
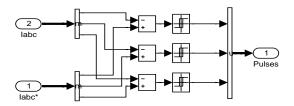


Figure 2-23: Variable-Speed Field-Oriented Induction Motor Drive (psbacdrive.mdl)

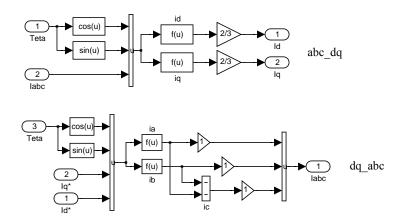
The induction motor is modeled by an Asynchronous Machine block. The motor used in this case study is a 50 HP, 460 V, four-pole, 60 Hz motor having the following parameters: Rs = 0.087 Ω , L1s = 0.8 mH, Lm = 34.7 mH, Rr = 0.228 Ω , L1r = 0.8 mH.

The current-controlled PWM inverter circuit is shown in Figure 2-23. The IGBT inverter is modeled by a Universal Bridge block in which the **Power Electronic device** and **Port configuration** options are selected as IGBT/Diode and ABC as output terminals respectively. The DC link input voltage is represented by a 780 V DC voltage source.

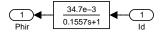
The current regulator, which consists of three hysteresis controllers, is built with Simulink blocks. The motor currents are provided by the measurement output of the Asynchronous Machine block.



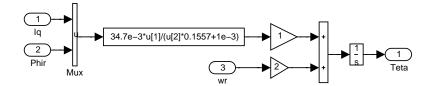
The conversions between abc and dq reference frames are executed by the abc_dq and dq_abc blocks of Figure 2-23.



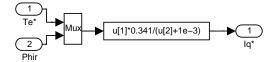
The rotor flux is calculated by the Flux_Calculation block of Figure 2-23.



The rotor flux position (θe) is calculated by the Teta Calculation block of Figure 2-23.



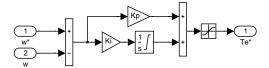
The stator quadrature-axis current reference (iqs*) is calculated by the iqs*_Calculation block of Figure 2-23.



The stator direct-axis current reference (ids*) is calculated by the id*_Calculation block of Figure 2-23.



The speed controller is of proportional-integral type and is implemented using Simulink blocks.



Simulating the Induction Motor Drive

Run the simulation by selecting **Start** from the **Simulation** menu in Simulink. The simulation parameters in the **Simulation parameters** dialog can be set as follows.

Simulation time: Start Time:0, Stop time: 1.5 Solver option: Type: Variable-step ode23tb

Max Step Size: auto Initial Step Size: auto Relative Tolerance: 1e-3 Absolute Tolerance: 1e-3

The motor voltage and current waveforms as well as the motor speed and torque are displayed on three axes of the scope connected to the variables Vab, Is, Te, and ω .

Once the simulation is complete, return to the MATLAB Command Window to examine the results with more detail by using the plot instruction.

Drive Starting

You can start the drive from a standstill by specifying null initial conditions for all state variables in the **powergui** and also specifying [1,0,0,0,0,0,0,0] as the initial conditions for the Asynchronous Machine block. In this example, the speed reference is stepped from 0 to 120 rad/s at t=0.

The transient responses for the starting of the induction motor drive are shown in Figure 2-24.

Note that you can save the final system state vector by previously selecting the Workspace I/O —> Save to work space —> Final state checkbox in the Simulation parameters dialog.

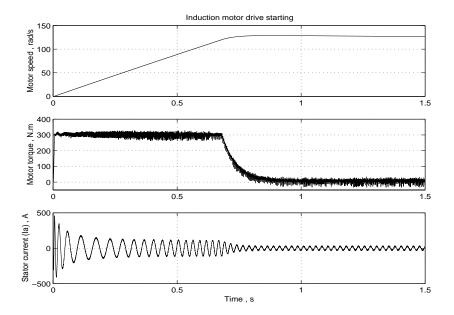


Figure 2-24: Starting the Induction Motor Drive

Steady-State Voltage and Current Waveforms

When the steady state is attained, you can stop the simulation and plot the voltage and current waveforms using the variables V_{ab} and I_a sent back to the MATLAB workspace by the scope.

This figure shows the motor current and voltage waveforms obtained when the load torque is $50\ N.m.$

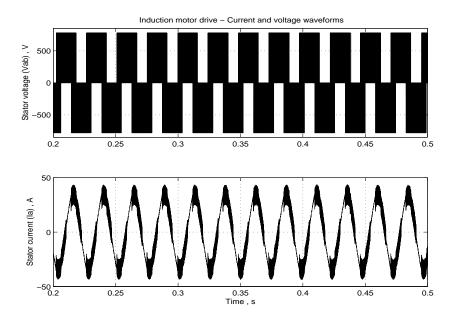


Figure 2-25: Steady-State Motor Current and Voltage Waveforms

Speed Regulation Dynamic Performance

You can study the drive dynamic performance (speed regulation performance versus reference and load torque changes) by applying two changing operating conditions to the drive: a step change in speed reference and a step change in load torque.

Use the Reference Speed Selection switch and the Torque Selection switch to set speed reference steps from 120 rad/s to 160 rad/s at t = 0.2 s and the load torque steps from 0 N.m to 200 N.m at t = 1.4 s. The final state vector obtained with the previous simulation can be used as the initial condition so that the simulation will start from steady state. Load the init_ACdrive.mat file, which creates the xInitial variable. Select the Workspace I/O —> Load from workspace —> Initial state check box in the Simulation parameters dialog and restart the simulation.

The response of the induction motor drive to successive changes in speed reference and load torque is shown here:

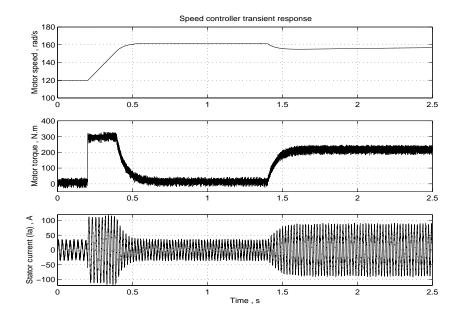


Figure 2-26: Dynamic Performance of the Induction Motor Drive

References

- [1] Leonhard, W., Control of Electrical Drives, Springer-Verlag, Berlin, 1996.
- [2] Murphy, J. M. D., and Turnbull, F. G., *Power Electronic Control of AC Motors*, Pergamon Press, Oxford, 1985.
- [3] Bose, B. K., *Power Electronics and AC Drives*, Prentice-Hall, Englewood Cliffs, N.J., 1986.

HVDC System

The final example described in this section illustrates modeling of a high-voltage direct current (HVDC) transmission link [1]. Perturbations are applied in order to examine the system performance [2]. The objectives of this example are to demonstrate the use of the Universal Bridge block and the Three-Phase Transformer (Three Windings) block in combination with Simulink blocks in the simulation of a complete pole of a 12-pulse HVDC transmission system. The electrical part representing the AC network is built using three-phase blocks. The Discrete 12-Pulse HVDC control system is a generic control available in the Discrete Control Blocks library of powerlib extras.

Description of the HVDC Transmission System

Open the psbhvdc12pulse model and save it as case5 in order to allow further modifications to the original system. This system is shown in Figure 2-27.

A 1000 MW (500 kV, 2 kA) DC interconnection is used to transmit power from a 500 kV, 5000 MVA, 60 Hz network to a 345 kV, 10000 MVA, 50 Hz network. The AC networks are represented by damped L-R equivalents with an angle of 80 degrees at fundamental frequency (60 Hz or 50 Hz) and at the third harmonic.

The rectifier and the inverter are 12-pulse converters using two Universal Bridge blocks connected in series. Open the two converter subsystems to see how they are built. The converters are interconnected through a 300 km line and 0.5 H smoothing reactors. The converter transformers (Wye grounded /Wye/Delta) are modeled with Three-Phase Transformer (Three-Windings) blocks. The transformer tap changers are not simulated. The tap position is rather at a fixed position determined by a multiplication factor applied to the primary nominal voltage of the converter transformers (0.90 on the rectifier side; 0.96 on the inverter side).

From the AC point of view, an HVDC converter acts as a source of harmonic currents. From the DC point of view, it is a source of harmonic voltages.

The order n of these characteristic harmonics is related to the pulse number p of the converter configuration: $n = kp \pm 1$ for the AC current and n = kp for the

direct voltage, k being any integer. In the example, p = 12, so that injected harmonics on the AC side are 11, 13, 23, 25, and on the DC side are 12, 24.

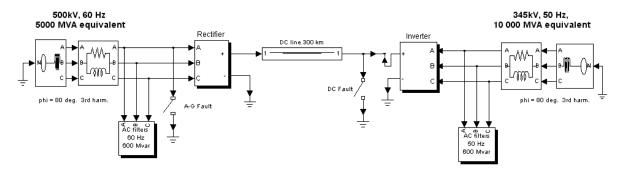


Figure 2-27: HVDC System

AC filters are used to prevent the odd harmonic currents from spreading out on the network. The filters are grouped in two subsystems. These filters also appear as large capacitors at fundamental frequency, thus providing reactive power compensation for the rectifier consumption due to the firing angle α . For $\alpha=30$ degrees, the converter reactive power demand is approximately 60% of the power transmitted at full load. Look under the AC filters subsystem mask to see the high Q (100) tuned filters at the 11th and 13th harmonics and the low Q (3), or damped filter, used to eliminate the higher order harmonics, e.g., 23rd and up. Extra reactive power is also provided by capacitor banks.

Two circuit breakers are used to apply faults on the rectifier AC and DC sides.

The rectifier and inverter control systems use the Discrete 12-pulse HVDC Control block of the Discrete Control Blocks library of **powerlib_extras**.

The power system and the control system are both discretized with the same sample time Ts.

Define parameter Ts = 50e-6 in your workspace before starting the simulation.

Frequency Response of the AC and DC Systems

You now measure the frequency response of the AC systems (rectifier and inverter sides) and of the DC line.

Session 2 in the Tutorial chapter already explained how the Impedance Measurement block allows you to compute the impedance of a linear system from its state-space model. As the thyristor valves of the converters are nonlinear blocks, they are ignored in the impedance calculation and you get the impedances with the valves open.

Open the Measurements library, copy three Impedance Measurement blocks into your model, and rename them Zrec, Zinv, and ZDC. Connect the two inputs of Zrec and Zinv between phase A and phase B of the AC system on the rectifier and inverter sides. Measuring the impedance between two phases gives two times the positive-sequence impedance. Therefore you must apply a factor of 1/2 to the impedance in order to obtain the correct impedance value. Open the two Impedance Measurement blocks and set the **Multiplication factor** to 0.5. Finally, connect input 1 of the ZDC block between the DC line terminal and the rectifier smoothing reactor, and connect input 2 to ground. Save your modified model as case5Zf.

In the **powergui** select **Impedance vs Frequency Measurement**. A new window opens, showing the three Impedance Measurement block names. Fill in the **Frequency range** by entering 10:2:1500. Select the lin scale to display the Z magnitude and lin scale for the frequency axis. Click the **Save data to workspace** button and enter Zcase5 as the variable name to contain the impedance vs. frequency. Click the **Display** button.

When the calculation is finished, the magnitude and phase as functions of frequency measured by the three Impedance Measurement blocks are displayed in the window. If you check in your workspace, you should have a variable named Zcase5. It is a four-column matrix containing frequency in column 1 and the three complex impedances in columns 2, 3, and 4 with the same order as in the window displaying the block names.

The magnitudes of the three impedances as a function of frequency are shown here:

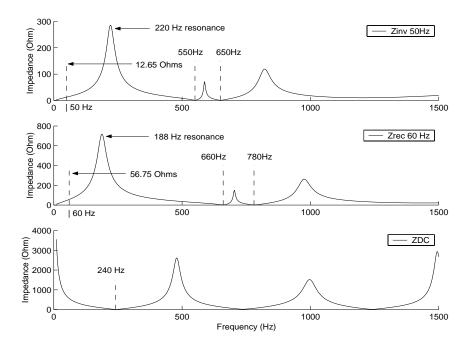


Figure 2-28: Positive-Sequence Impedances of the Two AC Networks and of the DC Line

Note the two minimum impedances on the Z magnitudes of the AC systems. These series resonances are created by the 11th and 13th harmonic filters. They occur at 660 Hz and 780 Hz on the 60 Hz system. Note also that the addition of 600 Mvar capacitive filters on the inductive systems creates resonances (around 188 Hz on the rectifier side and 220 Hz on the inverter side). Zoom in on the impedance magnitude in the 60 Hz region. You should find a magnitude of $56.75~\Omega$ for the 60 Hz system, corresponding to an effective short circuit level of $500^2/56.75 = 4405$ MVA on the rectifier side (5000 MVA - 600 Mvar of filters).

For the DC line, note the series resonance at 240 Hz, which corresponds to the main mode likely to be excited on the DC side, under large disturbances.

Description of the Control System

The control systems of the rectifier and of the inverter use the same 12-pulse HVDC Control block from the Controls library of **powerlib_extras**. The block can operate either in rectifier or inverter mode. Use the **Look under mask** to see how this block is built.

Inputs and Outputs

Input 1 (Vabc) is a vectorized signal of the three line-to-ground voltages measured at the primary of the converter transformer. These three voltages are used to synchronize the pulse generation on the line voltages. Inputs 2 and 3 are the DC line voltage (VdL) and current (Id). Note that the measured DC currents (IdR and IdI in A) and DC voltages (VdLR and VdLI in V) are scaled to p.u. (1 p.u. current = 2 kA; 1 p.u. voltage = 500 kV) before they are used in the controllers.

Inputs 4 and 5 (Vd_ref and Id_ref) are the Vd and Id reference values in p.u. The VdL and Id inputs are filtered before being processed by the regulators. A first-order filter is used on the Id input and a second-order filter is used on the VdL input. The filter parameters are shown in the dialog box of Figure 2-30.

Input 6 (Block) accepts a logical signal (0 or 1) used to block the converter when Block = 1. Input 7 is also a logical signal that can be used for protection purposes. If this signal is high (1), the firing angle is forced at the value defined in the block dialog box.

The first two block outputs (PulseY and PulseD) contain the vectorized signals of the six pulses to be sent to each of the six-pulse converters connected to the wye and delta windings of the converter transformer. The third output (alpha) is the firing delay angle in degrees ordered by the regulator. The fourth output (Id_ref_lim) is the actual reference current value (value of Id_ref limited by the VDCOL function as explained below).

Synchronization System

The Discrete 12-Pulse HVDC Control block uses the primary voltages (input 1) to synchronize and generate the pulses according to Vd_ref and Id_ref set points (inputs 4 and 5). The synchronizing voltages are measured at the primary side of the converter transformer because the waveforms are less distorted. The firing command pulse generator is synchronized to the fundamental frequency of the AC source. At the zero crossings of the commutating voltages (AB, BC, CA), a ramp is reset. A firing pulse is generated

whenever the ramp value becomes equal to the desired delay angle provided by the regulator. In order to improve the commutating voltages used by the pulse generator, the primary voltages (Vabc) are filtered by a low Q second-order band-pass filter centered at the fundamental system frequency. The base system frequency and the filter bandwidth are defined in the block dialog box.

Steady-State V-I Characteristic

The Discrete 12-Pulse HVDC Control block implements this steady-state characteristic:

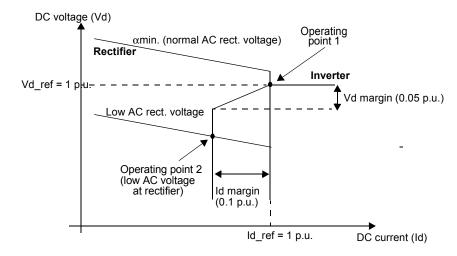


Figure 2-29: Rectifier and Inverter Steady-State Characteristics and VDCOL Function

In normal operation, the rectifier controls the current at the Id_ref reference value, whereas the inverter controls the voltage at the Vd_ref reference value. The Id_margin and Vd_margin parameters are defined in the inverter dialog box. They are set respectively at 0.1 p.u. and 0.05 p.u. The system normally operates at point 1 as shown in the figure. However, during a severe contingency producing a voltage drop on the AC network 1 feeding the rectifier, the operating point moves to point 2. The rectifier therefore is forced to a minimum mode and the inverter will be in current control mode.

Note In industrial controllers, the α angle at the inverter is normally limited in order to keep a minimum γ angle, where γ = extinction angle = 180° - α - μ , μ = commutation or overlap angle. The γ control required to avoid commutation failures is not implemented in this version of the HVDC control.

VDCOL Function

Another important control function is implemented to change the reference current according to the value of the DC voltage. This control, named Voltage Dependent Current Order Limiter (VDCOL), automatically reduces the reference current (Id_ref) set point when VdL decreases (as, for example, during a DC line fault or a severe AC fault). Reducing the Id reference currents also reduces the reactive power demand on the AC network, helping to recover from fault. The VDCOL parameters of the Discrete 12-Pulse HVDC Control block dialog box are explained by this diagram:

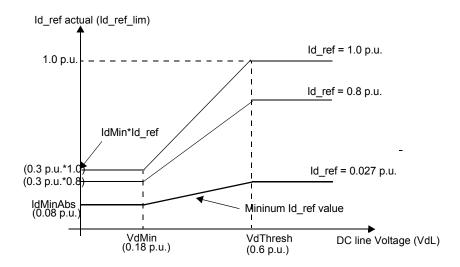


Figure 2-30: VDCOL Characteristic; Id_ref = f(VdL)

The Id_ref value starts to decrease when the Vd line voltage falls below a threshold value VdThresh (0.6 p.u.). The actual reference current used by the controllers is available at the fourth controller output, named Id_ref_lim.

IdMinAbs is the absolute minimum Id_ref value, set at 0.08 p.u. When the DC line voltage falls below the VdThresh value, the VDCOL reduces instantaneously to Id_ref. However, when the DC voltage recovers, VDCOL limits the Id_ref rise time with a time constant defined by parameter Tup (80 ms in the example).

Current and Voltage Regulators

The rectifier and the inverter controls both have a voltage and a current regulator operating in parallel calculating firing angles α_v and α_i . The effective α angle is the minimum value of α_v and α_i . This angle is available at the third block output, named alpha (deg). Both regulators are of the proportional-integral type. They should have high enough gains for low frequencies (<10 Hz) to maintain the current or voltage response equal to the reference current (Id_ref_lim) or reference voltage (Vd_ref), as long as α is within the minimum and maximum limits (5° < α < 165° for rectifier, 92° < α < 165° for inverter). The regulator gains Kp and KI are adjusted during small perturbations in the current reference. The following gains are used:

```
Current regulator: Kp = 92^{\circ}/p.u. Ki = 4500^{\circ}/p.u./s Voltage regulator: Kp = 35^{\circ}/p.u. Ki = 2250^{\circ}/p.u./s
```

Another particularity of the regulator is the linearization of the proportional gain. As the Vd voltage generated by the rectifier and the inverter is proportional to $\cos(\alpha)$, the ΔVd variation due to a $\Delta \alpha$ change is proportional to $\sin(\alpha)$. With a constant Kp value, the effective gain is therefore proportional to $\sin(\alpha)$. In order to keep a constant proportional gain, independent of the α value, the gain is linearized by multiplying the Kp constant with $1/\sin(\alpha)$. This linearization is applied for a range of α defined by two limits specified in the dialog box (third line).

System Startup and Steady State

Notice that the system is discretized, using sample time Ts (you should already have Ts = 50e-6 defined in your workspace).

The system is programmed to start and reach a steady state. Then a step is applied to the reference current to observe the dynamic response of the regulators.

Start the simulation and observe the signals on the rectifier and inverter scopes. The waveforms are reproduced here:

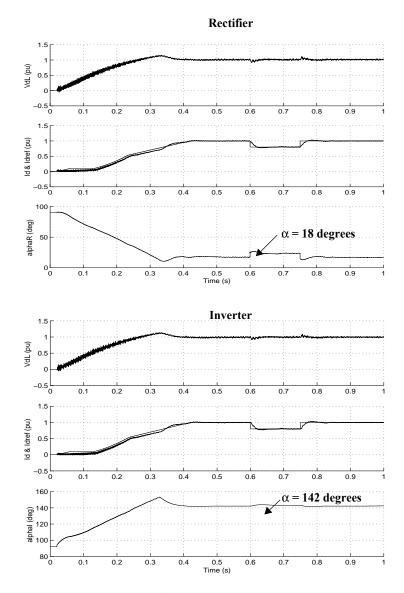


Figure 2-31: Startup of the DC System and Step Applied on the Reference Current

The reference current follows a ramp from zero to 1 p.u. (2 kA) in 0.4 s. Observe that the DC current starts to build up at t=20 ms, time at which the controller and the pulse generators are deblocked. The DC current and voltages start from zero and reach steady state in approximately 0.5 s. The rectifier controls the current and the inverter controls the voltage. Trace 1 of both rectifier and inverter scopes shows the DC line voltage (1 p.u. = 500 kV). Trace 2 shows the reference current and the measured Id current (1 p.u. = 2 kA). Once steady state is attained, the α firing angles are 18 degrees and 142 degrees respectively on the rectifier and inverter side.

Then at t = 0.6 s, a step is applied on the reference current to observe the dynamic response of the regulators.

The main equations governing the steady-state operation of the DC system are given here so that you can compare the theoretical values to the simulation results.

The following expression relates the mean direct voltage Vd of a 12-pulse bridge to the direct current Id and firing angle α :

$$Vd = 2 \times (Vdo \times \cos(\alpha) - Rc \times Id)$$

where *Vdo* is the ideal no-load direct voltage for a six-pulse bridge:

$$Vdo = (3\sqrt{2}/\pi) \times Vc$$

Vc is the line-to-line rms commutating voltage that is dependent on the AC system voltage and the transformer ratio.

Rc is the equivalent commutating resistance

$$Rc = (3/\pi) \times Xc$$

Xc is the commutating reactance or transformer reactance referred to the valve side.

The following rectifier parameters were used in the simulation.

The Vc voltage must take into account the effective value of the voltage on the 500 kV bus and the transformer ratio. If you look at the waveforms displayed on the V_I_Rect scope, you find 0.96 p.u.

If you open the rectifier transformer dialog box, you find a multiplication factor of 0.91 applied to the primary nominal voltage. The voltage applied to the inverter is therefore boosted by a factor of 1/0.91.

$$Vc = 0.96 * 200 \ kV/0.90 = 213.3 \ kV; \\ Id = 2 \ kA \\ \alpha = 18 \ ^{\circ}; \\ Xc = 0.24 \ p.u. \ based \ on \ 1200 \ MVA \ and \ 222.2 \ kV = 9.874 \ \varOmega$$

Therefore

$$Vdo = (3\sqrt{2}/\pi) \times 213.2 = 287.9 \ kV$$
 $Rc = (3/\pi) \times 9.874 = 9.429 \ \Omega$ $Vd = 2 \times (288.1 \ kV \times \cos{(18^{\circ})} - 9.429 \times 2) = 510 \ kV$

This theoretical voltage corresponds well with the expected rectifier voltage calculated from the inverter voltage and the voltage drop in the DC line.

$$Vd = VdL_{inverter} + (R_{DCline} + R_{inductance}) \times Id$$
$$Vd = 500 kV + (4.5 \Omega + 1 \Omega) \times 2 = 511 kV$$

The μ commutation or overlap angle can also be calculated. Its theoretical value depends on α , the DC current Id, and the commutation reactance Xc.

$$\mu = a\cos\left[\cos(\alpha) - \frac{Xc \cdot Id \cdot \sqrt{2}}{Vc}\right] - \alpha$$

$$\mu \; = \; acos \bigg[cos \, (18^\circ) - \frac{9.874 \cdot 2 \cdot \sqrt{2}}{213.3} \bigg] - 18^\circ \! = \; 16.9^\circ$$

Now verify the commutation angle by plotting the currents in two valves, showing for example current extinction in valve 1 and current buildup in valve 3 of one six-pulse bridge of the rectifier.

Open the rectifier subsystem. Then open the upper bridge dialog box and select **All voltages and currents** for the **Measurement** parameter. Now copy the Multimeter block from the Measurements library into your case5 model. Double-click the Multimeter block. A window showing all the bridge voltages and currents appears. Select the following signals:

uSw1: Rectifier/Universal Bridge
 iSw1: Rectifier/Universal Bridge
 iSw3: Rectifier/Universal Bridge

The number of signals (3) is displayed in the multimeter icon. Using a Demux block, send the three multimeter output signals to a two-trace scope (Trace 1: uSw1 Trace 2: iSw1 and iSw3). Restart the simulation. The waveforms illustrating two cycles are shown in the following figure. The measured commutation angle is 14 steps of 50 μs or 15.1° of a 60 Hz period. The resolution with a 50 μs time step is 1.1°, this angle compares reasonably well with the theoretical value.

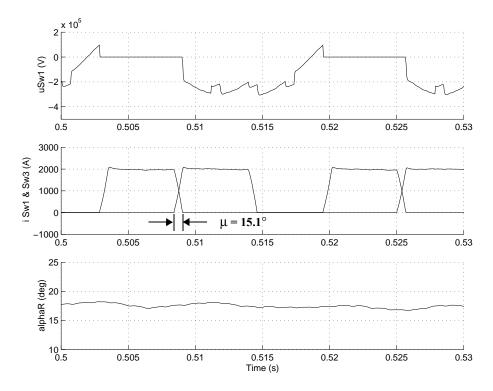


Figure 2-32: Valve Voltage and Currents (Commutation from Valve 1 to Valve 3)

Response to a Step of Reference Current

At $t=0.6\,\mathrm{s}$, a $0.2\,\mathrm{p.u.}$ step is applied to the reference current (decrease from 1 p.u. to $0.8\,\mathrm{p.u.}$). At $t=0.75\,\mathrm{s}$, another step is applied to set the reference back to 1 p.u. Observe the response of the current regulator. It stabilizes in approximately $0.1\,\mathrm{s.}$

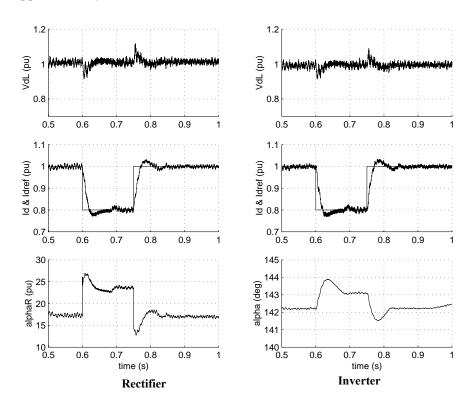


Figure 2-33: Response to a 0.2 p.u. Step of the Reference Current

DC Line Fault

Disconnect the Step Up & Down block in order to eliminate the step disturbance applied to the reference current. In the DC Fault Timer and Forced Delay blocks of the psbhvdc12pulse model, change the multiplication factor of 100 in the Transition Times to 1, so that a fault is now applied at t = 0.6 s. Open the I_DCfault scope to observe the fault current. Restart the simulation.

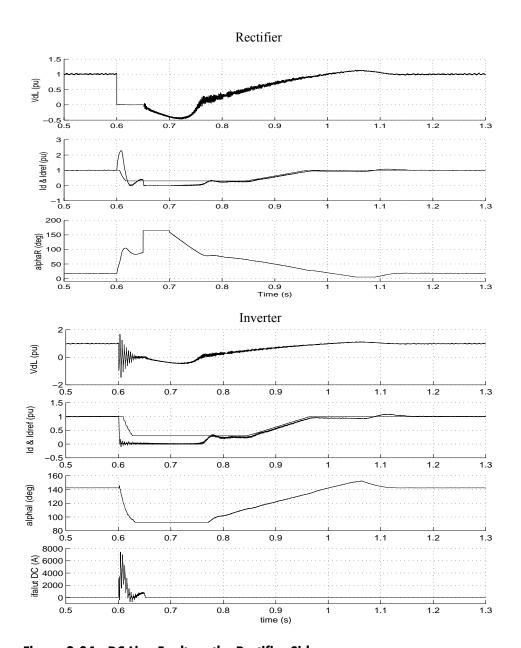


Figure 2-34: DC Line Fault on the Rectifier Side

At fault application (t = 0.6 s), the DC current increases to 2.3 p.u. and the DC voltage falls to zero at the rectifier. This DC voltage drop is seen by the Voltage Dependent Current Order Limiter (VDCOL), which reduces the reference current to 0.3 p.u. at the rectifier. A DC current still continues to circulate in the fault. Then, at t = 0.65 s, the rectifier α firing angle is forced to 165 degrees when the signal applied to the ForcedAlpha input goes high. This signal would normally be provided by the protection system not simulated here. The rectifier now operates in inverter mode. The DC line voltage becomes negative and the energy stored in the line is returned to the AC network, causing rapid extinction of the fault current at its next zero crossing. Then α is released at t = 0.7 s and the normal DC voltage and current recover in approximately 0.5 s.

AC Line-to-Ground Fault at the Rectifier

Now you modify the timer blocks in order to apply a line-to-ground fault. In the DC Fault Timer and Forced Delay blocks of psbhvdc12pulse, change the multiplication factor of 1 in the Transition Times to 100, so that the DC fault is now eliminated. In the AC Fault Timer block, change the multiplication factor in the transition times to 1, so that a six-cycle line-to-ground fault is now applied at the rectifier. Restart the simulation.

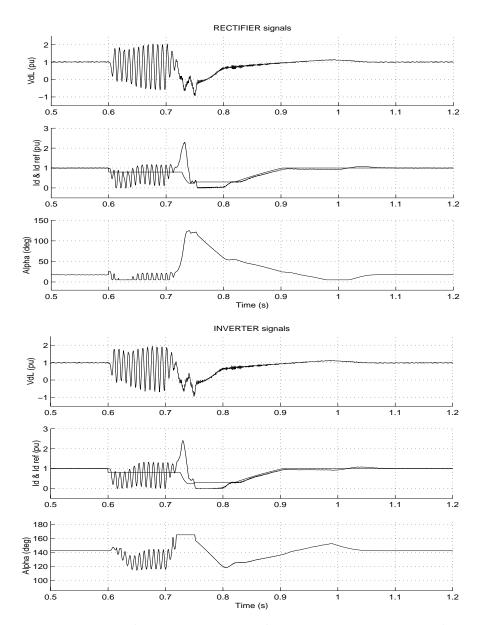


Figure 2-35: Rectifier, Inverter Signals for an AC Line Fault on Rectifier Side

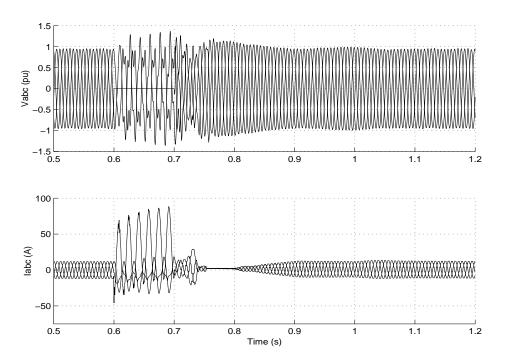


Figure 2-36: Voltages and Currents on the 60 Hz Side for an AC Line Fault on the Rectifier Side

Notice the 120 Hz oscillations in the DC voltage and currents during the fault. When the fault is cleared at $t=0.7\,\mathrm{s}$, the VDCOL operates and reduces the reference current to 0.3 p.u. The system recovers in approximately 0.4 s after fault clearing.

References

[1] Arrilaga, J., *High Voltage Direct Current Transmission*, IEEE Power Engineering Series 6, Peter Peregrinus, Ltd., 1983.

[2] Electromagnetic Transients Program (EMTP), Workbook IV (TACS), EL-4651, Volume 4, Electric Power Research Institute, 1989.

Advanced Topics

How the Power System Blockset Works (p. 3-2)

Choosing an Integration Method: Continuous or Discrete (p. 3-5)

Simulating with Continuous Integration Algorithms (p. 3-6)

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Advantages and disadvantages of continuous and discrete time simulations of power system models

How to integrate continuous time power models with

Power System Blockset

How to solve discretized power models with Power

System Blockset

Ways to optimize simulation speed and efficiency

Using and modifying the **powerlib_models** library to model nonlinear power components

Creating your own custom power system blocks with the Simulink block masking feature

Modifying Power System block parameters during simulation and automating with MATLAB scripts

How the Power System Blockset Works

Once you have built your circuit with the blocks of **powerlib**, you can start the simulation just like any other Simulink model. Each time you start the simulation, a special initialization mechanism is called. This initialization process computes the state-space model of your electric circuit and builds the equivalent system that can be simulated by Simulink.

The power2sys function is part of the process. It gets the state-space model and builds the Simulink model of your circuit. power2sys can also be called from the command line to obtain the state-space model of the linear part of the circuit. When called by the initialization process, power2sys performs the following four steps as shown in Figure 3-1:

- Sorts all Power System Blockset blocks, gets the block parameters and evaluates the network topology. The blocks are separated into linear and nonlinear blocks, and each electrical node is automatically given a node number.
- Once the network topology has been obtained, the state-space model of the linear part of the circuit is computed by the circ2ss function. All steady-state calculations and initializations are performed at this stage.
- If you have chosen to discretize your circuit, the discrete state-space model is computed from the continuous state-space model, using the Tustin method.
- Builds the Simulink model of your circuit and stores it inside one of the measurement blocks. This means that you need at least one measurement block (Current Measurement block, Voltage Measurement block, Three-Phase VI Measurement block, or Multimeter block) in your model. The connections between the equivalent circuit and measurements blocks are performed by invisible links using the Goto and From blocks.

The Simulink model uses a State-Space block or an S-Function block to model the linear part of the circuit. Predefined Simulink models are used to simulate nonlinear elements. These models can be found in the **powerlib_models** library available with Power System Blockset. Simulink source blocks connected at the input of the State-Space block are used to simulate the electrical sources blocks.

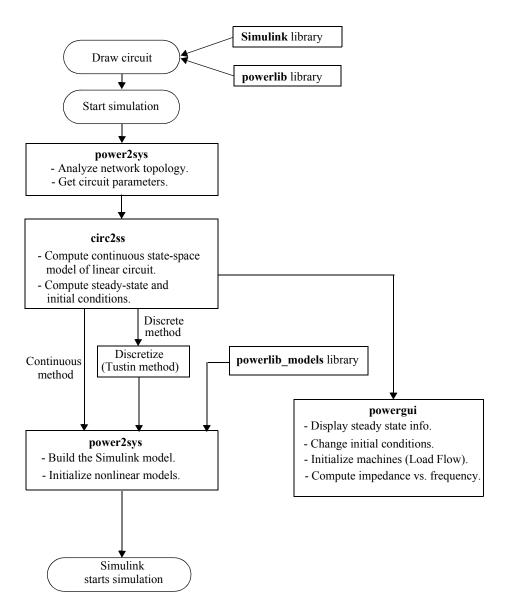


Figure 3-1: Power System Blockset Flowchart

The next figure represents the interconnections between the different parts of the complete Simulink model. The nonlinear models are connected in feedback between voltage outputs and current inputs of the linear model.

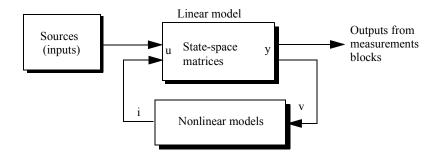


Figure 3-2: Interconnection of Linear Circuit and Nonlinear Models

Once **power2sys** has completed the initialization process, Simulink starts the simulation and you can observe waveforms on scopes connected at the outputs of your measurement blocks.

If you stop the simulation and drag a copy of the Powergui block into your circuit window, you will have access to the steady-state values of inputs, outputs, and state variables displayed as phasors. You can also use the interface to modify the initial conditions. The Powergui block interface allows you to perform a load flow with circuits involving three-phase machinery and initialize the machine models so that the simulation starts in steady state. This feature avoids long transients due to mechanical time constants of machines. The Powergui block allows you to specify the desired frequency range, visualize impedance curves, and store results in your workspace for Impedance Measurement blocks connected in your circuit.

Choosing an Integration Method: Continuous or Discrete

One important feature of Power System Blockset is its ability to simulate electrical systems either with continuous variable time-step integration algorithms or with a fixed time step using a discretized system. For small size systems, the continuous method is usually more accurate. Variable time step algorithms are also faster because the number of steps is fewer than with a fixed time step method giving comparable accuracy. When using line-commutated power electronics, the variable-step, event-sensitive algorithms detect the zero crossings of currents in diodes and thyristors with a high accuracy so that you do not observe any current chopping. However, for large systems (containing either a large number of states or nonlinear blocks), the drawback of the continuous method is that its extreme accuracy slows down the simulation. In such cases, it is advantageous to discretize your system. In the following two sections, we explain these two methods, their advantages, and their limitations.

What do we mean by "small size" and "large size" systems? Although the distinction is not clear, you can consider small size a system that contains fewer than 30 electrical states and fewer than 6 electronic switches. Circuit breakers do not affect the speed too much, because unlike power electronic switches, which are commutated at every cycle, these devices are operated only a couple of times during a test.

Simulating with Continuous Integration Algorithms

Simulink provides a variety of solvers. Most of the variable-step solvers will work well with linear circuits. However circuits containing nonlinear models, especially circuits with circuit breakers and power electronics, require stiff solvers.

Choosing an Integration Algorithm

Fastest simulation speed is usually achieved with ode23tb or ode15s with default parameters.

```
Solver: ode23tb or ode15s
Relative tolerance = 1e-3
Absolute tolerance = auto
Maximum step size = auto; Initial step size = auto.
Initial step size = auto
Maximum order (for ode15s) = 5
```

Normally, you can choose auto for the absolute tolerance and the maximum step size. In some occasions you might have to limit the maximum step size and the absolute tolerance. Selecting too small a tolerance can slow down the simulation considerably. The choice of the absolute tolerance depends on the maximum expected magnitudes of the state variables (inductor currents and capacitor voltages). For example, if you work with high-power converters where expected voltage and currents are thousands of volts and amperes, an absolute tolerance of 1e-1 or even 1.0 should be sufficient. If you are working with low-power circuits involving maximum values of 100 V and 10 A, you should use a smaller absolute tolerance, such as 1e-3 or 1e-2.

Simulating Switches and Power Electronic Devices

Two methods are used for simulation of switches and power electronic devices:

• If the switch is purely resistive the switch model is considered as part of the linear circuit. The state-space model of the circuit, including open and closed switches, is therefore recalculated at each switch opening or closing, producing a change in the circuit topology. This method is always used with the Breaker block and the Ideal Switch block because these elements do not have internal inductance. It is also applied for the Diode block and the

Thyristor block, with Ron > 0 and Lon = 0, and for the Universal Bridge with forced commutated devices.

• If the switch contains a series inductance (Diode and Thyristor with Lon > 0, IGBT, MOSFET, or GTO), the switch is simulated as a current source driven by voltage across its terminals. The nonlinear element (with a voltage input and a current output) is then connected in feedback on the linear circuit, as shown in Figure 3-2.

You have therefore the choice to simulate diodes and thyristors with or without Lon internal inductance. In most applications, it is not necessary to specify an inductance Lon. However, for circuit topologies resulting in zero commutation or overlap angle you will have to specify a switch inductance Lon in order to help commutation.

Consider for example the circuit shown in the following figure. This circuit is available in the psbrectifier_ideal model. The thyristor bridge is fed from an infinite source (zero impedance) so that the commutation between thyristors is quasi instantaneous.

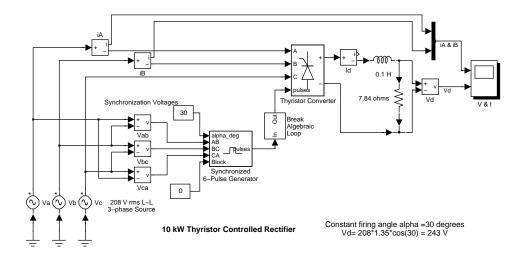


Figure 3-3: Three-Phase Thyristor Rectifier on Infinite Source

If you simulate this circuit without internal thyristor inductances (Lon = 0), you will observe high current spikes flowing in the three lines. This happens because during commutation two thyristors connected to the same positive or

negative terminal of the bridge are in conduction for a short period of time, applying a line-to-line short circuit on the source (see Figure 3-4 following). During commutation, the current is limited only by the internal resistance of thyristors (with Ron = 0.01 ohms, the current reaches 7.35 kA ($208^{2*}\sin(30)$ / (2*0.01) = or 245 times the normal DC current of 30 A). These short circuits can be avoided by using a small Lon = 1 μ H in the thyristor model. If you repeat the simulation, you will get square current waveforms with a peak value of 30 A.

If you zoom on the line current during a commutation, you discover that the commutation is not instantaneous. The commutation time depends on the Lon value and the DC current.

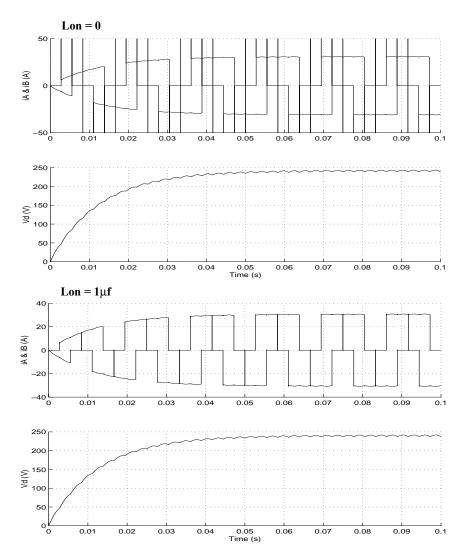


Figure 3-4: Source Currents and DC Load Voltage with Lon = 0 and Lon = 1 μH

Simulating Discretized Electrical Systems

Discretization is performed by dragging the Powergui block into your system. The sample time is specified in the block dialog box. The electrical system is discretized using the Tustin method, that is equivalent to a fixed-step trapezoidal integration. In order to avoid algebraic loops, the electrical machines are discretized using the Forward Euler method.

The precision of the simulation is controlled by the time step you choose for the Discretization. If you use too large a sample time, the precision might not be sufficient. The only way to know if it is acceptable is to repeat the simulation with different sample times or to compare with a continuous method and to find a compromise for the largest acceptable sample time. Usually sample times of 20 μs to 50 μs will give good results for simulation of switching transients on 50 Hz or 60 Hz power systems or on systems using line-commutated power electronic devices such as diodes and thyristors. However, for systems using forced-commutated power electronic switches, you must reduce the time step. These devices, the insulated-gate-bipolar transistor (IGBT), the field-effect transistor (FET), and the gate-turn-off thyristor (GTO) are usually operating at high switching frequencies. For example, simulating a pulse-width modulated (PWM) inverter operating at 8 kHz requires a time step of 1 μs or less.

Note that even if you discretize your electric circuit, you can still use a continuous control system. However, the simulation speed is improved by use of a discrete control system.

Limitations with Nonlinear Models

Discretization of individual forced-commutated electronic devices is not allowed.

Discretization of circuits containing forced-commutated power electronic devices (IGBT, GTO, or MOSFET) is permitted only with the Universal Bridge block. Discretization of circuits containing individual forced-commutated devices is not allowed. For example, an attempt to discretize the buck DC chopper circuit saved in the psbbuckconv model produces a warning message:

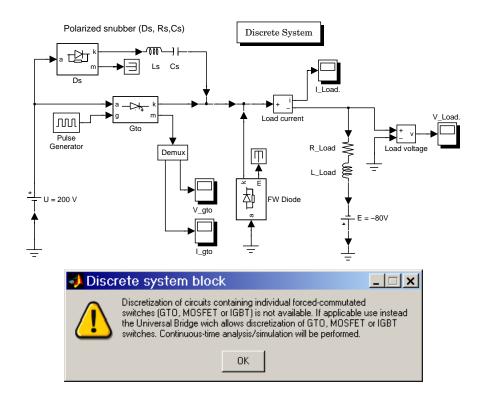


Figure 3-5: A Circuit Containing Individual Forced Commutated Electronic Switches Cannot be Discretized

In this circuit, the opening of the GTO will force a quasi instantaneous conduction of the freewheeling diode. If the circuit was discretized, the diode would be fired with one step delay, and the inductive current chopping would produce large overvoltages. However, for conventional converter topologies as in the case of the Universal Bridge, the switch interactions are known in advance. For example, in a six-switch IGBT/Diode inverter (Figure 3-6 following), opening of IGBT1 causes instantaneous conduction of diode D2 in the same arm. As the circuit topology is predetermined, it is possible to force firing of the diode in the same step that the IGBT opens. You should use a continuous method if you prefer to use individual IGBT and Diode blocks to simulate a complete inverter.

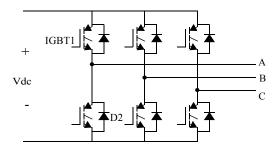


Figure 3-6: IGBT Inverter Simulated by the Universal Bridge

Minimal load is required at machine terminals. When using electrical machines in discrete systems, you might have to use a small parasitic resistive load, connected at the machine terminals, in order to avoid numerical oscillations. Large sample times require larger loads. The minimum resistive load is proportional to the sample time. As a rule of thumb, remember that with a 25 us time step on a 60 Hz system, the minimum load is approximately 2.5% of the machine nominal power. For example, a 200 MVA synchronous machine in a power system discretized with a 50 μs sample time requires approximately 5% of resistive load or 10 MW. If the sample time is reduced to 20 μs , a resistive load of 4 MW should be sufficient.

Lon = 0 is used for diodes and thyristors in discrete circuits. Diodes and thyristors used in a discretized circuit must have a zero internal inductance. If you discretize a circuit containing diodes or thyristors with Lon > 0, Power System Blockset prompts you with a warning indicating that Lon will be set to zero.

Increasing Simulation Speed

Once the proper method (continuous or discrete), solver type, and parameters have been selected, there are still some ways of optimizing the simulation speed:

- Discretize your electric circuit and your control system. You can even use a larger sample time for the control system, provided that it is a multiple of the smallest sample time.
- Simulating large systems or complex power electronic converters can be time-consuming. If you have to repeat several simulations from a particular operating point, you can save time by specifying a vector of initial states in the **Simulation** —> **Simulation parameters** —> **Workspace IO** dialog pane. This vector of initial conditions must have been saved from a previous simulation run.
- Reducing the number of open scopes and the number of points saved in the scope will also help in reducing the simulation time.

The Nonlinear Model Library

The building blocks used to assemble the Simulink model of the nonlinear circuit are stored in a library named **powerlib_models**. You do not normally need to work with the **powerlib_models** library. However, you might have to look inside the models or modify them for particular applications. You can access that library by entering powerlib_models in the MATLAB Command Window.

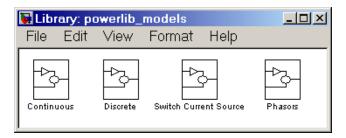
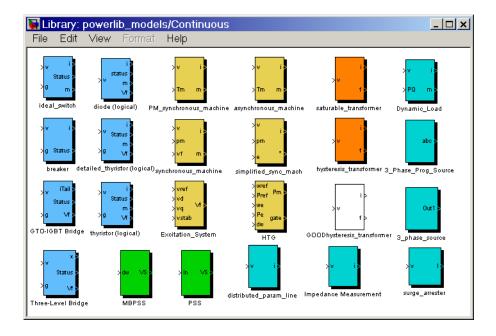


Figure 3-7: The powerlib_models Library

The Continuous Library

The Continuous library contains two types of blocks:

- Current sources simulating continuous machine models, surge arrester, saturable transformer, and distributed parameter lines
- Switching logics used for purely resistive power electronic devices (Breaker, Diode, Thyristor, and Universal Bridge of forced-commutated devices)



Nonlinear Blocks Simulated by Current Sources

These blocks use a voltage input (output of the state-space model of the linear circuit) and their current output is fed into the state-space model. For complex models, such as electrical machines requiring several inputs and outputs, vectorized signals are used. Useful internal signals are also returned by most of the models in a m measurement output vector.

For example, the Asynchronous Machine model is stored in the block named asynchronous_machine. The model uses as inputs a vector of four voltages: two rotor voltages (VabR and VbcR) and two stator voltages (VabS and VbcS). It returns a vector of four currents: two rotor currents (IaR and IbR) and two stator currents (IaS and IbS). The model also returns a measurement output vector of 20 signals. When the Asynchronous Machine block is used from **powerlib** this measurement output vector is accessible through the moutput of the machine icon. You can get details on the model inputs and outputs from the documentation of **powerlib** and **powerlib_models** block icons.

Logics for Switches and Power Electronic Devices

For switches and power electronic devices, the blocks contain only the logic returning the status (open or closed) of the switch. The switch status is passed to an S-function, which recomputes the state-space model of the linear circuit each time that a switch status is changed. The moutput is a vector returning the switch current and voltage. The i output returns the tail current of forced-commutated devices such as IGBTs and GTOs. All the switch logics are vectorized. This means that a single model is used by **power2sys** to simulate all the devices having the same type.

The Discrete Library

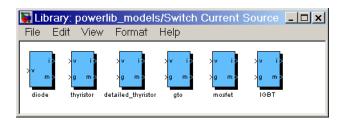
The Discrete library contains the discrete versions of the continuous models described above.

The Phasors Library

The Phasors library contains the phasor versions of some of the continuous models described above. See the "Tutorial" chapter for more details on the phasor simulation.

The Switch Current Source Library

This library contains models of power electronic devices, which are simulated by a current source external to the linear circuit.



These devices are the diode and the thyristor with Lon > 0, and the three forced commutated devices: gate-turn-off thyristor (GTO), metal-oxide-semiconductor field-effect transistor (MOSFET), and the insulated-gate-bipolar transistor (IGBT). All these models are continuous and contain an internal inductance, allowing you to handle fast transitions of forced commutated converters. As for electrical machines, these models use a voltage input (output of the state-space

model of the linear circuit) and their current output is fed into the state-space model. All these models are vectorized.

Limitations of the Nonlinear Models

Because nonlinear models are simulated as current sources, they cannot be connected in series with inductors and their terminals cannot be left open.

If you feed a machine through an inductive source, **power2sys** prompts you with an error message. You can avoid this by connecting large resistances in parallel with the source inductances or across the machine terminals.

A series RC snubber circuit is included in the model of the Breaker block and power electronic blocks. You will not have any problem if you keep these snubber circuits in service. The snubber can be changed to a single resistance by setting Cs to Inf, or to a single capacitor by setting Rs = 0. To eliminate the snubber, specify Rs = Inf or Cs = 0.

Modifying the Nonlinear Models of the powerlib_models Library

To use your own **powerlib_models** library, you must first copy the powerlib_models.mdl file into your working directory or any other directory. If you are using a directory different from the current directory, you must specify this new directory in the MATLAB search path in front of the standard blockset directory.

Then you can customize this new **powerlib_models** library as long as you do not change the names of the blocks, the number of inputs and outputs, and the number of parameters in their dialog boxes. The next time that you run the simulation, the modifications will take place in your circuit.

Creating Your Own Library of Models

Power System Blockset provides a variety of basic building blocks to build more complex electric blocks. Using the masking feature of Simulink, you can assemble several elementary blocks of **powerlib** into a subsystem, build your own parameter dialog box, create the desired block icon, and place this new block in your personal library.

The "Tutorial" chapter explained how to build a nonlinear model using a Voltage Measurement block and a Controlled Current Source block. The proposed examples (a nonlinear inductance and a nonlinear resistance) were relatively simple. Using the same principle, you can develop much more complex models using several controlled current sources, or even controlled voltage sources. Refer to the tutorial "Session 8: Building and Customizing Nonlinear Models" on page 1-57.

Changing Your Circuit Parameters

Each time that you change a parameter of the **powerlib** blocks, you have to restart the simulation in order to evaluate the state-space model and update the parameters of the nonlinear models. However, you can change any source parameter (Magnitude, Frequency or Phase) during the simulation. The modification takes place as soon as you apply the modification or close the source block menu.

As for the Simulink blocks, all the **powerlib** block parameters that you specify in the dialog box can contain MATLAB expressions using symbolic variable names. Before running the simulation, you must assign a value to each of these variables in your MATLAB workspace. This allows you to perform parametric studies by changing the parameter values in a MATLAB script.

Example of MATLAB Script Performing a Parametric Study

Suppose that you want to perform a parametric study in a circuit named my_circuit to find the impact of varying an inductance on switching transients. You want to find the highest overvoltage and the inductance value for which it occurred.

The inductance value of one of the blocks contains variable L1, which should be defined in your workspace. L1 is varied in 10 steps from 10 mH to 100 mH and the values to be tested are saved in a vector L1_vec. The voltage waveform to be analyzed is stored in a ToWorkspace block in matrix format with V1 variable name.

You can write a MATLAB M-file that loops on the 10 inductance values and displays the worst case.

```
L1_vec= (10:10:100)*1e-3; % 10 inductances values 10/100 mH
V1_max=0;
for i=1:10
    L1=L1_vec(i);
    fprintf('Test No %d L1= %g H\n', i, L1);
    sim('my_circuit'); % performs simulation
    % memorize worst case
    if max(abs(V1))>V1_max,
         imax=i;
        V1 max=max(abs(V1));
```

```
end
end
fprintf('Maximum overvoltage= g V occured for L1=Hn', V1_max,
L1_vec(imax));
```

Power System Block Reference

This chapter contains complete information on every block in Power System Blockset. Refer to this chapter when you need to find detailed information on a particular block.

Blocks – By Category (p. 4-2) The Power System Blockset blocks summarized by block

library

Blocks – Alphabetical List (p. 4-7) The Power System Blockset blocks listed alphabetically

by name

Blocks - By Category

The Power System Blockset main library, powerlib, organizes its blocks into libraries according to their behavior. The **powerlib** window displays the block library icons and names. This section contains the block library hierarchy and the block library contents, a listing of all Power System Blockset blocks arranged by library.

Use the Simulink Library Browser or the Power System Blockset library to access the blocks directly, guided by this hierarchical library list.

- The Electrical Sources library contains blocks that generate electric signals.
- The Elements library contains linear and nonlinear network elements.
- The Power Electronics library contains power electronics devices.
- The Machines library contains machinery models.
- The Connectors library contains blocks that can be used to interconnect blocks in various situations.
- The Measurements library contains blocks for the current and voltage measurements.
- The Extras library contains three-phase blocks and specialized measurements and control blocks. This library can also be opened by entering powerlib extras in the MATLAB window.
- The Demos library contains useful demos and case studies.
- The **powerlib** window also contains the Powergui block that opens a graphical user interface for the steady-state analysis of electrical circuits.

The Simulink models of the nonlinear blocks of **powerlib** are stored in a library named **powerlib models**. These Simulink models are used by the Power System Blockset to build the equivalent Simulink model of your circuit. See the "Advanced Topics" chapter for a description of the **powerlib models** library.

The subsequent pages contain reference information for all blocks in Power System Blockset, arranged in alphabetical order by block name.

Creating Electrical Sources

AC Current Source Implement a sinusoidal current source
AC Voltage Source Implement a sinusoidal voltage source
Controlled Current Source Implement a controlled current source
Controlled Voltage Source Implement a controlled voltage source

DC Voltage Source Implement a DC voltage source

3-Phase Programmable Implement a three-phase source signal with programmable time

Voltage Source variation of amplitude, phase, frequency, and harmonics

Creating Circuit Elements

Breaker Implement a circuit breaker opening at current zero crossing

Distributed Parameter Line Implement a N-phases distributed parameter line model with

lumped losses

Linear Transformer Implement a two- or three-windings linear transformer

Mutual Inductance Implement a magnetic coupling between two or three windings

Parallel RLC Branch Implement a parallel RLC branch
Parallel RLC Load Implement a linear parallel RLC load

PI Section Line Implement a single phase transmission line with lumped

parameters

Saturable Transformer Implement a two- or three-windings Saturable Transformer

Series RLC Branch Implement a series RLC branch
Series RLC Load Implement a linear series RLC load
Surge Arrester Implement a metal-oxide surge arrester

3-Phase Dynamic Load Implement a three-phase dynamic load with programmable active

power and reactive power

Three-Phase Transformer

(Two Windings)

Implement a three-phase transformer with two windings

Three-Phase Transformer

(Three Windings)

Implement a three-phase transformer with three windings

Zigzag Phase-Shifting

Transformer

Implement a zigzag phase-shifting transformer with secondary

winding connection

Creating Power Electronics Components

Diode Implement a diode model

GTO Implement a gate-turn-off (GTO) thyristor model

Ideal Switch Implement an ideal switch model

IGBT Implement an insulated-gate-bipolar-transformer (IGBT) model

MOSFET Implement a metal-oxide-semiconductor-field-effect-transistor

(MOSFET) model

Three Level Bridge Implement a three-level neutral point clamped (NPC) power

converter

Thyristor Implement a thyristor model

Universal Bridge Implement a universal three-phase bridge converter

Creating Electrical Machines

Asynchronous Machine Model the dynamics of a three-phase asynchronous machine

(induction machine)

DC Machine Model a separately excited DC machine.

Provide an excitation system for the synchronous machine and Excitation System

regulate its terminal voltage in generating mode

Generic Power System

Stabilizer

Provide a generic power system stabilizer for the synchronous

machine and regulate its electrical power

Hydraulic Turbine and

Governor

Model a hydraulic turbine and a proportional-integral-derivative

governor system

Multiband Power System

Stabilizer

Implement a multiband power system stabilizer

Permanent Magnet Model the dynamics of a three-phase permanent magnet synchronous Machine synchronous machine with sinusoidal flux distribution

Simplified Synchronous Model the dynamics of a simplified three-phase synchronous

Machine machine

Steam Turbine and Governor Implement a steam turbine and governor system

Synchronous Machine Model the dynamics of a three-phase salient-pole synchronous

machine

Creating Electrical Connectors

Ground Provide a connection to the ground

Neutral Implement a local common node in the circuit

Measuring Electrical Circuits

Current Measurement Measure a current in a circuit

Impedance Measurement Measure the impedance in a circuit as a function of the frequency

Multimeter Measure voltage and current in Power System Blockset blocks

Voltage Measurement Measure a voltage in a circuit

Analyzing Electrical Circuits

Powergui Graphical user interface for the analysis of circuits and systems

Additional Useful Blocks

Signal Measurements

abc_to_dq0 Transformation Perform a Park transformation from the three-phase (abc)

reference frame to the dq0 reference frame

Active & Reactive Power Measure the active and reactive powers of a voltage-current pair

dq0_to_abc Transformation Perform a Park transformation from the dq0 reference frame to

the three-phase (abc) reference frame

Fourier Perform a Fourier analysis of a signal

RMS Measure the root mean square (RMS) value of a signal

3-Phase Sequence Analyzer Measure the positive, negative, and zero sequence components of a

three phase signal

Three-Phase V-I Measurement Measure three-phase currents and voltages in a circuit

Total Harmonic Distortion Generate a control logical signal changing at specified transition

times

Signal and Pulse Sources

PWM Generator Generate pulses for a carried-based Pulse Width Modulator

(PWM)

Synchronized 6-Pulse

Generator

Implement a synchronized pulse generator to fire the thyristors of

a six-pulse converter

Synchronized 12-Pulse

Generator

Implement a synchronized pulse generator to fire the thyristors of

a twelve-pulse converter

Timer Generate a control logical signal changing at specified transition

times

Three-Phase Breakers

3-Phase Breaker Implement a three-phase circuit breaker opening at current zero

crossing

3-Phase Fault Implement a programmable phase-to-phase and phase-to-ground

fault breaker system

Blocks – Alphabetical List

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Purpose

Perform a Park transformation from the three-phase (abc) reference frame to the dq0 reference frame

Library

Extras/Measurements

Description



The abc_to_dq0 Transformation block computes the direct axis, quadratic axis, and zero sequence quantities in a two-axis rotating reference frame for a three-phase sinusoidal signal. The following transformation is used:

$$\begin{split} V_d &= \frac{2}{3}(V_a\sin(\omega t) + V_b\sin(\omega t - 2\pi/3) + V_c\sin(\omega t + 2\pi/3)) \\ V_q &= \frac{2}{3}(V_a\cos(\omega t) + V_b\cos(\omega t - 2\pi/3) + V_c\cos(\omega t + 2\pi/3)) \\ V_0 &= \frac{1}{3}(V_a + V_b + V_c) \end{split}$$

where

 ω = rotation speed (rad/s) of the rotating frame

The transformation is the same for the case of a three-phase current; you simply replace the V_a , V_b , V_c , V_d , V_q , and V_0 variables with the I_a , I_b , I_c , I_d , I_q , and I_0 variables.

This transformation is commonly used in three-phase electric machine models, where it is known as a Park transformation. It allows you to eliminate time-varying inductances by referring the stator and rotor quantities to a fixed or rotating reference frame. In the case of a synchronous machine, the stator quantities are referred to the rotor. I_d and I_q represent the two DC currents flowing in the two equivalent rotor windings (d winding directly on the same axis as the field winding, and q winding on the quadratic axis), producing the same flux as the stator $I_a,\,I_b,\,$ and I_c currents.

You can use this block in a control system to measure the positive-sequence component V_1 of a set of three-phase voltages or currents. The V_d and V_q (or I_d and $I_q)$ then represent the rectangular coordinates of the positive-sequence component.

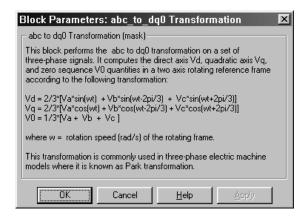
You can use the Math Function block and the Trigonometric Function block to obtain the modulus and angle of V_1 :

$$|V_1| = \sqrt{V_q^2 + V_d^2}$$

$$\angle V_1 = \operatorname{atan2}(V_q/V_d)$$

This measurement system does not introduce any delay, but, unlike the Fourier analysis done in the Sequence Analyzer block, it is sensitive to harmonics and unbalances.

Dialog Box



Inputs and Outputs

abc

Connect to the first input the vectorized sinusoidal phase signal to be converted [phase A phase B phase C].

sin cos

Connect to the second input a vectorized signal containing the $[\sin(\omega t)\cos(\omega t)]$ values, where ω is the rotation speed of the reference frame.

dq0

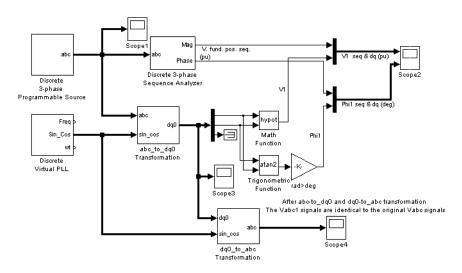
The output is a vectorized signal containing the three sequence components [d q o].

Example

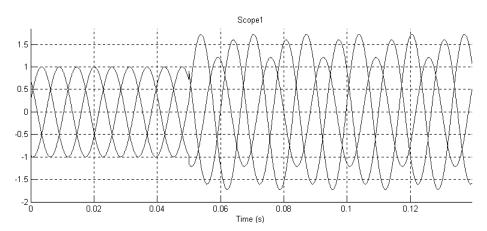
The psb3phsignaldq.mdl demo uses a Discrete 3-Phase Programmable Source block to generate a 1 p.u., 15 degrees positive sequence voltage. At 0.05 second the positive sequence voltage is increased to 1.5 p.u. and at 0.1 second an imbalance is introduced by the addition of a 0.3 p.u. negative sequence

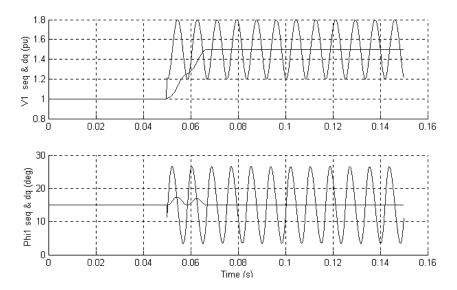
component with a phase of -30 degrees. The magnitude and phase of the positive-sequence component are evaluated in two different ways:

- Sequence calculation of phasors using Fourier analysis
- abc-to-dq0 transformation

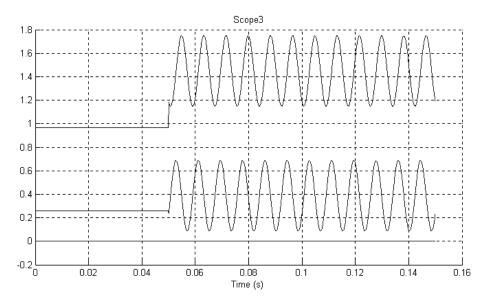


Start the simulation and observe the instantaneous signals Vabc (Scope1), the signals returned by the Sequence Analyzer (Scope2), and the abc-to-dq0 transformation (Scope3).





Note that the Sequence Analyzer, which uses Fourier analysis, is immune to harmonics and imbalance. However, its response to a step is a one-cycle ramp.



The abc-to-dqo transformation is instantaneous. However, an imbalance produces a ripple at the V1 and Phi1 outputs.

See Also dq0_to_abc Transformation

AC Current Source

Purpose

Implement a sinusoidal current source

Library

Electrical Sources

Description

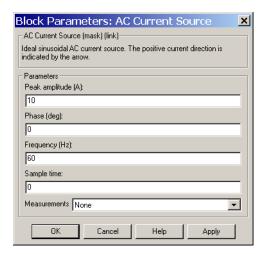
•û

The AC Current Source block implements an ideal AC current source. The positive current direction is indicated by the arrow in the block icon. The generated current I is described by the following relationship:

$$I = A\sin(\omega t + \phi)$$
 $\omega = 2\pi f$ $\phi = P\frac{\pi}{180}$

Negative values are allowed for amplitude and phase. A 0 frequency specifies a DC current source. Negative frequency is not allowed; otherwise, Simulink signals an error, and the block displays a question mark in the block icon. You can modify the first three block parameters at any time during the simulation.

Dialog Box and Parameters



Peak amplitude

The peak amplitude of the generated current source, in amperes (A).

Phase

The phase in degrees (deg).

Frequency

The source frequency in hertz (Hz).

Sample time

The sample period in seconds (s). The default is 0, corresponding to a continuous source.

Measurements

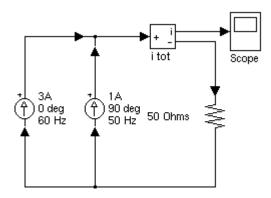
Selects **Current** to measure the current flowing through the AC Current Source block.

Place a Multimeter block in your model to display the selected measurements during the simulation. In the **Available Measurements** list box of the Multimeter block, the measurement is identified by a label followed by the block name.

Measurement	Label
Current	Isrc:

Example

The psbaccurrent.mdl demo uses two AC Current Source blocks in parallel to sum two sinusoidal currents in a resistor.



See Also

Controlled Current Source, Multimeter

Active & Reactive Power

Purpose

Measure the active and reactive powers of a voltage-current pair

Library

Extras/Measurements

Description



The Active & Reactive Power block measures the active power P and reactive power Q associated with a periodic voltage-current pair that can contain harmonics. P and Q are calculated by averaging the V I product with a running window over one cycle of the fundamental frequency, so that the powers are evaluated at fundamental frequency.

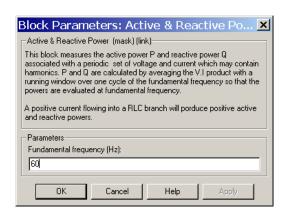
$$P = \frac{1}{T} \int_{t}^{(t+T)} (V(\omega t) \times I(\omega t)) dt$$

$$Q = \frac{1}{T} \int_{t}^{(t+T)} (V(\omega t) \times I(\omega t - \pi/2)) dt$$

where T = 1/(fundamental frequency).

A current flowing into an RL branch, for example, produces positive active and reactive powers.

Dialog Box and Parameters



Fundamental frequency (Hz)

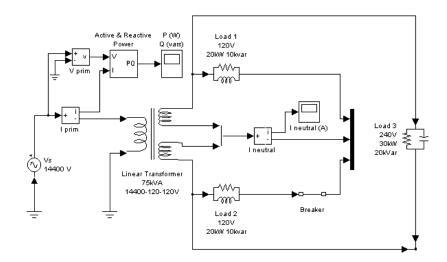
The fundamental frequency, in hertz, of the instantaneous voltage and current.

Inputs and Outputs

The first input is the instantaneous voltage. The second input is the instantaneous current. The output is a vector $[P\ Q]$ of the active and reactive powers.

Example

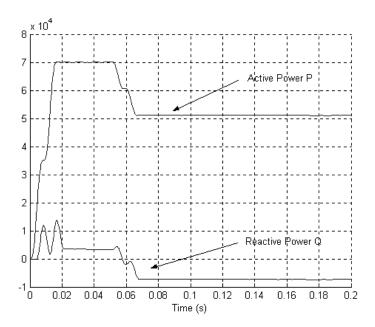
The psbtransfo.mdl demo simulates a three-winding distribution transformer rated at 75 kVA - 14400/120/120 V. The transformer primary winding is connected to a high-voltage source of 14400 Vrms. Two identical inductive loads (20 kW-10 kvar) are connected to the two secondary windings. A third capacitive load (30 kW-20 kvar) is fed at 240 V.



Initially, the circuit breaker in series with Load 2 is closed, so that the system is balanced. When the circuit breaker opens, a current starts to flow in the neutral path as a result of the load unbalance.

The active power computed from the primary voltage and current is measured by an Active & Reactive Power block.

Active & Reactive Power



When the breaker opens, the active power decreases from 70~kW to 50~kW.

Purpose

Implement a sinusoidal voltage source

Library

Electrical Sources

Description

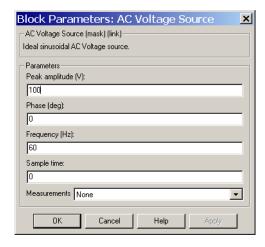
The AC Voltage Source block implements an ideal AC voltage source. The generated voltage *U* is described by the following relationship:

$$\Diamond$$

$$IV = A\sin(\omega t + \phi)$$
 $\omega = 2\pi f$ $\phi = P\frac{\pi}{180}$

Negative values are allowed for amplitude and phase. A 0 frequency specifies a DC voltage source. Negative frequency is not allowed; otherwise Simulink signals an error, and the block displays a question mark in the block icon. You can modify the first three block parameters at any time during the simulation.

Dialog Box and Parameters



Peak amplitude

The peak amplitude of the generated voltage source, in volts (V).

Phase

The phase in degrees (deg).

Frequency

The source frequency in hertz (Hz).

AC Voltage Source

Sample time

The sample period in seconds (s). The default is 0, corresponding to a continuous source.

Measurements

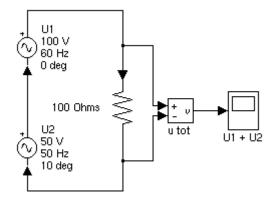
Selects **Voltage** to measure the voltage across the terminals of the AC Voltage Source block.

Place a Multimeter block in your model to display the selected measurements during the simulation. In the **Available Measurements** list box of the Multimeter block, the measurement is identified by a label followed by the block name.

Measurement	Label
Voltage	Usrc:

Example

The psbacvoltage.mdl demo uses two AC Voltage Source blocks at different frequencies connected in series across a resistor. The sum of the two voltages is read by a Voltage Measurement block.



See Also

Controlled Voltage Source, DC Voltage Source, Multimeter

Purpose

Model the dynamics of a three-phase asynchronous machine, also known as an induction machine

Library

Machines

Description

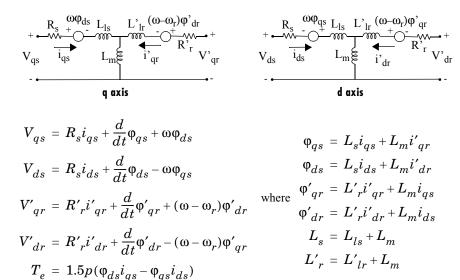




The Asynchronous Machine block operates in either generating or motoring mode. The mode of operation is dictated by the sign of the mechanical torque (positive for motoring, negative for generating). The electrical part of the machine is represented by a fourth-order state-space model and the mechanical part by a second-order system. All electrical variables and parameters are referred to the stator. This is indicated by the prime signs in the machine equations given below. All stator and rotor quantities are in the arbitrary two-axis reference frame (dq frame). The subscripts used are defined as follows:

Subscript	Definition
d	d axis quantity
q	q axis quantity
r	Rotor quantity
s	Stator quantity
1	Leakage inductance
m	Magnetizing inductance

Electrical System



Mechanical System

$$\frac{d}{dt}\omega_m = \frac{1}{2H}(T_e - F\omega_m - T_m)$$

$$\frac{d}{dt}\theta_m = \omega_m$$

The Asynchronous Machine block parameters are defined as follows (all quantities are referred to the stator):

Parameter	Definition
R_s, L_{ls}	Stator resistance and leakage inductance
R'_r, L'_{lr}	Rotor resistance and leakage inductance
L_{m}	Magnetizing inductance

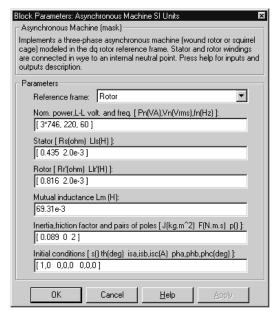
Parameter	Definition (Continued)
L_s, L'_r	Total stator and rotor inductances
$ m V_{qs}, i_{qs}$	q axis stator voltage and current
V'qr, i'qr	q axis rotor voltage and current
V_{ds},i_{ds}	d axis stator voltage and current
V' _{dr} , i' _{dr}	d axis rotor voltage and current
$\phi_{\mathrm{qs}},\phi_{\mathrm{ds}}$	Stator q and d axis fluxes
$\phi'_{ m qr},\phi'_{ m dr}$	Rotor q and d axis fluxes
ω_{m}	Angular velocity of the rotor
$\theta_{ m m}$	Rotor angular position
p	Number of pole pairs
$\omega_{\mathbf{r}}$	Electrical angular velocity $(\omega_m \ x \ p)$
$ heta_{f r}$	Electrical rotor angular position $(\theta_m \ x \ p)$
$T_{ m e}$	Electromagnetic torque
T_{m}	Shaft mechanical torque
J	Combined rotor and load inertia coefficient. Set to infinite to simulate locked rotor.
Н	Combined rotor and load inertia constant. Set to infinite to simulate locked rotor.
F	Combined rotor and load viscous friction coefficient

Parameters and Dialog Boxes

You can choose between two Asynchronous Machine blocks to specify the electrical and mechanical parameters of the model.

Note Depending on the dialog box you choose to use, Power System Blockset automatically converts the parameters you enter into per unit parameters. The Simulink model of the Asynchronous Machine block uses p.u. parameters.

S.I. Units Dialog Box



Rotor Type

Specifies the branching for the rotor windings.

Reference Frame

Specifies the reference frame that is used to convert input voltages (abc reference frame) to the dq reference frame, and output currents (dq reference frame) to the abc reference frame. You can choose among the following reference frame transformations:

- Rotor (Park transformation)
- Stationary (Clarke or αβ transformation)

Synchronous

The following relationships describe the abc-to-dq reference frame transformations applied to the Asynchronous Machine phase-to-phase voltages.

$$\begin{bmatrix} V_{qs} \\ V_{ds} \end{bmatrix} = \frac{1}{3} \begin{bmatrix} 2\cos\theta & \cos\theta + \sqrt{3}\sin\theta \\ 2\sin\theta & \sin\theta - \sqrt{3}\cos\theta \end{bmatrix} \begin{bmatrix} V_{abs} \\ V_{bcs} \end{bmatrix}$$

$$\begin{bmatrix} V'_{qr} \\ V'_{dr} \end{bmatrix} = \frac{1}{3} \begin{bmatrix} 2\cos\beta & \cos\beta + \sqrt{3}\sin\beta \\ 2\sin\beta & \sin\beta - \sqrt{3}\cos\beta \end{bmatrix} \begin{bmatrix} V'_{abr} \\ V'_{bcr} \end{bmatrix}$$

In the preceding equations, θ is the angular position of the reference frame, while $\beta = \theta - \theta_r$ is the difference between the position of the reference frame and the position (electrical) of the rotor. Because the machine windings are connected in a three-wire Y configuration, there is no homopolar (0) component. This also justifies the fact that two line-to-line input voltages are used inside the model instead of three line-to-neutral voltages. The following relationships describe the dq-to-abc reference frame transformations applied to the Asynchronous Machine phase currents.

$$\begin{bmatrix} i_{as} \\ i_{bs} \end{bmatrix} = \begin{bmatrix} \cos\theta & \sin\theta \\ \frac{-\cos\theta + \sqrt{3}\sin\theta}{2} & \frac{-\sqrt{3}\cos\theta - \sin\theta}{2} \end{bmatrix} \begin{bmatrix} i_{qs} \\ i_{ds} \end{bmatrix}$$

$$\begin{bmatrix} i'_{ar} \\ i'_{br} \end{bmatrix} = \begin{bmatrix} \cos\beta & \sin\beta \\ \frac{-\cos\beta + \sqrt{3}\sin\beta}{2} & \frac{-\sqrt{3}\cos\beta - \sin\beta}{2} \end{bmatrix} \begin{bmatrix} i'_{qr} \\ i'_{dr} \end{bmatrix}$$

$$i_{cs} = -i_{as} - i_{bs}$$

$$i'_{cr} = -i'_{ar} - i'_{br}$$

The following table shows the values taken by θ and β in each reference frame (θ_e is the position of the synchronously rotating reference frame).

Reference Frame	θ	β
Rotor	$ heta_{f r}$	0
Stationary	0	$- heta_{\mathbf{r}}$
Synchronous	$\theta_{ m e}$	$ heta_{ m e}$ - $ heta_{ m r}$

The choice of reference frame affects the waveforms of all dq variables. It also affects the simulation speed and in certain cases the accuracy of the results. The following guidelines are suggested in [1]:

- Use the stationary reference frame if the stator voltages are either unbalanced or discontinuous and the rotor voltages are balanced (or 0).
- Use the rotor reference frame if the rotor voltages are either unbalanced or discontinuous and the stator voltages are balanced.
- Use either the stationary or synchronous reference frames if all voltages are balanced and continuous.

Nominal

The nominal apparent power Pn~(VA), rms line-to-line voltage Vn~(V), and frequency fn~(Hz).

Stator

The stator resistance Rs (Ω or p.u.) and leakage inductance Lls (H or p.u.).

Rotor

The rotor resistance $Rr'(\Omega \text{ or p.u.})$ and leakage inductance Llr'(H or p.u.), both referred to the stator.

Magnetizing inductance

The magnetizing inductance Lm (H or p.u.).

Mechanical

For the **SI units** dialog box: the combined machine and load inertia coefficient J (kg.m²), combined viscous friction coefficient F (N.m.s), and pole pairs p.

For the **p.u. units** dialog box: the inertia constant H(s), combined viscous friction coefficient F(p.u.), and pole pairs p.

Initial conditions

Specifies the initial slip s, electrical angle θe (deg), stator current magnitude (A or p.u.), and phase angles (deg):

[slip, th, i_{as}, i_{bs}, i_{cs}, phase_{as}, phase_{bs}, phase_{cs}]

You can also specify optional initial values for the rotor current magnitude (A) or (p.u.), and phase angles (deg):

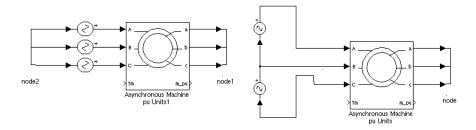
[slip, th, i_{as} , i_{bs} , i_{cs} , ph_{as} , ph_{bs} , ph_{cs} , i_{ar} , i_{br} , i_{cr} , $phase_{ar}$, $phase_{br}$, $phase_{cr}$]

The initial conditions can be computed by the load flow utility in the Powergui block.

Inputs and Outputs

The stator terminals of the Asynchronous Machine block are identified by the A, B, and C letters. The rotor terminals are identified by the a, b, and c letters. Note that the neutral connections of the stator and rotor windings are not available; three-wire Y connections are assumed.

You must be careful when you connect ideal sources to the machine's stator. If you choose to supply the stator via a three-phase Y-connected infinite voltage source, you must use three sources connected in Y. However, if you choose to simulate a delta source connection, you must only use two sources connected in series.



The Simulink input of the block is the mechanical torque at the machine's shaft. When the input is positive, the asynchronous machine behaves as a

motor. When the input is negative, the asynchronous machine behaves as a generator.

The Simulink output of the block is a vector containing 21 variables. They are, in order (refer to the preceding description section, all currents flowing into machine).

Input	Definition
1 to 3	Rotor currents i'ra, i'rb, and i'rc
4 to 9	$i'_{qr}, i'_{dr}, \phi'_{qr}, \phi'_{dr}, v'_{qr},$ and v'_{d}
10 to 12	Stator currents i_{sa},i_{sb} and i_{sc}
13 to 18	$i_{qs}, i_{ds}, \phi_{qs}, \phi_{ds}, v_{qs},$ and v_{ds}
19 to 21	$\omega_m, T_e,$ and θ_m

You can demultiplex these variables by using the Machines Measurement Demux block provided in the Machines library.

Limitations

The Asynchronous Machine block does not include a representation of the effects of stator and rotor iron saturation.

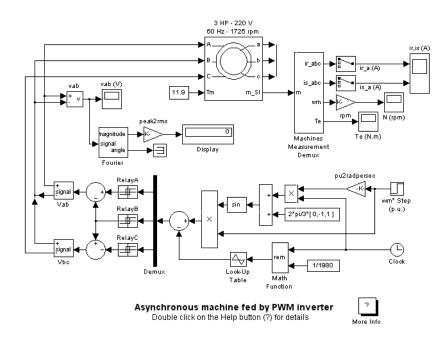
Example

The psbpwm.mdl demo illustrates the use of the Asynchronous Machine block in motoring mode. It consists of an asynchronous machine in an open-loop speed control system.

The machine's rotor is short-circuited, and the stator is fed by a PWM inverter, built with Simulink blocks and interfaced to the Asynchronous Machine block through the Controlled Voltage Source block. The inverter uses sinusoidal pulse-width modulation, which is described in [2]. The base frequency of the sinusoidal reference wave is set at 60 Hz and the triangular carrier wave's frequency is set at 1980 Hz. This corresponds to a frequency modulation factor m_f of 33 (60 Hz x 33 = 1980). It is recommended in [2] that m_f be an odd multiple of three and that the value be as high as possible.

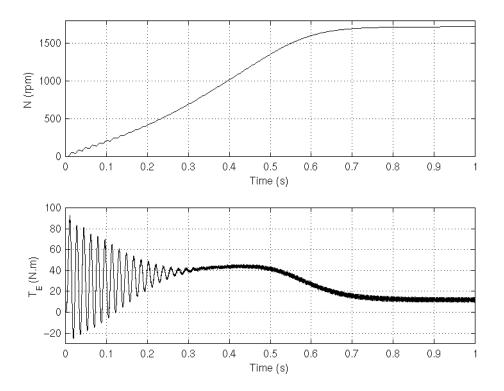
The 3 HP machine is connected to a constant load of nominal value (11.9 N.m). It is started and reaches the set point speed of 1.0 p.u. at t = 0.9 second.

The parameters of the machine are those found in the **SI Units** dialog box above, except for the stator leakage inductance, which is set to twice its normal value. This is done to simulate a smoothing inductor placed between the inverter and the machine. Also, the stationary reference frame was used to obtain the results shown.



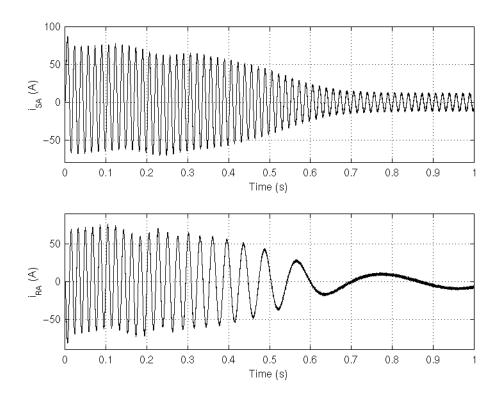
Open the psbpwm demo. Note in the simulation parameters that a small relative tolerance is required because of the high switching rate of the inverter.

Run the simulation and observe the machine's speed and torque.

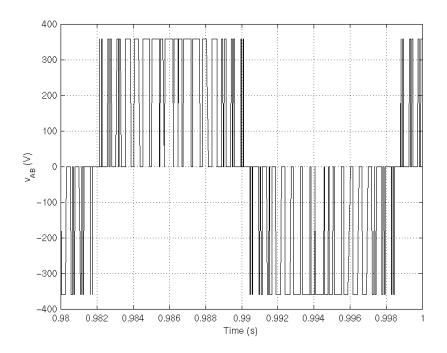


The first graph shows the machine's speed going from 0 to 1725 rpm $(1.0 \ p.u.)$. The second graph shows the electromagnetic torque developed by the machine. Because the stator is fed by a PWM inverter, a noisy torque is observed.

However, this noise is not visible in the speed because it is filtered out by the machine's inertia, but it can also be seen in the stator and rotor currents, which are observed next.



Finally, look at the output of the PWM inverter. Because nothing of interest can be seen at the simulation time scale, the graph concentrates on the last moments of the simulation.



References

[1] Krause, P.C., O. Wasynczuk, and S.D. Sudhoff, Analysis of Electric Machinery, IEEE Press, 1995.

[2] Mohan, N., T.M. Undeland, and W.P. Robbins, *Power Electronics: Converters, Applications, and Design*, John Wiley & Sons, Inc., New York, 1995, Section 8.4.1.

Purpose

Implement a circuit breaker opening at the current zero crossing

Library

Elements

Description



The Breaker block implements a circuit breaker where the opening and closing times can be controlled either from an external Simulink signal (external control mode), or from an internal control timer (internal control mode).

The arc extinction process is simulated by opening the breaker device when the current passes through 0 (first current zero crossing following the transition of the Simulink control input from 1 to 0).

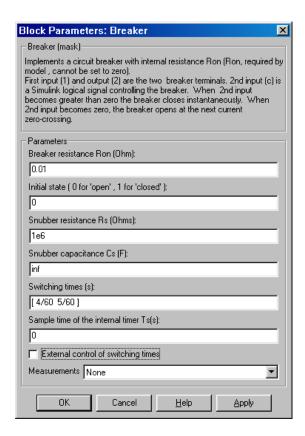
When the breaker is closed it behaves as a resistive circuit. It is represented by a resistance Ron. The Ron value can be set as small as necessary in order to be negligible compared with external components (typical value is $10~\text{m}\Omega$). When the breaker is open it has an infinite resistance.

If the Breaker block is set in external control mode, a Simulink input appears on the block icon. The control signal connected to the Simulink input must be either 0 or 1: 0 to open the breaker, 1 to close it. If the Breaker block is set in internal control mode, the switching times are specified in the dialog box of the block.

If the breaker initial state is set to 1 (closed), Power System Blockset automatically initializes all the states of the linear circuit and the Breaker block initial current so that the simulation starts in steady state.

A series Rs-Cs snubber circuit is included in the model. It can be connected to the circuit breaker.

Dialog Box and Parameters



Breaker resistance Ron

The internal breaker resistance, in ohms (Ω) . The **Breaker resistance Ron** parameter cannot be set to 0.

Initial state

The initial state of the breaker. A closed contact is displayed in the block icon when the **Initial state** parameter is set to 1, and an open contact is displayed when it is set to 0.

Snubber resistance Rs

The snubber resistance, in ohms (Ω) . Set the **Snubber resistance Rs** parameter to inf to eliminate the snubber from the model.

Snubber capacitance Cs

The snubber capacitance, in farads (F). Set the **Snubber capacitance Cs** parameter to 0 to eliminate the snubber, or to inf to get a resistive snubber.

Switching times

Specifies the vector of switching times when using the Breaker block in internal control mode. At each switching time the Breaker block opens or closes depending on its initial state. For example, if the **Initial state** parameter is 0 (open), the breaker closes at the first switching time, opens at the second switching time, and so on. The **Switching times** parameter is not visible in the dialog box if the **External control of switching times** parameter is selected.

Sample time of the internal timer Ts

The sample time Ts, in seconds, of the internal time used to control the opening or closing of the breaker block. The **Sample time of the internal timer Ts** parameter is not visible in the dialog box if the **External control of switching times** parameter is selected.

External control of switching times

If selected, adds a Simulink input to the Breaker block for external control of the switching times of the breaker. The switching times are defined by a logical signal (0 or 1) connected to the Simulink input.

Measurements

Select **Branch voltage** to measure the voltage across the Breaker block terminals.

Select **Branch current** to measure the current flowing through the Breaker block. If the snubber device is connected to the breaker model, the measured current is the one flowing through the breaker contacts only.

Select **Branch voltage and current** to measure the breaker voltage and the breaker current.

Place a Multimeter block in your model to display the selected measurements during the simulation.

In the **Available Measurements** list box of the Multimeter block, the measurement is identified by a label followed by the block name:

Measurement	Label
Branch voltage	Ub:
Branch current	Ib:

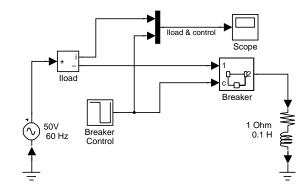
Limitations

When the block is connected in series with an inductor or another current source, you must add the snubber circuit. In most applications you can use a resistive snubber (**Snubber capacitance** parameter set to inf) with a large resistor value (**Snubber resistance** parameter set to 1e6, or so). Because of modeling constraints, the internal breaker inductance Ron cannot be set to 0.

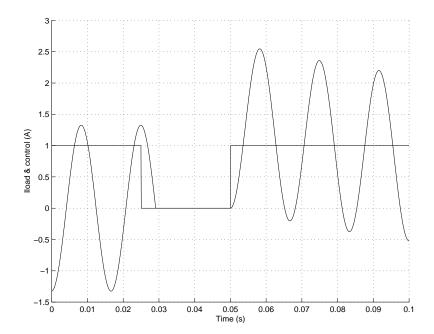
You must use a stiff integration algorithm to simulate circuits with the Breaker block. ode23tb or ode15s with default parameters usually gives the best simulation speed.

Example

The psbbreaker.mdl demo illustrates a circuit breaker connected in series with a series RL circuit on a 60 Hz voltage source. The switching times of the Breaker block are controlled by a Simulink signal. The breaker device is initially closed and an opening order is given at t=1.5 cycles, when current reaches a maximum. The current stops at the next zero crossing, then the breaker is reclosed at a zero crossing of voltage at t=3 cycles.



Simulation produces the following results.



Note that the breaker device opens only when the load current has reached zero, after the opening order.

See Also

3-Phase Breaker, 3-Phase Fault

Controlled Current Source

Purpose

Implement a controlled current source

Library

Electrical Sources

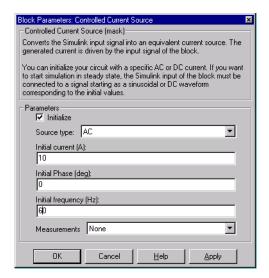
Description



The Controlled Current Source block provides a current source controlled by a Simulink signal. The positive current direction is as shown by the arrow in the block icon. It flows from the negative terminal to the positive terminal.

You can initialize the Controlled Current Source block with a specific AC or DC current. If you want to start the simulation in steady state, the block input must be connected to a signal starting as a sinusoidal or DC waveform corresponding to the initial values.

Dialog Box and Parameters



Initialize

If selected, initializes the Controlled Current Source block with the specified **Initial current**, **Initial phase**, and **Initial frequency** parameters.

Source type

The type of current source. Select **AC** to initialize the Controlled Current Source Block as an AC current source. Select **DC** to initialize the Controlled Current Source block as a DC current.

The **Source type** parameter is not visible in the dialog box if the **Initialize** parameter is not selected.

Initial current

The initial peak current for the initialization of the source, in amperes (A). The **Initial current** parameter is not visible in the dialog box if the **Initialize** parameter is not selected.

Initial phase

The initial phase for the initialization of the source, in degrees. The **Initial phase** parameter is not visible in the dialog box if the **Source type** parameter is set to **DC**.

Initial frequency

The initial frequency for the initialization of the source, in hertz (Hz). The **Initial frequency** parameter is not visible in the dialog box if the **Source type** parameter is set to **DC**.

Measurements

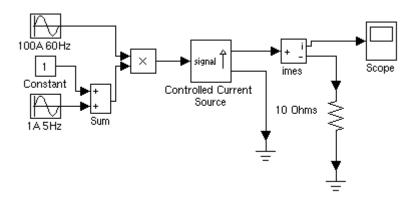
Select **Current** to measure the current flowing through the Controlled Current Source block.

Place a Multimeter block in your model to display the selected measurements during the simulation. In the **Available Measurements** list box of the Multimeter block, the measurement is identified by a label followed by the block name:

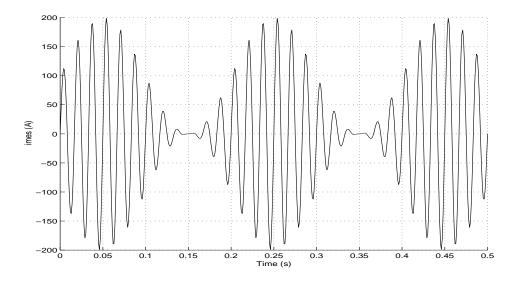
Measurement	Label
Current	Isrc:

Example

The psbcontrolcurr.mdl demo uses a Controlled Current Source to generate a 60 Hz current modulated at 5 Hz.



Simulation produces the following waveforms:



See Also

AC Current Source, Controlled Voltage Source, Multimeter

Purpose Implement a controlled voltage source

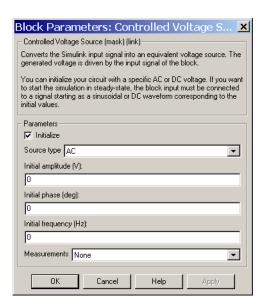
Library Electrical Sources

Description The Controlled Voltage Source block provides a voltage source controlled by a

Simulink signal.

You can initialize the Controlled Voltage Source block with a specific AC or DC voltage. If you want to start the simulation in steady state, the Simulink input must be connected to a signal starting as a sinusoidal or DC waveform corresponding to the initial values.

Dialog Box and Parameters



Initialize

If selected, initializes the Controlled Voltage Source block with the specified **Initial voltage**, **Initial phase**, and **Initial frequency** parameters.

Source type

The type of voltage source. Select **AC** to initialize the Controlled Voltage Source block with an AC voltage source. Select **DC** to initialize the Controlled Voltage Source Block with a DC voltage.

The **Source type** parameter is not available in the dialog box if the **Initialize** parameter is not selected.

Initial voltage

The initial voltage for the initialization of the source, in amperes (A). The **Initial voltage** parameter is not available in the dialog box if the **Initialize** parameter is not selected.

Initial phase

The initial phase for the initialization of the source, in degrees. The **Initial phase** parameter is not available in the dialog box if the **Source type** parameter is set to **DC**.

Initial frequency

The initial frequency for the initialization of the source, in hertz (Hz). The **Initial frequency** parameter is not available in the dialog box if the **Source type** parameter is set to **DC**.

Measurements

Select **Voltage** to measure the voltage across the terminals of the Controlled Voltage Source block.

Place a Multimeter block in your model to display the selected measurements during the simulation. In the **Available Measurements** list box of the Multimeter block, the measurement is identified by a label followed by the block name:

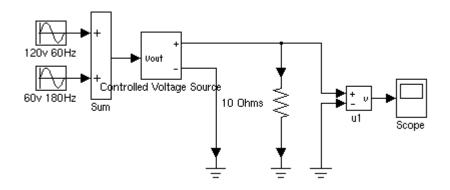
Measurement	Label
Voltage	Usrc:

Example

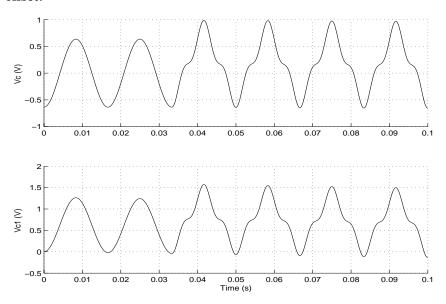
The psbcontrolvolt.mdl demo uses Controlled Voltage Source blocks to generate a 60 Hz sinusoidal voltage containing a third harmonic. One Controlled Voltage Source block is initialized as a 120 V AC voltage source with

an initial frequency of 60 Hz and initial phase set to 0. The second Controlled Voltage Source block is not initialized.

At $t = 0.0333 \, s$ a 100 V-180 Hz sinusoidal signal is added to the 120 V Simulink signal. The resulting capacitor voltages are compared on a Scope block.



The Vc voltage starts in steady state, whereas the Vc1 voltage contains a DC offset.



See Also

AC Current Source, Controlled Current Source, Multimeter

Purpose

Measure a current in a circuit

Library

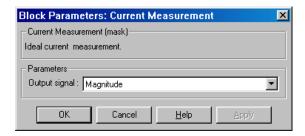
Measurements

Description

The Current Measurement block is used to measure the instantaneous current flowing in any electrical block or connection line. The Simulink output provides a Simulink signal that can be used by other Simulink blocks.



Dialog Box and Parameters



Output signal

Specifies the format of the output signal when the block is used in a phasor simulation. The **Output signal** parameter is disabled when the block is not used in a phasor simulation. The phasor simulation is activated by a Powergui block placed in the model.

Set to Complex to output the measured current as a complex value. The output is a complex signal.

Set to Real-Imag to output the real and imaginary parts of the measured current. The output is a vector of two elements.

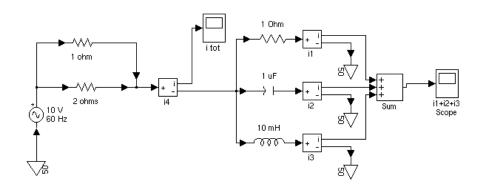
Set to Magnitude-Angle to output the magnitude and angle of the measured current. The output is a vector of two elements.

Set to Magnitude to output the magnitude of the measured current. The output is a scalar value.

Example

The psbcurrmeasure.mdl demo uses four Current Measurement blocks to read currents in different branches of a circuit. The two scopes display the same current.

Current Measurement



See Also

Powergui, Three-Phase V-I Measurement, Voltage Measurement

Purpose

Implement a separately excited DC machine

Library

Machines

Description

This block implements a separately excited DC machine. An access is provided to the field terminals (F+, F-) so that the machine model can be used as a shunt-connected or a series-connected DC machine.



The armature circuit (A+, A-) consist of an inductor La and resistor Ra in series with a counter-electromotive force (CEMF) E.

The CEMF is proportional to the machine speed.

$$E = K_E \omega$$

 K_E is the voltage constant and ω is the machine speed.

In a separately excited DC machine model, the voltage constant $K_{\!E}$ is proportional to the field current I_f

$$K_E = L_{af} I_f$$

where L_{af} is the field-armature mutual inductance.

The electromechanical torque developed by the DC machine is proportional to the armature current $\mathbf{I}_{\mathbf{a}}$

$$T_e = K_T I_a$$

where K_{T} is the torque constant. The sign convention for T_{e} is

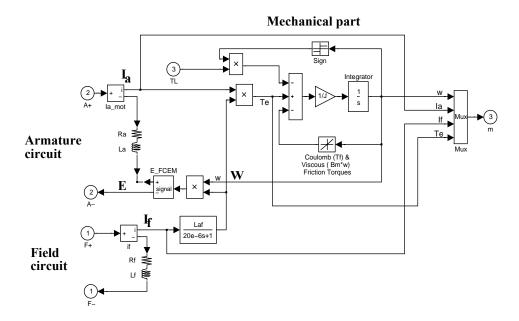
 $T_{\rho} > 0$: Generator mode

 $T_{\rho} < 0$: Motor mode

The torque constant is equal to the voltage constant.

$$K_T = K_E$$

The armature circuit is connected between the A+ and A- ports of the DC Machine block. It is represented by a Series RLC Branch block in series with a Controlled Voltage Source and a Current Measurement block.



The field circuit is represented by an RL circuit. It is connected between the F+ and F- ports of the DC Machine block.

The mechanical part computes the speed of the DC machine from the net torque applied to the rotor. The speed is used to implement the CEMF voltage E of the armature circuit.

The mechanical part is represented by Simulink blocks that implement the equation

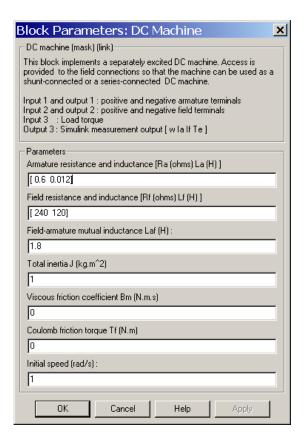
$$T_e = J \frac{d\omega}{dt} + B\omega + \operatorname{sgn} T_L$$

Measurements

Four internal signals are multiplexed on the Simulink measurement output vector returning

- Rotor speed in rad/s
- Armature current in A
- Field current in A
- Electromechanical torque in N.m

Dialog Box



Armature resistance and inductance [Ra (ohms) La (H)]

The armature resistance Ra, in ohms, and the armature inductance La, in henries.

Field resistance and inductance [Rf (ohms) Lf (H)]

The field resistance Rf, in ohms, and the field inductance Lf, in henries.

Field-armature mutual inductance Laf (H):

The field armature mutual inductance, in henries.

Total inertia J (kg.m²)

The total inertia of the DC machine, in kg.m².

Viscous friction coefficient Bm (N.m.s)

The total friction coefficient of the DC machine, in N.m.s.

Coulomb friction torque Tf (N.m)

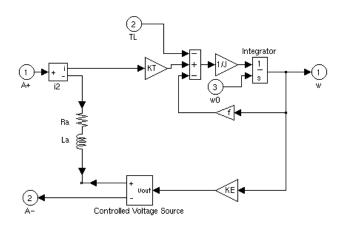
The total Coulomb friction torque constant of the DC machine, in N.m.

Initial speed (rad/s)

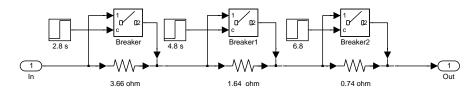
Specifies an initial speed for the DC machine, in rad/s, in order to start the simulation with a specific initial speed. To start the simulation in steady state, the initial value of the input torque signal TL must be proportional to the initial speed.

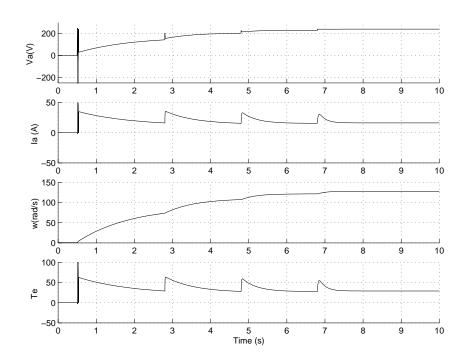
Example

The psbdcmotor.mdl demoillustrates the starting of a 5 HP 240 V DC machine with a three-step resistance starter.



The Motor Starter subsystem is





Reference

Reference Analysis of Electric Machinery, Krause et al., pp. 89-92.

See Also

Asynchronous Machine, Synchronous Machine

DC Voltage Source

Purpose

Implement a DC voltage source

Library

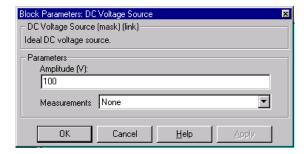
Electrical Sources

Description

The DC Voltage Source block implements an ideal DC voltage source. The positive terminal is represented by a plus sign on one port. You can modify the voltage at any time during the simulation.



Dialog Box and Parameters



Amplitude

The amplitude of the source, in volts (V).

Measurements

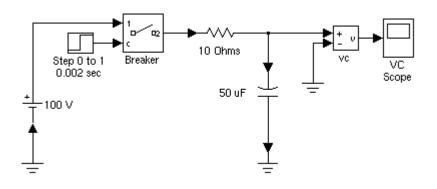
Select **Voltage** to measure the voltage across the terminals of the DC Voltage Source block.

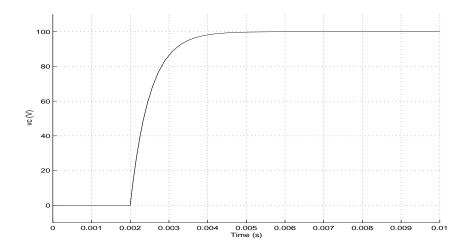
Place a Multimeter block in your model to display the selected measurements during the simulation. In the **Available Measurements** list box of the Multimeter block, the measurement is identified by a label followed by the block name:

Measurement	Label
Voltage	Usrc:

Example

The ${\tt psbdcvoltage.mdl}$ demo illustrates the simulation of the transient response of a first-order RC circuit.





See Also

AC Voltage Source, Controlled Voltage Source

Diode

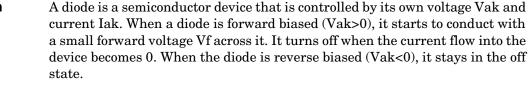
Purpose

Implement a diode model

Library

Power Electronics

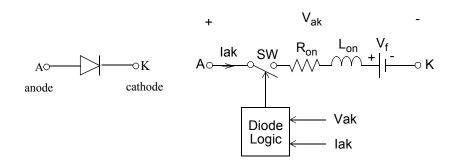
Description





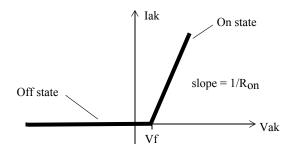
The Diode block is simulated as a resistor, an inductor, and a DC voltage source connected in series with a switch. The switch is controlled by the voltage Vak and current Iak.



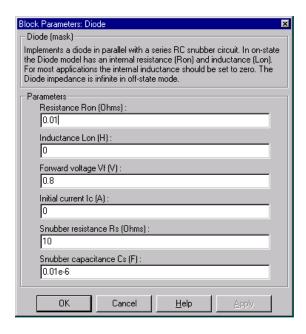


The Diode block also contains a series Rs-Cs snubber circuit that can be connected in parallel with the diode device (between nodes A and K).

The static VI characteristic of this model is shown in the following figure.



Dialog Box and Parameters



Resistance Ron

The diode internal resistance Ron, in ohms (Ω) . The **Resistance Ron** parameter cannot be set to 0 when the **Inductance Lon** parameter is set to 0.

Inductance Lon

The diode internal inductance Lon, in henries (H). The **Inductance Lon** parameter cannot be set to 0 when the **Resistance Ron** parameter is set to 0.

Forward voltage Vf

The forward voltage of the diode device, in volts (V).

Initial current Ic

Specifies an initial current flowing in the diode device. It is usually set to 0 in order to start the simulation with the diode device blocked. If the **Initial Current IC** parameter is set to a value greater than 0, the steady state calculation of Power System Blockset considers the initial status of the diode as closed.

Initializing all states of a power electronic converter is a complex task. Therefore, this option is useful only with simple circuits.

Snubber resistance Rs

The snubber resistance, in ohms (Ω) . Set the **Snubber resistance Rs** parameter to inf to eliminate the snubber from the model.

Snubber capacitance Cs

The snubber capacitance in farads (F). Set the **Snubber capacitance Cs** parameter to 0 to eliminate the snubber, or to inf to get a resistive snubber.

Show measurement port

If selected, adds a Simulink output to the block returning the diode current and voltage.

Inputs and Outputs

The anode of the diode is identified with the letter a and the cathode is identified by the letter k. The Simulink output is a measurement output vector [Iak Vak] returning the diode current and voltage.

Assumptions and Limitations

The Diode block implements a macromodel of a diode device. It does not take into account either the geometry of the device or the complex physical processes underlying the state change [1]. The leakage current in the blocking state and the reverse-recovery (negative) current are not considered. In most circuits, the reverse current does not affect converter or other device characteristics.

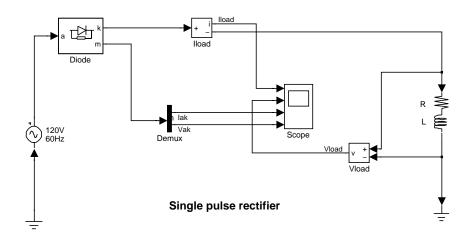
Depending on the value of the inductance Lon, the diode is modeled either as a current source (Lon > 0) or as a variable topology circuit (Lon = 0). The Diode block cannot be connected in series with an inductor, a current source, or an open circuit, unless its snubber circuit is in use. See the "Advanced Topics" chapter for more details on this topic.

You must use a stiff integrator algorithm to simulate circuits containing diodes. ode23tb or ode15s with default parameters usually gives the best simulation speed.

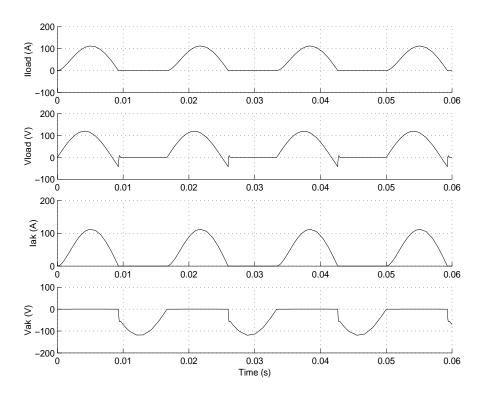
The inductance Lon is forced to 0 if you choose to discretize your circuit.

Example

The psbdiode.mdl demo illustrates a single pulse rectifier consisting of a Diode block, an RL load, and an AC Voltage source block, with the following parameters.



Simulation produces the following results.



References

[1] Rajagopalan, V., Computer-Aided Analysis of Power Electronic Systems, Marcel Dekker, Inc., New York, 1987.

[2] Mohan, N., T.M. Undeland, and W.P. Robbins, *Power Electronics: Converters, Applications, and Design*, John Wiley & Sons, Inc., New York, 1995.

See Also

Thyristor, Universal Bridge

Discrete System

Purpose

Discretize the state-space model of a circuit

Library

powerlib

Description

The Discrete System block is used, in previous versions of Power System Blockset, to discretize the state-space model of an electrical model. Discrete time models are used for the linear elements as well as for the nonlinear blocks of the Elements, Machines, and Power Electronics libraries of **powerlib**.

Discrete system Ts=5e-005

> Continuous system

Note This block is now obsolete. Use the Powergui block to replace this block.

See Also

Powergui

Purpose

Implement an N-phase distributed parameter transmission line model with lumped losses

Library

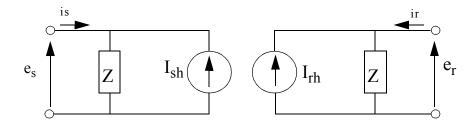
Elements

Description



The Distributed Parameter Line block implements an N-phase distributed parameter line model with lumped losses. The model is based on the Bergeron's traveling wave method used by the Electromagnetic Transient Program (EMTP)[1]. In this model, the lossless distributed LC line is characterized by two values (for a single-phase line): the surge impedance $Zc = \sqrt{L/C}$ and the phase velocity $v = 1/\sqrt{LC}$.

The model uses the fact that the quantity e+Zi, where e is line voltage and i is line current, entering one end of the line must arrive unchanged at the other end after a transport delay of $\tau=d/v$, where d is the line length. By lumping R/4 at both ends of the line and R/2 in the middle and using the current injection method of Power System Blockset, the following two-port model is derived.



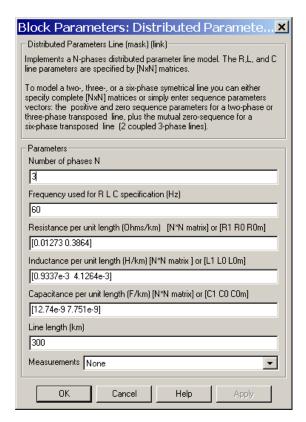
$$\begin{split} I_{sh}(t) &= \bigg(\frac{1+h}{2}\bigg)\bigg[\frac{1}{Z}e_r(t-\tau) + hi_r(t-\tau)\bigg] + \bigg(\frac{1-h}{2}\bigg)\bigg[\frac{1}{Z}e_s(t-\tau) + hi_s(t-\tau)\bigg] \\ I_{rh}(t) &= \bigg(\frac{1+h}{2}\bigg)\bigg[\frac{1}{Z}e_s(t-\tau) + hi_s(t-\tau)\bigg] + \bigg(\frac{1-h}{2}\bigg)\bigg[\frac{1}{Z}e_r(t-\tau) + hi_r(t-\tau)\bigg] \end{split}$$

where
$$Z=Z_C+rac{R}{4}$$
 $h=rac{Z_C-rac{R}{4}}{Z_C+rac{R}{4}}$ $Z_C=\sqrt{rac{L}{C}}$ $au=d\sqrt{LC}$

For multiphase line models, modal transformation is used to convert line quantities from phase values (line currents and voltages) into modal values independent of each other. The previous calculations are made in the modal domain before being converted back to phase values.

In comparison to the PI sections line model, the distributed line represents wave propagation phenomena and line end reflections with much better accuracy. See the comparison between the two models in the Example section.

Dialog Box and Parameters



Number of phases N

Specifies the number of phases, N, of the model. The block icon dynamically changes according to the number of phases that you specify. When you

apply the parameters or close the dialog box, the number of inputs and outputs is updated.

Frequency used for RLC specifications

Specifies the frequency used to compute the modal resistance R, inductance L, and capacitance C matrices of the line model.

Resistance per unit length

The resistance R per unit length, as an N-by-N matrix in ohms/km $(\Omega/\kappa\mu)$.

For a symmetrical line, you can either specify the N-by-N matrix or the sequence parameters. For a two-phase or three-phase continuously transposed line, you can enter the positive and zero-sequence resistances [R1 R0]. For a symmetrical six-phase line you can enter the sequence parameters plus the zero-sequence mutual resistance [R1 R0 R0m].

For unsymmetrical lines, you must specify the complete N-by-N resistance matrix.

Inductance per unit length

The inductance L per unit length, as an N-by-N matrix in henries/km (H/km).

For a symmetrical line, you can either specify the N-by-N matrix or the sequence parameters. For a two-phase or three-phase continuously transposed line, you can enter the positive and zero-sequence inductances [L1 L0]. For a symmetrical six-phase line you can enter the sequence parameters plus the zero-sequence mutual inductance [L1 L0 L0m].

For unsymmetrical lines, you must specify the complete N-by-N inductance matrix.

Capacitance per unit length

The capacitance C per unit length, as an N-by-N matrix in farads/km (F/km).

For a symmetrical line, you can either specify the N-by-N matrix or the sequence parameters. For a two-phase or three-phase continuously transposed line, you can enter the positive and zero-sequence capacitances [C1 C0]. For a symmetrical six-phase line you can enter the sequence parameters plus the zero-sequence mutual capacitance [C1 C0 C0m].

For unsymmetrical lines, you must specify the complete N-by-N capacitance matrix.

Line length

The line length, in km.

Measurements

Select **Phase-to-ground voltages** to measure the sending end and receiving end voltages for each phase of the line model.

Place a Multimeter block in your model to display the selected measurements during the simulation.

In the **Available Measurements** list box of the Multimeter block, the measurement is identified by a label followed by the block name:

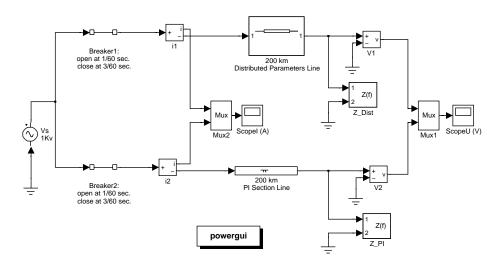
Measurement	Label
Phase-to-ground voltages, sending end	Us_ph1_gnd:, Us_ph2_gnd:, Us_ph3_gnd:, etc.
Phase-to-ground voltages, receiving end	<pre>Ur_ph1_gnd:, Ur_ph2_gnd:, Ur_ph3_gnd:, etc.</pre>

Limitations

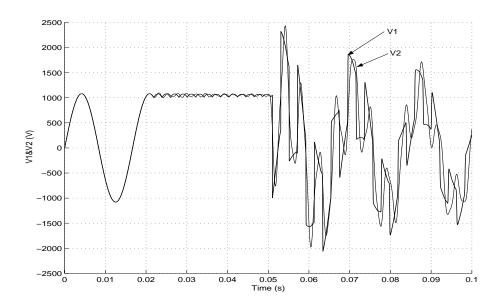
This model does not represent accurately the frequency dependence of RLC parameters of real power lines. Indeed, because of the skin effects in the conductors and ground, the R and L matrices exhibit strong frequency dependence, causing an attenuation of the high frequencies.

Example

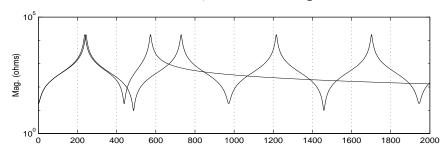
The psbmonophaseline.mdl demo illustrates a 200 km line connected on a 1 kV, 60 Hz infinite source. The line is deenergized and then reenergized after 2 cycles. The simulation is performed simultaneously with the Distributed Parameter Line block and with the PI Section Line block.

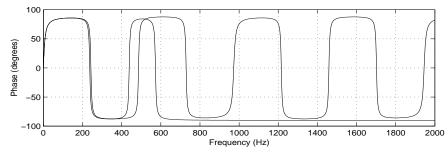


The receiving end voltage obtained with the Distributed Parameter Line block is compared with the one obtained with the PI Section Line block (two sections).



Open the **powergui**. Click the **Impedance vs Frequency Measurement** button. A new window appears, listing the two Impedance Measurement blocks connected to your circuit. Set the parameters of **Impedance vs Frequency Measurement** to compute impedance in the 0:2000 Hz frequency range, select the two measurements in the list, then click the **Update** button.





The distributed parameter line shows a succession of poles and 0s equally spaced, every 486 Hz. The first pole occurs at 243 Hz, corresponding to frequency f = 1/(4 * T) where

 $T = traveling \; time = l \; *sqrt(L*C) = 200 * sqrt(2.137e-3*12.37e-9) = 1.028 \; ms$

The PI section line only shows two poles because it consists of two PI sections. Impedance comparison shows that a two-section PI line gives a good approximation of the distributed line for the 0 to 350 Hz frequency range.

References

[1] Dommel, H, "Digital Computer Solution of Electromagnetic Transients in Single and Multiple Networks," *IEEE Transactions on Power Apparatus and Systems*, Vol. PAS-88, No. 4, April, 1969.

See Also PI Section Line

dq0_to_abc Transformation

Purpose

Perform a Park transformation from the dq0 reference frame to the abc reference frame

Library

Extras/Measurements

Description



The dq0_to_abc Transformation block performs the reverse of the so-called Park transformation, which is commonly used in three-phase electric machine models. It transforms three quantities (direct axis, quadratic axis, and zero-sequence components) expressed in a two-axis reference frame back to phase quantities. The following transformation is used:

$$\begin{split} &V_{a} = V_{d}\sin(\omega t) + V_{q}\cos(\omega t) + V_{0} \\ &V_{b} = V_{d}\sin(\omega t - 2\pi/3) + V_{q}\cos(\omega t - 2\pi/3) + V_{0} \\ &V_{c} = V_{d}\sin(\omega t + 2\pi/3) + V_{q}\cos(\omega t + 2\pi/3) + V_{0} \end{split}$$

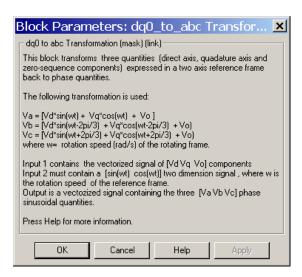
where

 ω = rotation speed (rad/s) of the rotating frame

The transformation is the same for the case of a three-phase current; you simply replace the V_a , V_b , V_c , V_d , V_q , and V_0 variables with the I_a , I_b , I_c , I_d , I_q , and I_0 variables.

The dq0_to_abc Transformation block is used in the model of the Synchronous Machine block where the stator quantities are referred to the rotor. The Park transformation then eliminates time-varying inductances by referring the stator and rotor quantities to a fixed or rotating reference frame. The I_d and I_q currents represent the two DC currents flowing in the two equivalent rotor windings (d winding on the same axis as the field winding, and q winding in quadratic) producing the same flux as the stator $I_a,\,I_b,\,$ and I_c currents.

Dialog Box



Inputs and Outputs

dq0

Connect to the first input a vectorized signal containing the sequence components [d q 0] to be converted.

sin cos

Connect to the second input a vectorized signal containing the $[\sin(\omega t)\cos(\omega t)]$ values, where ω is the rotation speed of the reference frame.

abc

The output is a vectorized signal containing the three-phase sinusoidal quantities [phase A phase B phase C].

Example

See the demo of the abc_to_dq0 Transformation block for an example using the $dq0_to_abc$ Transformation block.

See Also

abc_to_dq0 Transformation

Excitation System

Purpose

Provide an excitation system for the synchronous machine and regulate its terminal voltage in generating mode

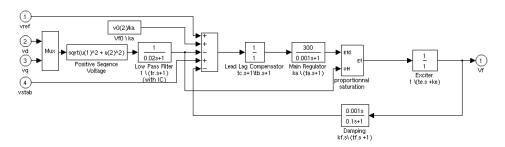
Library

Machines

Description



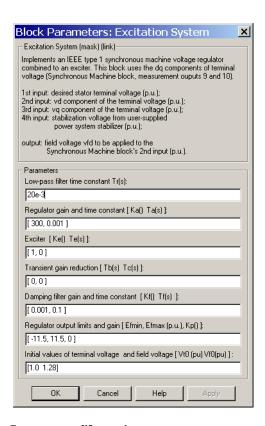
The Excitation System block is a Simulink system implementing a DC exciter described in [1], without the exciter's saturation function. The basic elements that form the Excitation System block are the voltage regulator and the exciter.



The exciter is represented by the following transfer function between the exciter voltage Vfd and the regulator's output ef:

$$\frac{V_{fd}}{ef} = \frac{1}{Ke + sTe}$$

Dialog Box and Parameters



Low-pass filter time constant

The time constant Tr, in seconds (s), of the first-order system that represents the stator terminal voltage transducer.

Regulator

The gain Ka and time constant Ta, in seconds (s), of the first-order system representing the main regulator.

Exciter

The gain Ke and time constant Te, in seconds (s), of the first-order system representing the exciter.

Excitation System

Transient gain reduction time constants

The time constants Tb, in seconds (s), and Tc, in seconds (s), of the first-order system representing a lead-lag compensator.

Damping filter

The gain Kf and time constant Tf, in seconds (s), of the first-order system representing a derivative feedback.

Regulator output limits

Limits Efmin and Efmax are imposed on the output of the voltage regulator. The upper limit can be constant and equal to Efmax, or variable and equal to the rectified stator terminal voltage Vtf times a proportional gain Kp. If Kp is set to 0, the former applies. If Kp is set to a positive value, the latter applies.

Initial conditions

The initial values of terminal voltage Vt0 (p.u.) and field voltage Vf0 (p.u.). When set correctly, they allow you to start the simulation in steady state. Initial terminal voltage should normally be set to 1 p.u. Initial field voltage can be computed by the load flow utility of the Powergui block.

Example

See the Hydraulic Turbine and Governor block.

Inputs and Outputs

The first input of the block is the desired value of the stator terminal voltage. The following two inputs are the \boldsymbol{v}_q and \boldsymbol{v}_d components of the terminal voltage. The fourth input can be used to provide additional stabilization of power system oscillations. All inputs are in p.u. The output of the block is the field voltage Vf for the Synchronous Machine block (p.u.).

References

[1] "Recommended Practice for Excitation System Models for Power System Stability Studies," *IEEE Standard 421.5-1992*, August, 1992.

See Also

Hydraulic Turbine and Governor, Steam Turbine and Governor, Synchronous Machine

Purpose

Perform a Fourier analysis of a signal

Library

Extras/Measurements

Description



The Fourier block performs a Fourier analysis of the input signal over a running window of one cycle of the fundamental frequency of the signal. The Fourier block can be programmed to calculate the magnitude and phase of the DC component, the fundamental, or any harmonic component of the input signal.

Recall that a signal f(t) can be expressed by a Fourier series of the form

$$f(t) = \frac{a_0}{2} + \sum_{n=1}^{\infty} a_n \cos(n\omega t) + b_n \sin(n\omega t)$$

where n represents the rank of the harmonics (n = 1 corresponds to the fundamental component). The magnitude and phase of the selected harmonic component are calculated by the following equations:

$$|H_n| = \sqrt{a_n^2 + b_n^2}$$
 $\angle H_n = \operatorname{atan}\left(\frac{b_n}{a_n}\right)$

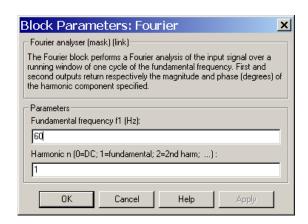
where

$$a_n = \frac{2}{T} \int_{t}^{(t+T)} f(t) \cos(n\omega t) dt$$

$$b_n = \frac{2}{T} \int_{t}^{(t+T)} f(t) \sin(n\omega t) dt$$

$$T = \frac{1}{f_1}$$
 f_1 : Fundamental frequency

Dialog Box and Parameters



Fundamental frequency f1 (Hz)

The fundamental frequency, in hertz, of the input signal.

Harmonic n (0 = DC; 1 = fundamental; 2 = 2nd harm;...)

Specify the harmonic component you want to perform the Fourier analysis. Enter 0 if you want to analyze the DC component. Enter 1 if you want to analyze the fundamental frequency, or enter a number corresponding to the desired harmonic.

Inputs and Outputs

signal

Connect to the first input the input signal to be analyzed. Typical input signals are voltages or currents measured by Current Measurement blocks or Voltage Measurement blocks.

magnitude

The first output returns the magnitude of the harmonic component specified, in the same units as the input signal.

phase

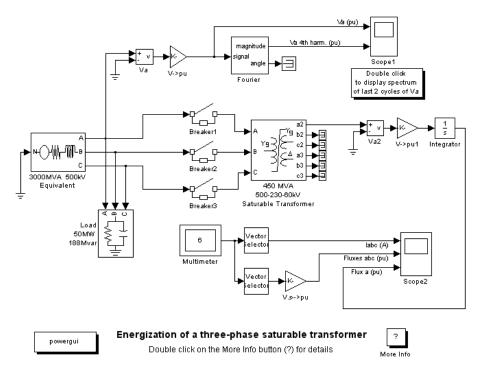
The second output returns the phase, in degrees, of the harmonic component specified.

Example

The psbtransfosat.mdl demo shows the energization of a 450 MVA three-phase transformer on a 500 kV network. The power system is simulated

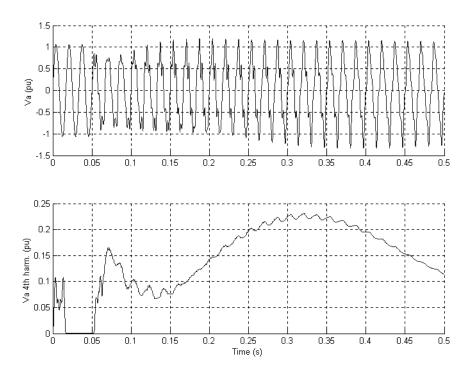
by an equivalent circuit consisting of an inductive source having a short-circuit power of 3000 MVA and a parallel RC load.

The load capacitance is set to produce a resonance at 240 Hz (fourth harmonic). A Fourier block is used to measure the fourth harmonic content of phase A of the primary voltage.



The Fourier block measures a high level of the fourth harmonic in the voltage (on the second trace of Scope1) because of the fourth harmonic content of the current injected into the network resonating at that particular frequency (240 Hz).

Fourier



Purpose

Implement a generic power system stabilizer for the synchronous machine

Library

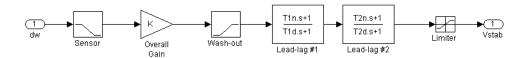
Machines

Description



The Generic Power System Stabilizer (PSS) block can be used to add damping to the rotor oscillations of the synchronous machine by controlling its excitation. The disturbances occurring in a power system induce electromechanical oscillations of the electrical generators. These oscillations, also called power swings, must be effectively damped to maintain the system stability. The output signal of the PSS is used as an additional input (vstab) to the Excitation System block. The PSS input signal can be either the machine speed deviation, dw, or its acceleration power, Pa = Pm – Peo (difference between the mechanical power and the electrical power).

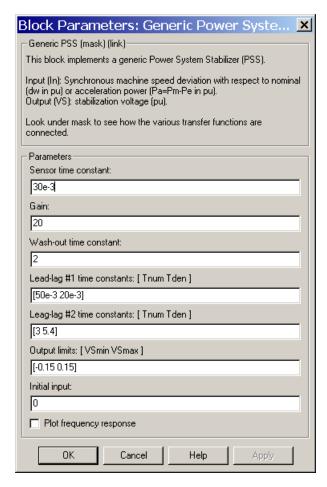
The Generic Power System Stabilizer is modeled by the following nonlinear system:



To ensure a robust damping, the PSS should provide a moderate phase advance at frequencies of interest in order to compensate for the inherent lag between the field excitation and the electrical torque induced by the PSS action.

The model consists of a low pass filter, a general gain, a wash-out high pass filter, a phase-compensation system, and an output limiter. The general gain K determines the amount of damping produced by the stabilizer. The Wash-out high pass filter eliminates low frequencies that are present in the dw signal and allows the PSS to respond only to speed changes. The phase-compensation system is represented by a cascade of two first-order lead-lag transfer functions used to compensate the phase lag between the excitation voltage and the electrical torque of the synchronous machine.

Dialog Box



Sensor time constant

The time constant, in seconds (s), of the first order low pass filter used to filter the block's input signal.

Gain

The overall gain K of the generic power system stabilizer.

Wash-out time constant

The time constant, in seconds (s), of the first order high pass filter used by the wash-out system of the model.

Lead-lag #1 time constants: [Tnum Tden]

The numerator time constant T1n and denominator time constant T1d, in seconds (s), of the first lead-lag transfer function.

Lead-lag #2 time constants: [Tnum Tden]

The numerator time constant T2n and denominator time constant T2d, in seconds (s), of the second lead-lag transfer function.

Output limits: [Vsmin Vsmax]

The limits VSmin and VSmax, in p.u., imposed on the output of the stabilizer.

Initial input:

The initial DC voltage, in volts, of the block's input signal. Specification of this parameter is required to initialize all states and start the simulation in steady state with vstab set to zero.

Plot frequency response

If selected, a plot of the frequency response of the stabilizer is displayed when you click the **Apply** button.

Magnitude in dB

If selected, the magnitude of the frequency response is plotted in dB. The **Magnitude in dB** parameter is not visible in the dialog box if the **Plot frequency response** is not selected.

Frequency range (Hz)

Specify the frequency range used to plot the frequency response of the stabilizer. The **Frequency range** (**Hz**) parameter is not visible in the dialog box if the **Plot frequency response** is not selected.

Inputs and Outputs

dw

Two types of signals can be used at the input dw:

• The synchronous machine speed deviation dw signal (in p.u.)

 The synchronous machine acceleration power Pa = Pm – Peo (difference between the machine mechanical power and output electrical power (in p.u.))

Vstab

The output is the stabilization voltage (in p.u.) to connect to the Vstab input of the Excitation System block used to control the terminal voltage of the synchronous machine.

Example See the help text of the psbPSS demonstration file.

Reference Kundur, P., *Power System Stability and Control*, McGraw-Hill, 1994, section

12.5.

See Also Multiband Power System Stabilizer

Purpose

Provide a connection to the ground

Library

Connectors

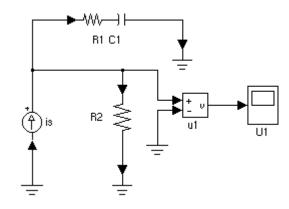
Description

The Ground block implements a connection to the ground. For drawing ease, two types of Ground blocks are provided: one block with an input and one block with an output.

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Example

The psbground.mdl demo shows an application of both types of Ground blocks.



See Also

Neutral

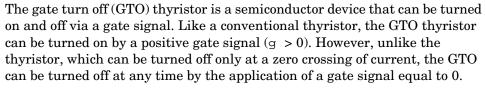
Purpose

Implement a gate turn off (GTO) thyristor model

Library

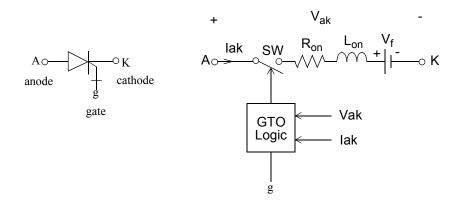
Power Electronics

Description



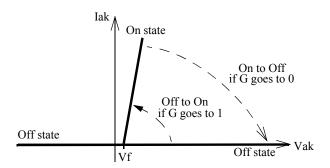
The GTO thyristor is simulated as a resistor Ron, an inductor Lon, and a DC voltage source Vf connected in series with a switch. The switch is controlled by a logical signal depending on the voltage Vak, current Iak, and the gate signal g.



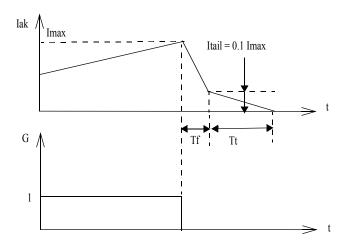


The Vf, Ron, and Lon parameters are the forward voltage drop while in conduction, the forward conducting resistance, and the inductance of the device. The GTO block also contains a series Rs-Cs snubber circuit that can be connected in parallel with the GTO device (between terminal ports A and K).

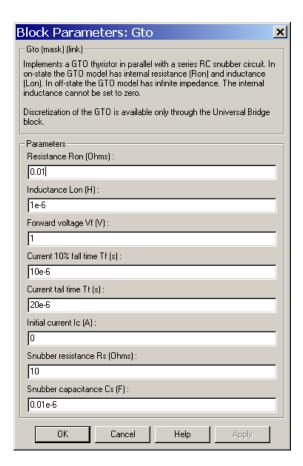
The GTO thyristor turns on when the anode-cathode voltage is greater than Vf and a positive pulse signal is present at the gate input (g > 0). When the gate signal is set to 0, the GTO thyristor starts to block but its current does not stop instantaneously.



Because the current extinction process of a GTO thyristor contributes significantly to the turnoff losses, the turnoff characteristic is built into the model. The current decrease is approximated by two segments. When the gate signal becomes 0, the current Iak first decreases from the value Imax (value of Iak when the GTO thyristor starts to open) to Imax/10, during the fall time (Tf), and then from Imax/10 to 0 during the tail time (Tt). The GTO thyristor turns off when the current Iak becomes 0. The latching and holding currents are not considered.



Dialog Box and Parameters



Resistance Ron

The internal resistance Ron, in ohms (Ω) .

Inductance Lon

The internal inductance Lon, in henries (H). The **Inductance Lon** parameter cannot be set to 0.

Forward voltage Vf

The forward voltage of the GTO thyristor device, in volts (V).

Current 10% fall time

The current fall time Tf, in seconds (s).

Current tail time

The current tail time Tt, in seconds (s).

Initial current Ic

You can specify an initial current flowing in the GTO thyristor. It is usually set to 0 in order to start the simulation with the device blocked.

If the **Initial Current IC** parameter is set to a value greater than 0, the steady state calculation of Power System Blockset considers the initial status of the GTO as closed. Initializing all states of a power electronic converter is a complex task. Therefore, this option is useful only with simple circuits.

Snubber resistance Rs

The snubber resistance, in ohms (Ω) . Set the **Snubber resistance Rs** parameter to inf to eliminate the snubber from the model.

Snubber capacitance Cs

The snubber capacitance, in farads (F). Set the **Snubber capacitance Cs** parameter to 0 to eliminate the snubber, or to inf to get a resistive snubber.

Show measurement port

If selected, add a Simulink output to the block returning the GTO current and voltage.

Inputs and Outputs

The input port (g) is a Simulink signal applied to the gate of the GTO thyristor. The output port (m) is a Simulink measurement vector [Iak Vak] returning the GTO thyristor current and voltage.

Assumptions and Limitations

The GTO block implements a macromodel of a real GTO thyristor. It does not take into account either the geometry of the device or the underlying physical processes of the device [1].

The GTO block requires a continuous application of the gate signal (g > 0) in order to be in the on state (with Iak > 0). The latching current and the holding current are not considered. The critical value of the derivative of the reapplied anode-cathode voltage is not considered.

The GTO block is modeled as a current source. It cannot be connected in series with an inductor, a current source, or an open circuit, unless its snubber circuit is in use. In order to avoid an algebraic loop, you cannot set the inductance Lon to 0.

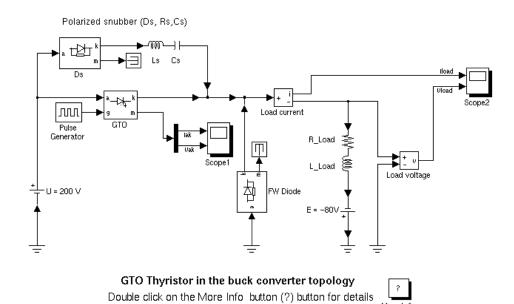
Each GTO block adds an extra state to the electrical circuit model. See the "Advanced Topics" chapter for more details on this topic.

You must use a stiff integrator algorithm to simulate circuits containing GTO blocks. ode23tb or ode15s with default parameters usually gives the best simulation speed.

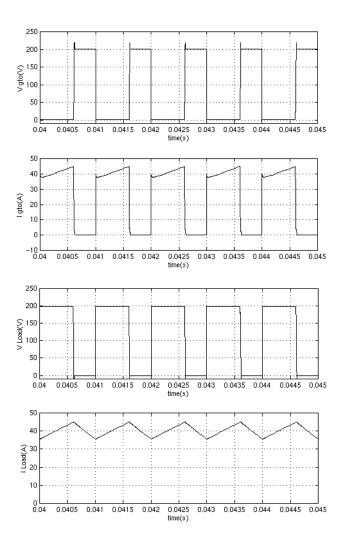
Example

The psbbuckconv.mdl demo illustrates the use of the GTO block in a buck converter topology. The basic polarized snubber circuit is connected across the GTO block. The snubber circuit consists of a capacitor Cs, a resistor Rs, and a diode Ds. The parasitic inductance Ls of the snubber circuit is also taken into consideration.

The parameters of the GTO block are those found in the dialog box section, except for the internal snubber, which is not used (Rs = Inf Cs = 0). The switching frequency is 1000 Hz and the pulse width is 216 degrees (duty cycle: 60%).



Run the simulation. Observe the GTO block voltage and current as well as the load voltage and current.



References

[1] Mohan, N., T.M. Undeland, and W.P. Robbins, *Power Electronics: Converters, Applications, and Design*, John Wiley & Sons, Inc., New York, 1995.

See Also IGBT, MOSFET, Thyristor

Purpose

 $Model\ a\ hydraulic\ turbine\ and\ a\ proportional\mbox{-integral-derivative}\ (PID)$

governor system

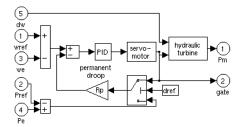
Library

Machines

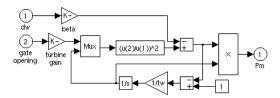
Description

The Hydraulic Turbine and Governor block implements a nonlinear hydraulic turbine model, a PID governor system, and a servomotor [1].

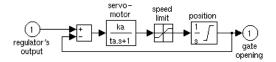




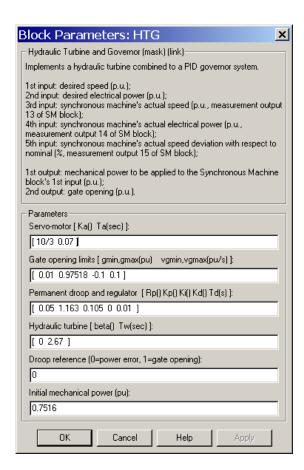
The hydraulic turbine is modeled by the following nonlinear system.



The gate servomotor is modeled by a second-order system.



Dialog Box and Parameters



Servo-motor

The gain Ka and time constant Ta, in seconds (s), of the first-order system representing the servomotor.

Gate opening limits

The limits gmin and gmax (p.u.) imposed on the gate opening, and vgmin and vgmax (p.u./s) imposed on gate speed.

Permanent droop and regulator

The static gain of the governor is equal to the inverse of the permanent droop Rp in the feedback loop. The PID regulator has a proportional gain

Kp, an integral gain Ki, and a derivative gain Kd. The high-frequency gain of the PID is limited by a first-order low-pass filter with time constant Td (s).

Hydraulic turbine

The speed deviation damping coefficient β and water starting time Tw (s).

Droop reference

Specifies the input of the feedback loop: gate position (set to 1) or electrical power deviation (set to 0).

Initial mechanical power

The initial mechanical power Pm0 (p.u.) at the machine's shaft. This value can be computed by the load flow utility of the Powergui block.

Inputs and Outputs

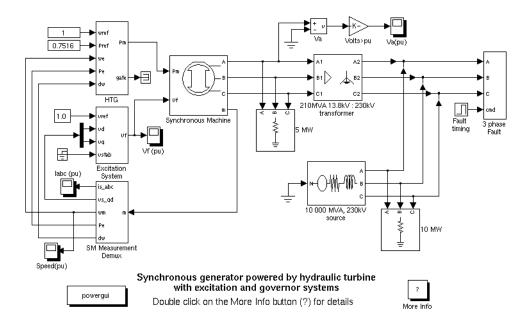
The first two inputs are the desired speed and mechanical power. The third and fourth inputs are the machine's actual speed and electrical power. The fifth input is the speed deviation. Inputs 2 and 4 can be left unconnected if you want to use the gate position as input to the feedback loop instead of the power deviation. All inputs are in p.u. The outputs of the block are mechanical power Pm for the Synchronous Machine block and gate opening (both in p.u.).

Example

This psbturbine.mdl demo illustrates the use of the Synchronous Machine associated with the Hydraulic Turbine and Governor (HTG) and Excitation System blocks. It also demonstrates the use of the load flow tool of the Powergui block to initialize machine currents. A three-phase generator rated 200 MVA, 13.8 kV, 112.5 rpm is connected to a 230 kV network through a Delta-Y 210 MVA transformer. The network short-circuit level is 10000 VA and the transformer has a 0.16 p.u. leakage reactance. The system starts in steady state with the generator supplying 150 MW of active power. At t = 0.1 s, a three-phase to ground fault occurs on the 230 kV bus of the transformer. The fault is cleared after six cycles (t = 0.2 s).

In order to start the simulation in steady state, you must initialize the Synchronous Machine block for the desired load flow. Open the **powergui** and select **Load flow and machine initialization**. The machine **Bus type** should be already initialized as PV generator, indicating that the load flow is performed with the machine controlling the active power and its terminal voltage. Specify the desired values by entering the following parameters:

- U AB (Vrms) = 13800
- P (watts) = 150e6

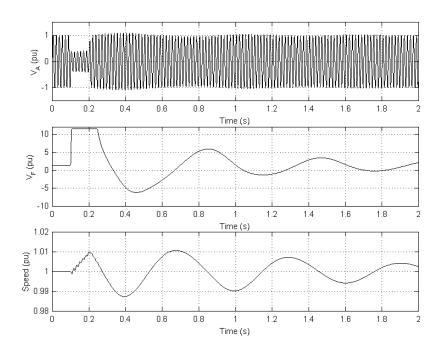


Then click the **Update Load Flow** button. Once the load flow has been solved, the phasors of AB and BC machine voltages as well as the currents flowing out of phases A and B are updated. The machine reactive power, mechanical power, and field voltage requested to supply the electrical power should also be displayed:

- Q = 3.4 Myar
- Pmec = 150.32 MW (0.7516 p.u.)
- \bullet Field voltage Ef = 1.291 p.u.

In order to start the simulation in steady state with the HTG and Excitation System blocks connected, you must also initialize these two Simulink blocks according to the values calculated by the load flow. Open the HTG block menu and notice that the initial mechanical power is set to 0.5007 p.u. (100.14 MW). Then open the Excitation System block menu and note that the initial terminal

voltage and field voltage are set respectively to 1.0 and 1.126 p.u. Open the four scopes and start the simulation. The simulation starts in steady state.



Observe that the terminal voltage Va is 1.0 p.u. at the beginning of the simulation. It falls to about 0.4 p.u. during the fault and returns to nominal quickly after the fault is cleared. This quick response in terminal voltage is due to the fact that the Excitation System output Vf can go as high as 11.5 p.u., which it does during the fault. The speed of the machine increases to 1.01 p.u. during the fault, then it oscillates around 1 p.u. as the governor system regulates it. The speed takes much longer than the terminal voltage to stabilize, mainly because the rate of valve opening/closing in the governor system is limited to 0.1 p.u./s.

References

[1] IEEE Working Group on Prime Mover and Energy Supply Models for System Dynamic Performance Studies, "Hydraulic Turbine and Turbine Control Models for Dynamic Studies," *IEEE Transactions on Power Systems*, Vol. 7, No. 1, February, 1992, pp. 167-179.

See Also

Excitation System, Steam Turbine and Governor, Synchronous Machine

Purpose

Implement an ideal switch device

Library

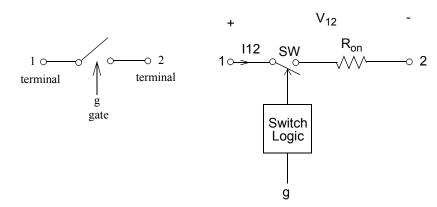
Power Electronics

Description

The Ideal Switch block does not correspond to a particular physical device. When used with appropriate switching logic, it can be used to model simplified semiconductor devices such as a GTO or a MOSFET, or even a power circuit breaker with current chopping. The switch is simulated as a resistor Ron in series with a switch controlled by a logical g.





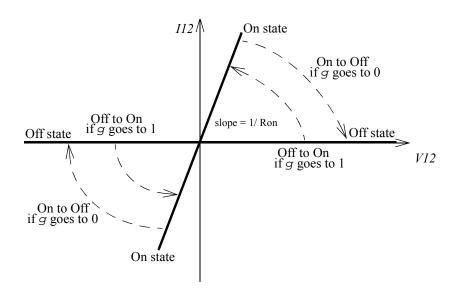


The Ideal Switch block is fully controlled by the gate signal (g > 0 or g = 0). It has the following characteristics:

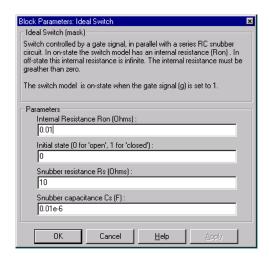
- Blocks any forward or reverse applied voltage with 0 current flow when g = 0
- \bullet Conducts any bidirectional current with quasi 0 voltage drop when g $\,>0$
- Switches instantaneously between on and off states when triggered

The Ideal Switch block turns on when a positive signal is present at the gate input (g > 0). It turns off when the gate signal equals 0 (g = 0).

The Ideal Switch block also contains a series Rs-Cs snubber circuit that can be connected in parallel with the ideal switch (between nodes 1 and 2).



Dialog Box and Parameters



Internal Resistance Ron

The internal resistance of the switch device, in ohms (Ω) . The **Internal resistance Ron** parameter cannot be set to 0.

Initial state

The initial state of the Ideal Switch block. The initial status of the Ideal Switch block is taken into account in the steady state calculation of Power System Blockset.

Snubber resistance Rs

The snubber resistance, in ohms (Ω) . Set the **Snubber resistance Rs** parameter to inf to eliminate the snubber from the model.

Snubber capacitance Cs

The snubber capacitance in farads (F). Set the **Snubber capacitance Cs** parameter to 0 to eliminate the snubber, or to inf to get a resistive snubber.

Show measurement port

If selected, add a Simulink output to the block returning the diode current and voltage.

Inputs and Outputs

The input port (g) controls the opening and closing of the switch. The output port (m) is a measurement output vector [Iak Vak] returning the Ideal Switch block current and voltage.

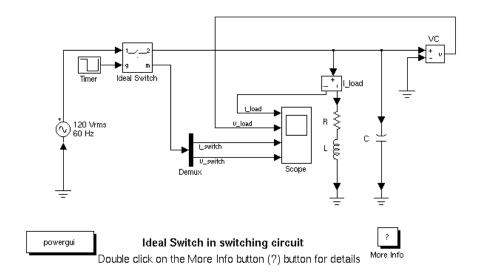
Assumptions and Limitations

The Ideal Switch block is modeled as a current source. It cannot be connected in series with an inductor, a current source, or an open circuit, unless its snubber circuit is in use. See the "Advanced Topics" chapter for more details on this topic.

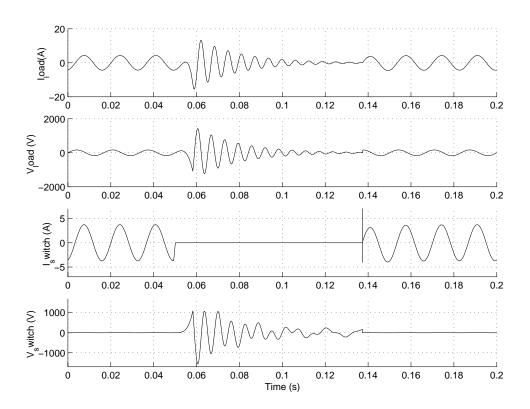
You must use a stiff integrator algorithm to simulate circuits containing Ideal Switch blocks. ode23tb or ode15s with default parameters usually gives the best simulation speed.

Example

The psbswitch.mdl demo uses the Ideal Switch block to switch an RLC circuit on an AC source (60 Hz). The switch, which is initially closed, is first open at t = 50 ms (3 cycles) and then reclosed at t = 138 ms (8.25 cycles). The **Ideal Switch** block has 0.01 ohms resistance and no snubber is used.



Run the simulation and observe the inductor current, the switch current, and the capacitor voltage. Notice the high-frequency overvoltage produced by inductive current chopping. Note also the high switch current spike when the switch is reclosed on the capacitor at maximum source voltage.



See Also Breaker

References

Mohan, N., T.M. Undeland, and W.P. Robbins, *Power Electronics: Converters, Applications, and Design*, John Wiley & Sons, Inc., New York, 1995.

IGBT

Purpose

Implement an insulated gate bipolar transistor (IGBT)

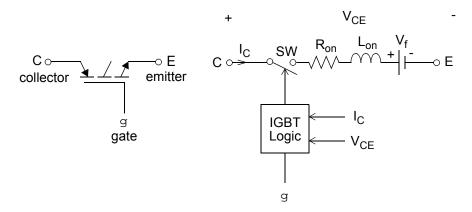
Library

Power Electronics

Description



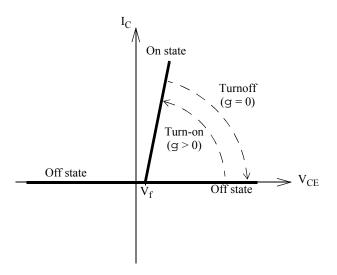
The IGBT block implements a semiconductor device controllable by the gate signal. The IGBT is simulated as a series combination of a resistor Ron, inductor Lon, and a DC voltage source Vf in series with a switch controlled by a logical signal (g > 0 or g = 0).



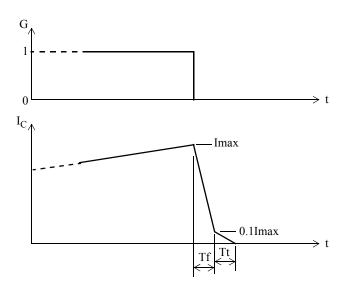
The IGBT turns on when the collector-emitter voltage is positive and greater than Vf and a positive signal is applied at the gate input (g > 0). It turns off when the collector-emitter voltage is positive and a 0 signal is applied at the gate input (g = 0).

The IGBT device is in the off state when the collector-emitter voltage is negative. Note that many commercial IGBTs do not have the reverse blocking capability. Therefore, they are usually used with an antiparallel diode.

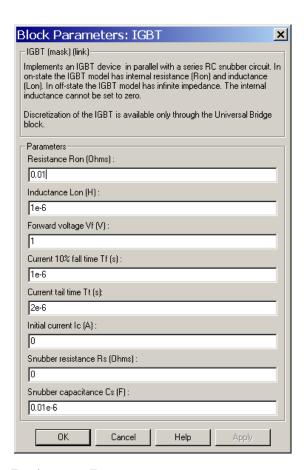
The IGBT block contains a series Rs-Cs snubber circuit, which is connected in parallel with the IGBT device (between nodes C and E).



The turnoff characteristic of the IGBT model is approximated by two segments. When the gate signal falls to 0, the collector current decreases from Imax to 0.1 Imax during the fall time (Tf), and then from 0.1 Imax to 0 during the tail time (Tt).



Dialog Box and Parameters



Resistance Ron

The internal resistance Ron, in ohms (Ω) .

Inductance Lon

The internal inductance Lon, in henries (H). The **Inductance Lon** parameter cannot be set to 0.

Forward voltage Vf

The forward voltage of the IGBT device, in volts (V).

Current 10% fall time

The current fall time Tf, in seconds (s).

Current tail time

The current tail time Tt, in seconds (s).

Initial current Ic

You can specify an initial current flowing in the IGBT. It is usually set to 0 in order to start the simulation with the device blocked.

If the **Initial Current IC** parameter is set to a value greater than 0, the steady state calculation of Power System Blockset considers the initial status of the IGBT as closed. Initializing all states of a power electronic converter is a complex task. Therefore, this option is useful only with simple circuits.

Snubber resistance Rs

The snubber resistance, in ohms (Ω) . Set the **Snubber resistance Rs** parameter to inf to eliminate the snubber from the model.

Snubber capacitance Cs

The snubber capacitance in farads (F). Set the **Snubber capacitance Cs** parameter to 0 to eliminate the snubber, or to inf to get a resistive snubber.

Show measurement port

If selected, add a Simulink output to the block returning the diode current and voltage.

Inputs and Outputs

The input port (g) is a logical Simulink signal applied to the gate. The output port is a measurement vector [Ic Vce] returning the IGBT current and voltage.

Assumptions and Limitations

The IGBT block implements a macromodel of the real IGBT device. It does not take into account either the geometry of the device or the complex physical processes [1].

The IGBT block is modeled as a current source. It cannot be connected in series with an inductor, a current source, or an open circuit, unless its snubber circuit is in use. In order to avoid an algebraic loop, you cannot set the IGBT block

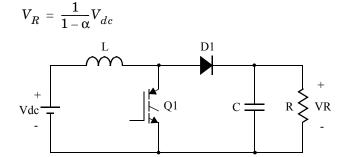
inductance Lon to 0. Each IGBT block adds an extra state to the electrical circuit model. See Chapter 3, "Advanced Topics," for more details on this topic.

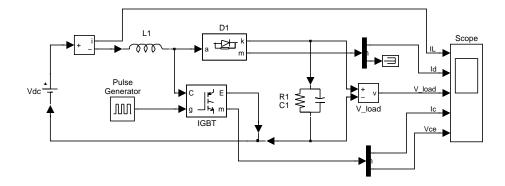
Circuits containing individual IGBT blocks cannot be discretized. However, discretization is permitted for IGBT/Diode bridges simulated with the Universal Bridge block.

You must use a stiff integrator algorithm to simulate circuits containing IGBTs. ode23tb or ode15s with default parameters usually gives the best simulation speed.

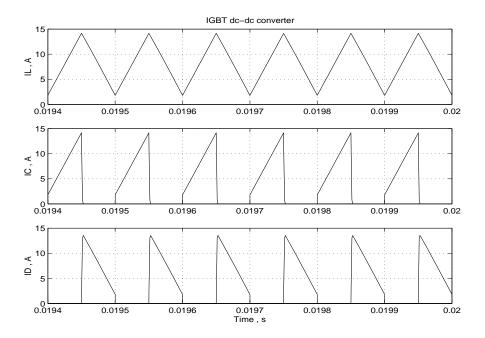
Example

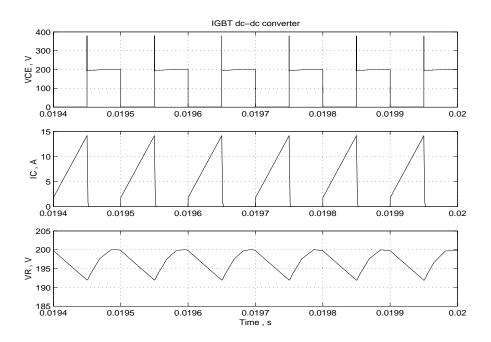
The psbigbtconv.mdl demo illustrates the use of the IGBT block in a boost DC-DC converter. The IGBT is switched on and off at a frequency of 10 kHz to transfer energy from the DC source to the load (RC). The average output voltage (V_R) is a function of the duty cycle (α) of the IGBT switch:





Run the simulation and observe the inductor current $(I_L),$ the IGBT collector current $(I_C),$ the diode current $(I_D),$ the IGBT device collector-emitter voltage $(V_{CE}),$ and the load voltage $(V_R).$





References

[1] Mohan, N., T.M. Undeland, and W.P. Robbins, *Power Electronics: Converters, Applications, and Design*, John Wiley & Sons, Inc., New York, 1995.

See Also

GTO, MOSFET, Thyristor, Universal Bridge

Purpose

Measure the impedance of a circuit as a function of frequency

Library

Measurements

Description

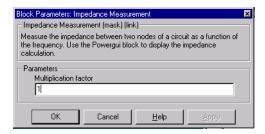


The Impedance Measurement block measures the impedance between two nodes of a circuit as a function of the frequency. It consists of a current source Iz, connected between inputs one and two of the Impedance Measurement block, and a voltage measurement Vz, connected across the terminals of the current source. The network impedance is calculated as the transfer function H(s) from the current input to the voltage output of the state-space model.

$$H(s) = \frac{V_z(s)}{I_z(s)}$$

The measurement takes into account the initial states of the Breaker and Ideal Switch blocks. It also allows impedance measurements with Distributed Parameter Line blocks in your circuit.

Dialog Box and Parameter



Multiplication factor

If you plan to use the Impedance Measurement block in a three-phase circuit, you can use the **Multiplication factor** parameter to rescale the measured impedance. For example, measuring the impedance between two phases of a three-phase circuit gives two times the positive-sequence impedance. Therefore you must apply a multiplication factor of 1/2 to the impedance in order to obtain the correct positive-sequence impedance value.

Impedance Measurement

Similarly, to measure the zero-sequence impedance of a balanced three-phase circuit, you can connect the Impedance Measurement block between ground or neutral and the three phases connected together.

In that case, you are measuring one third of the zero-sequence impedance and you must apply a multiplication factor of 3 to obtain the correct zero-sequence value.

Limitations

The only nonlinear blocks that are taken into account during the impedance measurement are the Breaker block, the Ideal Switch block, and the Distributed Parameter Line block. All other nonlinear blocks, such as machines and power electronic devices, are not considered, and they are disconnected during the measurement.

If you plan to connect the Impedance Measurement block in series with an inductance, a current source, or any nonlinear element, you must add a large resistor across the terminals of the block. Because the Impedance Measurement block is simulated as a current source block.

Example

See the Powergui block reference page for an example using the Impedance Measurement block.

See Also

Powergui

Purpose

Implement a two-winding or three-winding linear transformer

Library

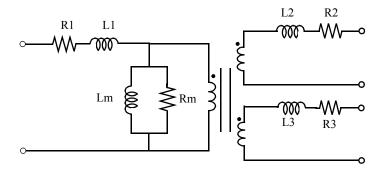
Elements

Description

The Linear Transformer block model shown consists of three coupled windings wound on the same core.







The model takes into account the winding resistances (R1 R2 R3) and the leakage inductances (L1 L2 L3), as well as the magnetizing characteristics of the core, which is modeled by a linear (Rm Lm) branch.

The Per Unit Conversion

In order to comply with industry, you must specify the resistance and inductance of the windings in per unit (p.u.). The values are based on the transformer rated power Pn, in VA, nominal frequency fn, in Hz, and nominal voltage Vn, in Vrms, of the corresponding winding. For each winding, the per unit resistance and inductance are defined as

$$R(\text{p.u.}) = \frac{R(\Omega)}{R_{base}}$$

$$L(\text{p.u.}) = \frac{L(H)}{L_{base}}$$

The base resistance and base inductance used for each winding are

Linear Transformer

$$egin{aligned} R_{base} &= rac{\left(Vn
ight)^2}{Pn} \ L_{base} &= rac{Rbase}{2\pi f_n} \end{aligned}$$

For the magnetization resistance Rm and inductance Lm, the p.u. values are based on the transformer rated power and on the nominal voltage of winding 1.

For example, the default parameters of winding 1 specified in the dialog box section give the following bases:

$$Rbase = \frac{(735e3/sqrt(3))^2}{250e6} = 720.3\Omega$$
 $Lbase = \frac{720.3}{2\pi60} = 1.91H$

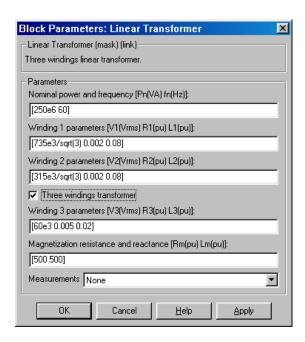
Suppose that the winding 1 parameters are R1 = 1.44 Ω and L1 = 0.1528 H; the corresponding values to be entered in the dialog box are

$$R_1 = \frac{1.44\Omega}{720.3\Omega} = 0.002 \text{ p.u.}$$

$$L_1 = \frac{0.1528H}{1.91H} = 0.08 \text{ p.u.}$$

To specify a magnetizing current of 0.2% (resistive and inductive) based on nominal current, you must enter per unit values of 1/0.002 = 500 p.u. for the resistance and the inductance of the magnetizing branch. Using the base values calculated previously, these per unit values correspond to Rm = 8.6e5 ohms and Lm = 995 henries.

Dialog Box and Parameters



Nominal power and frequency

The nominal power rating Pn in volt amperes (VA) and frequency fn, in hertz (Hz), of the transformer.

Winding 1 parameters

The nominal voltage V, in volts rms, resistance, and leakage inductance in p.u. The p.u. values are based on the nominal power Pn and on V1.

Winding 2 parameters

The nominal voltage V2 in volts rms, resistance, and leakage inductance in p.u. The p.u. values are based on the nominal power Pn and on V2.

Three windings transformer

If selected, implements a linear transformer with three windings; otherwise, it implements a two-windings transformer.

Winding 3 parameters

The nominal voltage in volts rms (Vrms), resistance, and leakage inductance in p.u. The p.u. values are based on the nominal power Pn and

Linear Transformer

on V3. The **Winding 3 parameters** parameter is not available if the **Three windings transformer** parameter is not selected.

Magnetization resistance and reactance

The resistance and inductance simulating the core active and reactive losses, both in p.u. The p.u. values are based on the nominal power Pn and on V1. For example, to specify 0.2% of active and reactive core losses, at nominal voltage, use Rm = 500 p.u. and Lm = 500 p.u.

Measurements

Select **Winding voltages** to measure the voltage across the winding terminals of the Linear Transformer block.

Select **Winding currents** to measure the current flowing through the windings of the Linear Transformer block.

Select **Magnetization current** to measure the magnetization current of the Linear Transformer block.

Select **All voltages and currents** to measure the winding voltages and currents plus the magnetization current.

Place a Multimeter block in your model to display the selected measurements during the simulation.

In the **Available Measurements** list box of the Multimeter block, the measurements are identified by a label followed by the block name.

Measurement	Label
Winding voltages	Uw1:, Uw2:, Uw3:
Winding currents	Iw1:, Iw2:, Iw3:
Magnetization current	Imag:

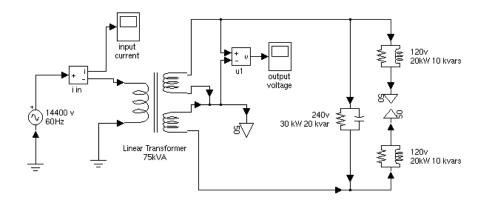
Note To implement an ideal transformer model, set the winding resistances and inductances to 0, and the magnetization resistance and inductance to inf.

Limitations

Windings can be left floating (that is, not connected to the rest of the circuit). However, the floating winding is connected internally to the main circuit through a resistor. This internal connection does not affect voltage and current measurements.

Example

The psbtransformer.mdl demo shows a typical residential distribution transformer network feeding line-to-neutral and line-to-line loads.



See Also

Mutual Inductance, Saturable Transformer

MOSFET

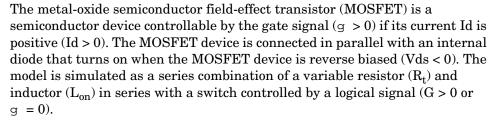
Purpose

Implement a MOSFET model

Library

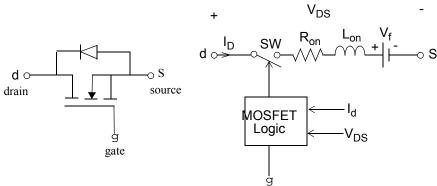
Power Electronics

Description









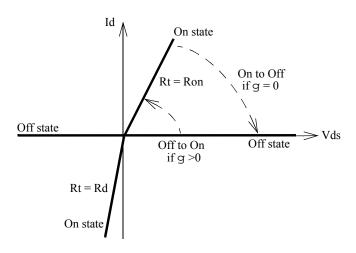
The MOSFET device turns on when the drain-source voltage is positive and a positive signal is applied at the gate input (g > 0).

With a positive current flowing through the device, the MOSFET turns off when the gate input becomes 0. If the current Id is negative (Id flowing in the internal diode) and without a gate signal (g = 0), the MOSFET turns off when the current Id becomes 0 (Id = 0).

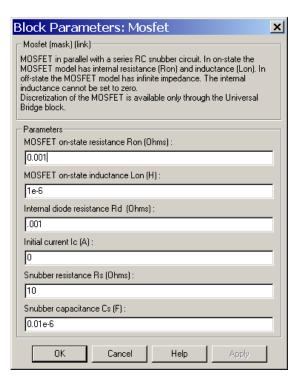
Note that the on state resistance Rt depends on the drain current direction:

- Rt = Ron if Id > 0, where Ron represents the typical value of the forward conducting resistance of the MOSFET device.
- \bullet Rt = Rd if Id < 0, where Rd represents the internal diode resistance.

The MOSFET block also contains a series Rs-Cs snubber circuit that can be connected in parallel with the MOSFET (between nodes d and s).



Dialog Box and Parameters



Resistance Ron

The internal resistance Ron, in ohms (Ω) .

Inductance Lon

The internal inductance Lon, in henries (H). The **Inductance Lon** parameter cannot be set to 0.

Internal diode resistance Rd

The internal resistance of the internal diode, in ohms (Ω) .

Initial current Ic

You can specify an initial current flowing in the MOSFET device. It is usually set to 0 in order to start the simulation with the device blocked.

If the **Initial Current IC** parameter is set to a value greater than 0, the steady state calculation of Power System Blockset considers the initial status of the MOSFET as closed. Initializing all states of a power electronic converter is a complex task. Therefore, this option is useful only with simple circuits.

Snubber resistance Rs

The snubber resistance, in ohms (Ω) . Set the **Snubber resistance Rs** parameter to inf to eliminate the snubber from the model.

Snubber capacitance Cs

The snubber capacitance, in amperes (A). Set the **Snubber capacitance Cs** parameter to 0 to eliminate the snubber, or to inf to get a resistive snubber.

Show measurement port

If selected, add a Simulink output to the block returning the diode current and voltage.

Inputs and Outputs

The input port is a logical signal applied to the gate. The output port is a measurement vector [Id Vds] returning the MOSFET device current and voltage.

Assumptions and Limitations

The MOSFET block implements a macromodel of the real MOSFET device. It does not take into account either the geometry of the device or the complex physical processes [1].

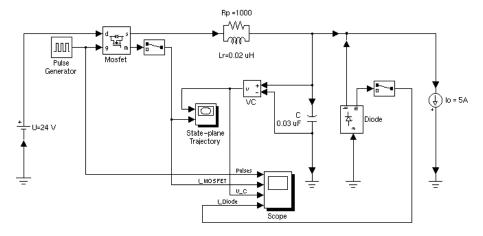
The MOSFET block is modeled as a current source. It cannot be connected in series with an inductor, a current source, or an open circuit, unless its snubber circuit is in use. In order to avoid an algebraic loop, you cannot set the MOSFET block inductance Lon to 0. Each MOSFET block adds an extra state to the electrical circuit model. See Chapter 3, "Advanced Topics," for more details on this topic.

Circuits containing individual MOSFET blocks cannot be discretized. However discretization is permitted for MOSFET/Diode bridges simulated with the Universal Bridge block.

You must use a stiff integrator algorithm to simulate circuits containing MOSFETs. ode23tb or ode15s with default parameters usually gives the best simulation speed.

Example

The psbmosconv.mdl demo illustrates the use of the MOSFET block in a 0-current quasi-resonant switch converter. In such a converter, the current produced by the Lr-Cr resonant circuit flows through the MOSFET and internal diode. The negative current flows through the internal diode that turns off at 0 current [1]. The switching frequency is 2 MHz and the pulse width is 72 degrees (duty cycle: 20%).

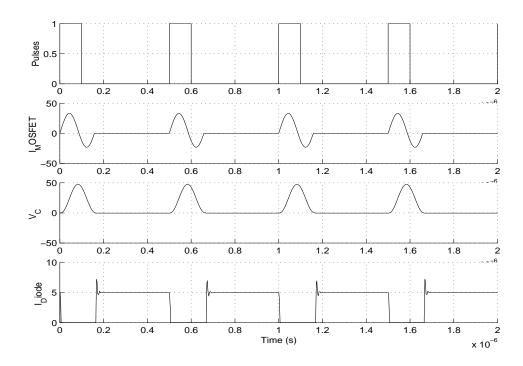


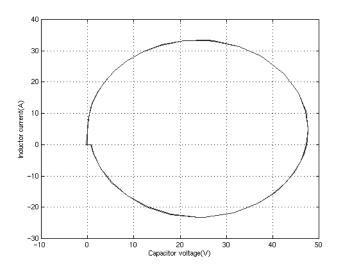
MOSFET in Zero-Current-Quasi-Resonant Converter

Double click on the More Info button (?) for details



Run the simulation and observe the gate pulse signal, the MOSFET current, the capacitor voltage, and the diode current on the four-trace Scope block. Also observe the state-plane trajectory (inductor current versus capacitor voltage).





References

[1] Mohan, N., T.M. Undeland, and W.P. Robbins, *Power Electronics: Converters, Applications, and Design*, John Wiley & Sons, Inc., New York, 1995.

See Also

Diode, GTO, Ideal Switch, Thyristor

Purpose

Implement a multiband power system stabilizer

Library

Machines

Description



The disturbances occurring in a power system induce electromechanical oscillations of the electrical generators. These oscillations, also called power swings, must be effectively damped to maintain the system stability. Electromechanical oscillations can be classified in four main categories:

- Local oscillations: between a unit and the rest of the generating station and between the latter and the rest of the power system. Their frequencies typically range from 0.8 to 4.0 Hz.
- Interplant oscillations: between two electrically close generation plants. Frequencies can vary from 1 to 2 Hz.
- Interarea oscillations: between two major groups of generation plants. Frequencies are typically in a range of 0.2 to 0.8 Hz.
- Global oscillation: characterized by a common in-phase oscillation of all generators as found on an isolated system. The frequency of such a global mode is typically under 0.2 Hz.

The need for effective damping of such a wide range, almost two decades, of electromechanical oscillations motivated the concept of the multiband power system stabilizer (MB-PSS).

As its name reveals, the MB-PSS structure is based on multiple working bands. Three separate bands are used, respectively dedicated to the low-, intermediate-, and high-frequency modes of oscillations: the low band is typically associated with the power system global mode, the intermediate with the interarea modes, and the high with the local modes.

Each of the three bands is made of a differential bandpass filter, a gain, and a limiter (see Figure 4-1). The outputs of the three bands are summed and passed through a final limiter producing the stabilizer output $V_{stab}.$ This signal then modulates the set point of the generator voltage regulator so as to improve the damping of the electromechanical oscillations.

To ensure robust damping, the MB-PSS should include a moderate phase advance at all frequencies of interest to compensate for the inherent lag

between the field excitation and the electrical torque induced by the MB-PSS action.

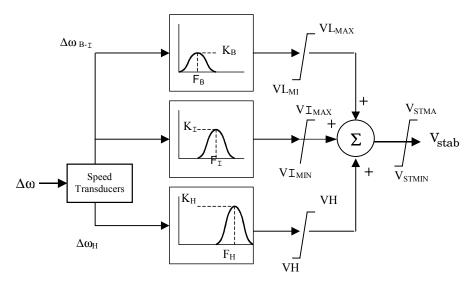


Figure 4-1: Conceptual Representation

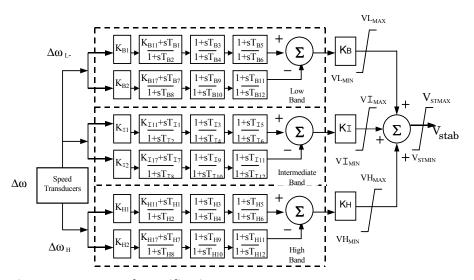


Figure 4-2: Internal Specifications

The MB-PSS is represented by the IEEE Std. 421.5 PSS 4B type model [2], illustrated in Figure 4-2, with built-in speed transducers whose parameters are fixed according to manufacturer's specifications.

Generally, only a few of the lead-lag blocks in Figure 4-2 should be used in a given PSS application. Two different approaches are available to configure the settings in order to facilitate the tuning process:

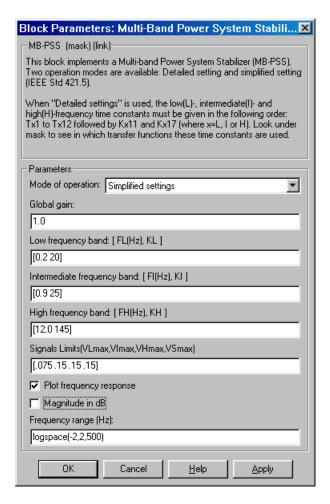
1 Simplified settings:

Only the first lead-lag block of each frequency band is used to tune the Multiband Power System Stabilizer block. The differential filters are assumed to be symmetrical bandpass filters respectively tuned at the center frequency $F_L,\,F_I$, and $F_H.$ The peak magnitude of the frequency responses (Figure 4-1) can be adjusted independently through the three gains $K_L,\,K_I$, and $K_H.$ Only six parameters are therefore required for a simplified tuning of the MB-PSS.

2 Detailed settings:

The designer is free to use all the flexibility built into the MB-PSS structure to achieve nontrivial controller schemes and to tackle even the most constrained problem (for example, multiunit plant including an intermachine mode in addition to a local mode and multiple interarea modes). In this case, all the time constants and gains appearing in Figure 4-2 have to be specified in the dialog box.

Dialog Box Mode of Operation: Simplified Settings



Global gain

The overall gain K of the multiband power system stabilizer.

Low frequency band: [FL KL]

The center frequency, in hertz, and peak gain of the low-frequency bandpass filter.

Intermediate frequency band: [FI KI]

The center frequency, in hertz, and peak gain of the intermediate frequency bandpass filter.

High frequency band: [FH KH]

The center frequency, in hertz, and peak gain of the high-frequency bandpass filter.

Signal limits: [VLmax VImax VHmax VSmax]

The limits imposed on the output of the low-, intermediate-, and high-frequency bands and the limit VSmax imposed on the output of the stabilizer, all in p.u.

Plot frequency response

If selected, a plot of the frequency response of the stabilizer is displayed when you click the **Apply** button.

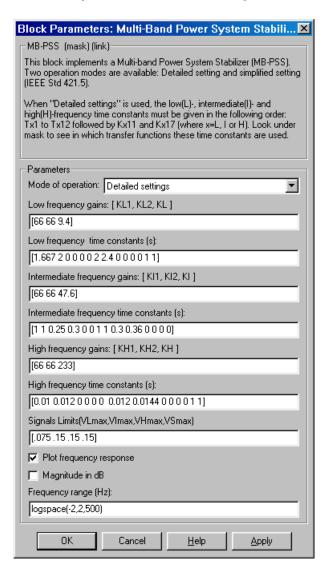
Magnitude in dB

If selected, the magnitude of the frequency response is plotted in dB. The **Magnitude in dB** parameter is not visible in the dialog box if the **Plot frequency response** is not selected.

Frequency range (Hz)

The frequency range used to plot the frequency response of the stabilizer. The **Frequency range** (**Hz**) parameter is not visible in the dialog box if the **Plot frequency response** is not selected.

Mode of Operation: Detailed Settings



Low frequency gains: [KL1 KL2 KL]

The gains of the positive and negative branches of the differential filter in the low-frequency band and the overall gain K_L of the low-frequency band, in p.u.

Low frequency time constants:

The time constants, in seconds, of the lead-lag blocks in the positive and negative branches of the low-frequency filter. You need to specify the following twelve time constants and two gains:

$$[T_{B1} T_{B2} T_{B3} T_{B4} T_{B5} T_{B6} T_{B7} T_{B8} T_{B9} T_{B10} T_{B11} T_{B12} K_{B11} K_{B17}]$$

Set K_{B11} to 0 in order to make the first block of the positive filter branch a washout block. Set K_{B11} to 1 in order to make the block a lead-lag block.

Set K_{B17} to 0 in order to make the first block of the negative filter branch a washout block. Set K_{B17} to 1 in order to make the block a lead-lag block.

Intermediate frequency gains: [KI1 KI2 KI]

The gains of the positive and negative branches of the differential filter in the intermediate-frequency band and the overall gain K_I of the intermediate-frequency band, in p.u.

Intermediate frequency time constants:

The time constants, in seconds, of the lead-lag blocks in the positive and negative branches of the intermediate-frequency filter. You need to specify the following twelve time constants and two gains:

$$[T_{I1}\ T_{I2}\ T_{I3}\ T_{I4}\ T_{I5}\ T_{I6}\ T_{I7}\ T_{I8}\ T_{I9}\ T_{I10}\ T_{I11}\ T_{I12}\ K_{I11}\ K_{I17}]$$

Set K_{I11} to 0 in order to make the first block of the positive filter branch a washout block. Set K_{I11} to 1 in order to make the block a lead-lag block.

Set K_{I17} to 0 in order to make the first block of the negative filter branch a washout block. Set K_{I17} to 1 in order to make the block a lead-lag block.

High frequency gains: [KH1 KH2 KH]

The gains of the positive and negative branches of the differential filter in the high-frequency band and the overall gain $K_{\rm I}$ of the high-frequency band, in p.u.

High frequency time constants:

The time constants, in seconds, of the lead-lag blocks in the positive and negative branches of the high-frequency filter. You need to specify the following twelve time constants and two gains:

 $[T_{H1} \ T_{H2} \ T_{H3} \ T_{H4} \ T_{H5} \ T_{H6} \ T_{H7} \ T_{H8} \ T_{H9} \ T_{H10} \ T_{H11} \ T_{H12} \ K_{H11} \ K_{H17}]$

Set K_{H11} to 0 in order to make the first block of the positive filter branch a washout block. Set K_{H11} to 1 in order to make the block a lead-lag block.

Set K_{H17} to 0 in order to make the first block of the negative filter branch a washout block. Set K_{H17} to 1 in order to make the block a lead-lag block.

Output limits: [VLmax VImax VHmax VSmax]

The limits imposed on the output of the low-, intermediate-, and high-frequency bands and the limit VSmax imposed on the output of the stabilizer, all in p.u.

Input and Output

dw

Connect to the first input the synchronous machine speed deviation dw signal (in p.u.).

Vstab

The output is the stabilization voltage, in p.u., to connect to the vstab input of the Excitation System block used to control the terminal voltage of the Synchronous Machine block.

Example

See the help text of the psbPSS demonstration file.

Reference

[1] Grondin, R., I. Kamwa, L. Soulieres, J. Potvin, R. Champagne, "An approach to PSS design for transient stability improvement through supplementary damping of the common low frequency," *IEEE Transactions on Power Systems*, **8**(3), Aug. 1993, pp. 954 -963.

[2] IEEE recommended practice for excitation system models for power system stability studies: *IEEE Std 421.5-2002* (Section 9).

See Also

Generic Power System Stabilizer

Multimeter

Purpose

Measure the voltages and currents specified in dialog boxes of Power System

Blockset blocks

Library

Measurements

Description

The Multimeter block is used to measure voltages and currents of the measurements described by the dialog boxes of Power System Blockset blocks.

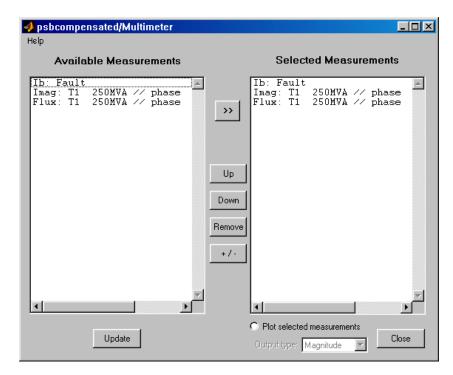


The **powerlib** blocks listed in the following table have a special parameter (**Measurements**) that allows you to measure voltages or currents related to the block. Choosing voltages or currents through this measurement parameter is equivalent to connecting an internal voltage or current measurement block inside your blocks. The measured signals can be observed through a Multimeter block placed in your circuit.

Drag the Multimeter block into the top-level system of your circuit and double-click the icon to open the graphical user interface (GUI).

Block Name	Block Name
AC Current Source	Parallel RLC Branch
AC Voltage Source	Parallel RLC Load
Controlled Current Source	PI Section Line
Controlled Voltage Source	Saturable Transformer
DC Voltage Source	Series RLC Branch
Breaker	Series RLC Load
Distributed Parameter Line	Surge Arrester
Linear Transformer	Three-Phase Transformer (Two and Three Windings)
Mutual Inductance	Universal Bridge

Dialog Box and Parameters



Available Measurements

The **Available Measurements** list box shows the measurements in the Multimeter block. Use the >> button to select measurements from the **Available Measurements** list box. Click the **Update** button to refresh the list of available measurements in the Multimeter block.

The measurements in the list box are identified by the name of the block where the measurement is done. The type of measurement (voltage measurement, current measurement, or flux) is defined by a label preceding the block name. See the reference sections of blocks listed in the previous table for a description of these measurements.

Selected Measurements

The **Selected Measurements** list box shows the measurements sent to the output of the block. You can reorder the measurements by using the **Up**,

Down, and **Remove** buttons. The **+/-** button allow you to reverse the polarity of any selected measurement.

Plot selected measurements

If selected, displays a plot of selected measurements using a MATLAB figure window. The plot is generated when the simulation stops.

Output type

Specifies the format of the output signals when the block is used in a phasor simulation. The **Output signal** parameter is disabled when the block is not used in a phasor simulation. The phasor simulation is activated by a Powergui block placed in the model.

Set to **Complex** to output the selected measurements as complex values. The outputs are complex signals.

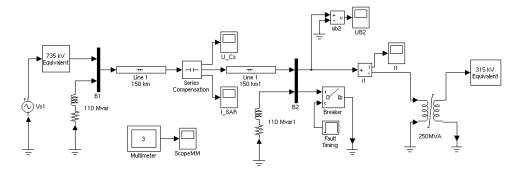
Set to **Real-Imag** to output the real and imaginary parts of the measurements. For each selected measurement, the multimeter outputs the real and imaginary parts.

Set to **Magnitude-Angle** to output the magnitude and angle of the selected measurements. For each selected measurement, the multimeter outputs the magnitude and angle values.

Set to Magnitude to output the magnitude of the selected measurements.

Example

The psbcompensated.mdl demo uses a Multimeter block to measure the voltage at the secondary winding of a Saturable Transformer block and the currents flowing through two Series RLC Load blocks.



Multimeter

A Multimeter block is dragged into the model. In the dialog box of the 250 MVA block, set the **Measurements** parameter to All measurements (V,I,flux). In the 110 Mvar block, set it to Branch voltage and in the 110 Mvar1 block, set it to Branch voltage and current.

The output of the Multimeter block is connected to a Scope block in order to display the measurements during the simulation. In addition, you can check the **Plot selected measurements** parameter to display a plot of selected measurements when simulation stops.

Open the Multimeter block dialog box and select the signals you want to observe, as shown on the Parameters and Dialog Box section of the Multimeter block. Notice the labels used to defined the available measurements in the Multimeter block. See the reference section of the Saturable Transformer block and Series RLC Load block for a description of these labels.

Start the simulation. After 0.4 seconds, the simulation stops and a MATLAB figure window opens to display the selected measurements in the Multimeter block.

See Also

Current Measurement, Voltage Measurement

Mutual Inductance

Purpose

Implement a magnetic coupling between two or three windings

Library

Elements

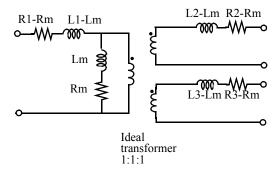
Description

The Mutual Inductance block implements a magnetic coupling between three separate windings. Specify the self resistance and inductance of each winding on the first three entries of the dialog box and the mutual resistance and inductance in the last entry.

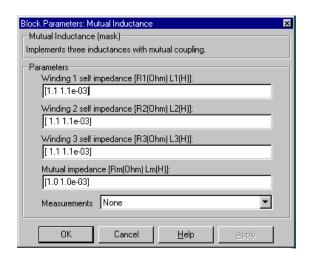


The electrical model for this block is given below:





Dialog Box and Parameters



Winding 1 self impedance

The self resistance and inductance for the winding 1, in ohms (Ω) and henries (H).

Winding 2 self impedance

The self resistance and inductance for the winding 2, in ohms (Ω) and henries (H).

Winding 3 self impedance

The self resistance and inductance in ohms (Ω) and henries (H) for the winding 3. Set the **Winding 3 self impedance** parameter to 0 to implement a Mutual Inductance block with two-windings; a new icon is displayed:



Mutual impedance

The mutual resistance and inductance between windings, in ohms (Ω) and henries (H). If the mutual resistance and reactance are set to $[0\ 0]$, the block implements three separate inductances with no mutual coupling.

Measurements

Select **Winding voltages** to measure the voltage across the winding terminals.

Select **Winding currents** to measure the current flowing through the windings.

Select **Winding voltages and currents** to measure the winding voltages and currents.

Place a Multimeter block in your model to display the selected measurements during the simulation.

Mutual Inductance

In the **Available Measurements** list box of the Multimeter block, the measurements are identified by a label followed by the block name.

Measurement	Label
Winding voltages	Uw1:, Uw2:, Uw3:
Winding currents	Iw1:, Iw2:, Iw3:

Inputs and Outputs

Winding 1 is connected between input and output one of the Mutual Inductance block. Winding 2 is connected between input and output two and winding 3, if defined, is connected between input and output three.

The input ports of the Mutual impedance block are at the same instantaneous polarity.

Limitations

Because of modeling constraints, the following restrictions apply:

$$R_1, R_2, R_3 \neq R_m$$

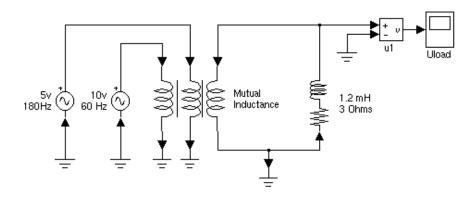
$$L_1,L_2,L_3\neq L_m$$

Negative values are allowed for the self and mutual inductances as long as the self-inductances are different from the mutual inductance.

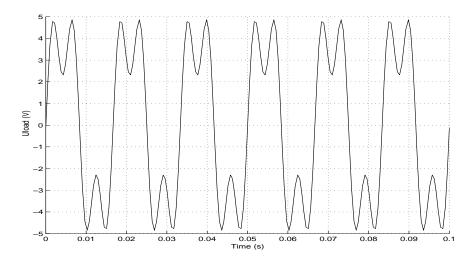
Windings can be left floating (not connected by an impedance to the rest of the circuit). The floating winding is internally connected to the main circuit through a resistor. This invisible connection does not affect voltage and current measurements.

Example

The psbmutual.mdl demo uses three coupled windings to inject a third harmonic voltage into a circuit fed at 60 Hz.



Simulation produces the following load voltage waveform:



See Also Linear Transformer, Saturable Transformer

Neutral

Purpose

Implement a common node in the circuit

Library

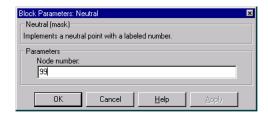
Elements

Description

The Neutral block implements a common node with a specific node number. If the node number is set to 0, the neutral block automatically makes a connection to ground. The node number is displayed on the icon. You can use this block to create a floating neutral or to interconnect two points without drawing a connection line.

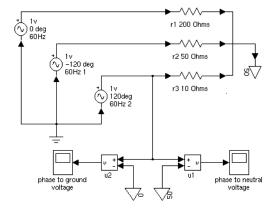
For drawing ease, two types of Neutral blocks are available in the library: one block with an input and one block with an output.

Dialog Box



Example

The psbneutral.mdl demo uses three Neutral blocks to connect three resistor blocks to a floating neutral (node 50).



See Also

Ground

Purpose

Implement a parallel RLC branch

Library

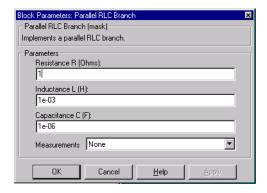
Elements

Description

The Parallel RLC Branch block implements a single resistor, inductor, and capacitor or a parallel combination of these. To eliminate either the resistance, inductance, or capacitance of the branch, the R, L, and C values must be set respectively to infinity (inf), infinity (inf), and 0. Only existing elements are displayed in the block icon.

Negative values are allowed for resistance, inductance, and capacitance.

Dialog Box and Parameters



Resistance R

The branch resistance, in ohms (Ω) .

Inductance L

The branch inductance, in henries (H).

Capacitance C

The branch capacitance, in farads (F).

Measurements

Select **Branch voltage** to measure the voltage across the Parallel RLC Branch block terminals.

Select **Branch current** to measure the total current (sum of R, L, C currents) flowing through the Parallel RLC Branch block.

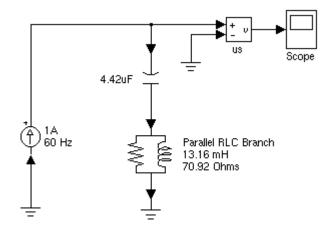
Select **Branch voltage and current** to measure the voltage and the current of the Parallel RLC Branch block.

Place a Multimeter block in your model to display the selected measurements during the simulation. In the **Available Measurements** list box of the Multimeter block, the measurement is identified by a label followed by the block name.

Measurement	Label
Branch voltage	Ub:
Branch current	Ib:

Example

The psbparalbranch.mdl demo is used to obtain the frequency response of an eleventh-harmonic filter turned at $660~{\rm Hz}$ connected on the $60~{\rm Hz}$ power system



The network impedance in the Laplace domain is

$$Z(s) = rac{V(s)}{I(s)} = rac{RLCs^2 + Ls + R}{LCs^2 + RCs}$$

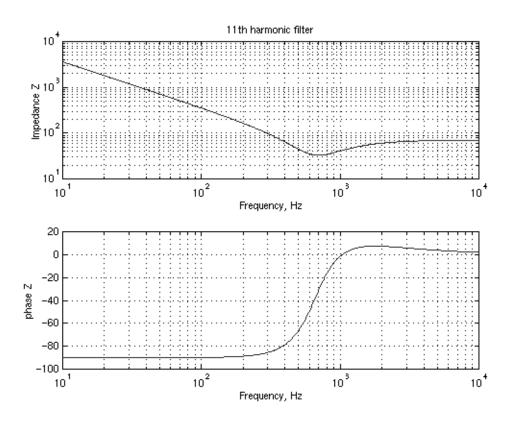
To obtain the frequency response of the impedance you have to get the state-space model (A B C D matrices) of the system.

This system is a one input (Is) and one output (Vs) system. If you own the Control System Toolbox, you can get the transfer function Z(s) from the state-space matrices and the bode function.

```
[A,B,C,D] = power2sys('psbparalbranch');
freq = logspace(1,4,500);
w = 2π*freq;
[Zmag,Zphase] = bode(A,B,C,D,1,w);
subplot(2,1,1)
loglog(freq,Zmag)
grid
title( 11th harmonic filter')
xlabel('Frequency, Hz')
ylabel('Impedance Z')
subplot(2,1,2)
semilogx(freq,Zphase)
xlabel('Frequency, Hz')
ylabel('phase Z')
grid
```

You can also use the Impedance Measurement block and the Powergui block to plot the impedance as a function of frequency.

Parallel RLC Branch



See Also Parallel RLC Load, Series RLC Branch, Series RLC Load

Purpose

Implement a linear parallel RLC load

Library

Elements

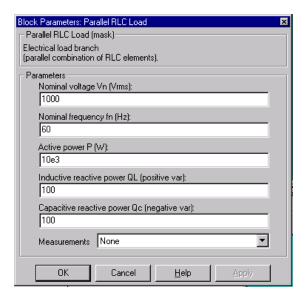
Description

The Parallel RLC Load block implements a linear load as a parallel combination of RLC elements. At the specified frequency, the load exhibits a constant impedance and its power is proportional to the square of the applied voltage.



Only elements associated with nonzero powers are displayed in the block icon.

Dialog Box and Parameters



Nominal voltage Vn

The nominal voltage of the load, in volts rms (Vrms).

Nominal frequency fn

The nominal frequency, in hertz (Hz).

Active power P

The active power of the load, in watts.

Parallel RLC Load

Inductive reactive power QL

The inductive reactive power QL, in vars. Specify a positive value, or 0.

Capacitive reactive power QC

The capacitive reactive power QC, in vars. Specify a positive value, or 0.

Measurements

Select **Branch voltage** to measure the voltage across the Parallel RLC Load block terminals.

Select **Branch current** to measure the current flowing through the Parallel RLC Load block.

Select **Branch voltage and current** to measure the voltage and the current of the Parallel RLC Load block.

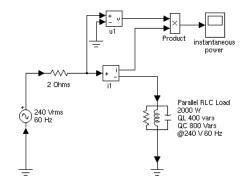
Place a Multimeter block in your model to display the selected measurements during the simulation. In the **Available Measurements** list box of the Multimeter block, the measurement is identified by a label followed by the block name.

Measurement	Label
Branch voltage	Ub:
Branch current	Ib:

Example

The psbparalload.mdl demo uses a parallel RLC load block to implement a load.

Parallel RLC Load



See Also Parallel RLC Branch, Series RLC Branch, Series RLC Load

Purpose

Model the dynamics of a three-phase permanent magnet synchronous machine with sinusoidal flux distribution

Library

Machines

Description



The Permanent Magnet Synchronous Machine block operates in either generating or motoring mode. The mode of operation is dictated by the sign of the mechanical torque (positive for motoring, negative for generating). The electrical and mechanical parts of the machine are each represented by a second-order state-space model. The model assumes that the flux established by the permanent magnets in the stator is sinusoidal, which implies that the electromotive forces are sinusoidal.

The block implements the following equations expressed in the rotor reference frame (qd frame).

Electrical System

$$\begin{split} &\frac{d}{dt}i_d = \frac{1}{L_d}v_d - \frac{R}{L_d}i_d + \frac{L_q}{L_d}p\omega_r i_q \\ &\frac{d}{dt}i_q = \frac{1}{L_q}v_q - \frac{R}{L_q}i_q - \frac{L_d}{L_q}p\omega_r i_d - \frac{\lambda p\omega_r}{L_q} \\ &T_e = 1.5p[\lambda i_q + (L_d - L_q)i_d i_q] \end{split}$$

where (all quantities in the rotor reference frame, referred to the stator)

- \bullet L_q, L_d: q and d axis inductances
- R: Resistance of the stator windings
- \bullet $i_q,\,i_d\!:q$ and d axis currents
- vq, vd: q and d axis voltages
- \bullet $\omega_r\!:$ Angular velocity of the rotor
- \bullet $\lambda :$ Amplitude of the flux induced by the permanent magnets of the rotor in the stator phases
- p: Number of pole pairs
- T_e: Electromagnetic torque

Mechanical System

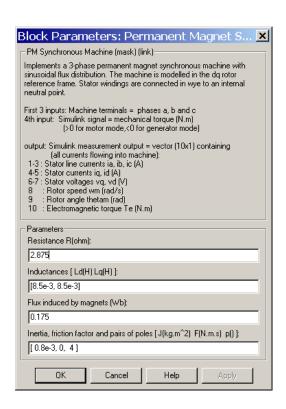
$$\frac{d}{dt}\omega_r = \frac{1}{J}(T_e - F\omega_r - T_m)$$

$$\frac{d\theta}{dt} = \omega_r$$

where

- J: Combined inertia of rotor and load
- F: Combined viscous friction of rotor and load
- θ: Rotor angular position
- T_m: Shaft mechanical torque

Dialog Box and Parameters



Resistance

The stator resistance $R(\Omega)$.

Inductances

The d-axis and q-axis stator inductances Ld (H) and Lq (H).

Flux induced by magnets

The constant flux λ (Wb) induced in the stator windings by the magnets.

Mechanical

The combined machine and load inertia coefficient J (kg.m²), combined viscous friction coefficient F (N.m.s), and pole pairs p.

Inputs and Outputs

The first three inputs are the electrical connections of the machine's stator. The fourth input is the mechanical torque at the machine's shaft (Simulink signal). This input should normally be positive because the Permanent Magnet Synchronous Machine block is usually used as a motor. Nevertheless, you can apply a negative torque input if you choose to use the block in generating mode.

The block outputs a vector containing the following 10 variables (all currents flowing into machine):

- 1-3: Line currents i_a , i_b , and i_c , in A
- ullet 4-5: q and d axis currents i_q and i_d , in A
- \bullet 6-7: q and d axis voltages $\boldsymbol{v}_{\boldsymbol{q}}$ and $\boldsymbol{v}_{\boldsymbol{d}},$ in \boldsymbol{V}
- \bullet 8: Rotor mechanical speed $\omega_{\rm r},$ in rad/s
- 9: Rotor mechanical angle θ , in rad
- \bullet 10: Electromagnetic torque T_e , in N.m

You can demultiplex these variables by using the Machines Measurement Demux block provided in the Machines library.

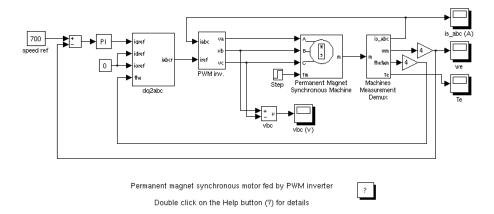
Assumption

The Permanent Magnet Synchronous Machine block assumes a linear magnetic circuit with no saturation of the stator and rotor iron. This assumption can be made because of the large air gap usually found in permanent magnet synchronous machines.

Example

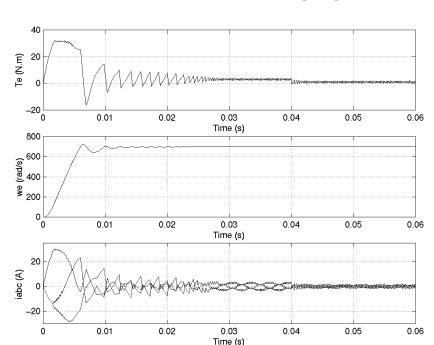
This psbpmmotor.mdl demo illustrates the use of the Permanent Magnet Synchronous Machine block in motoring mode with a closed-loop control

system built entirely in Simulink. The interfacing is done using Controlled Voltage Source blocks from the Electrical Sources library. The complete system consists of a PWM inverter built with ideal switches (Simulink Relay blocks). Two control loops are used; the inner loop is used to regulate the motor line currents and the outer loop regulates the motor's speed. More elaborate and efficient control schemes for the Permanent Magnet Synchronous Machine block can be found, for instance, in [1]. The mechanical torque applied at the motor's shaft is originally 3 N.m (nominal) and steps to 1 N.m at t=0.04 seconds. The parameters of the machine are those found in the dialog box section.



Set the simulation parameters as follows:

- Integrator type: stiff, ode15s
- Stop time: 0.06
- Integration options: Use default options, except for absolute tolerance that you can set to 1e-3



Run the simulation and observe the motor's torque, speed, and currents.

The torque climbs to nearly 32 N.m when the motor starts but stabilizes rapidly to its nominal value (3 N.m), until the step is applied, at which point the torque oscillates slightly before stabilizing to its new value (1 N.m). As for the speed, you can see that it stabilizes quite fast at start-up and is not affected by the load step.

The currents are initially high when the machine starts, like the torque, but stabilize quickly to their nominal values, until the step is applied, at which point they oscillate before stabilizing to a lower value, corresponding to the load torque decrease.

References

[1] Grenier D., L.-A. Dessaint, O. Akhrif, Y. Bonnassieux, and B. LePioufle, "Experimental Nonlinear Torque Control of a Permanent Magnet Synchronous Motor Using Saliency," *IEEE Transactions on Industrial Electronics*, Vol. 44, No. 5, October 1997, pp. 680-687.

Purpose

Implement a single phase transmission line with lumped parameters

Library

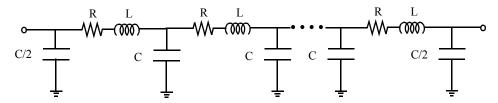
Elements

Description

The PI Section Line block implements a single-phase transmission line with parameters lumped in PI sections.

For a transmission line, the resistance, inductance, and capacitance are uniformly distributed along the line. An approximate model of the distributed parameter line is obtained by cascading several identical PI sections, as shown in the following figure.

Section1



Unlike the Distributed Parameter Line block, which has an infinite number of states, the PI section linear model has a finite number of states that permit you to compute a linear state-space model. The number of sections to be used depends on the frequency range to be represented.

A good approximation of the maximum frequency range represented by the PI line model is given by the following equation:

$$f_{max} = \frac{Nv}{8l}$$

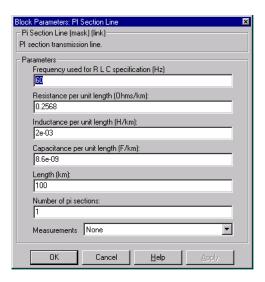
where

- N: Number of PI sections
- v: Propagation speed in km/s = $1/\sqrt{LC} L$ in H/km, C in F/km
- *l*: Line length in km

For example, for a 100 km aerial line having a propagation speed of $300 \ 000 \text{ km/s}$, the maximum frequency range represented with a single PI section is

approximately 375 Hz. For studying interactions between a power system and a control system, this simple model could be sufficient. However for switching surge studies involving high-frequency transients in the kHz range, much shorter PI sections should be used. In fact, accurate results would probably only be obtained by using a distributed parameters line model.

Dialog Box and Parameters



Frequency used for RLC specifications

Frequency used to compute the line parameters, in hertz (Hz).

Resistance per unit length

The resistance per unit length of the line, in ohms/km $(\Omega).$

Inductance per unit length

The inductance per unit length of the line, in henries/km (H/km).

Capacitance per unit length

The capacitance per unit length of the line, in farads/km (F/km).

Length

The line length in km.

Number of pi sections

The number of PI sections. The minimum value is 1.

Measurements

Select **Input and output voltages** to measure the sending end (input port) and receiving end (output port) voltages of the line model.

Select **Input** and output voltages to measure the sending end and receiving end currents of the line model.

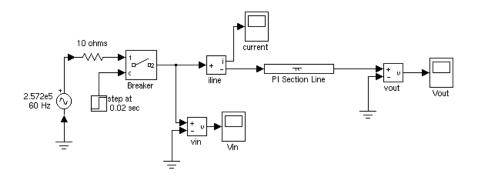
Select **All voltages and currents** to measure the sending end and receiving end voltages and currents of the line model.

Place a Multimeter block in your model to display the selected measurements during the simulation. In the **Available Measurements** list box of the Multimeter block, the measurement is identified by a label followed by the block name.

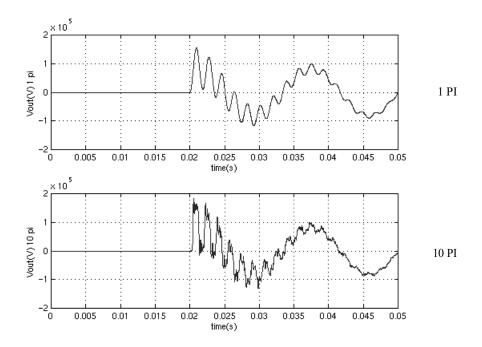
Measurement	Label
Sending end voltage (block input)	Us:
Receiving end voltage (block output)	Ur:
Sending end current (input current)	Is:
Receiving end current (output current)	Ir:

Example

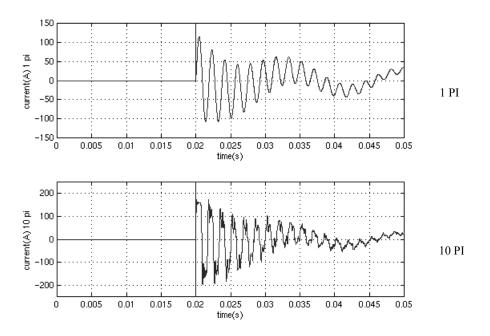
The psbpiline.mdl demo shows the line energization voltages and currents of a PI section line.



The results obtained with the line modeled by one PI section of 100 km and 10 PI sections of 10 km are shown.



PI Section Line



See Also Distributed Parameter Line

Powergui

Purpose

Graphical user interface for the analysis of circuits and systems

Library

powerlib

Description

The Powergui block provides useful graphical user interface (GUI) tools for the analysis of Power System Blockset models. Copy the Powergui block into the top level of your model and double-click the block to open the interface.

powergui

The Powergui block allows you to displays steady state values of measured current and voltages as well as all state variables (inductor currents and capacitor voltages) in a circuit.

The Powergui block allows you to modify the initial states in order to start the simulation from any initial conditions. The name of the state variables is the name of the block where the capacitor or the inductor is found, preceded by the Uc_label for capacitor voltages and by the IL_label for the inductor currents.

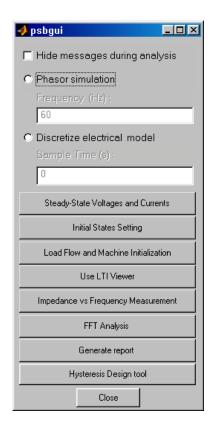
The Powergui block allows you to perform load flows and initialize three-phase networks containing three-phase machines so that the simulation starts in steady state. This option is available with circuits containing the following types of machines: Simplified Synchronous Machine, Synchronous Machine, or Asynchronous Machine blocks.

The Powergui block also displays impedance versus frequency plots when Impedance Measurement blocks are present in your circuit.

If you own the Control System Toolbox, the Powergui block can generate the state-space model (SS) of your system and automatically open the LTI Viewer interface for time and frequency domain responses.

The Powergui block can generate a report containing steady state values of the measurement blocks, the sources, the nonlinear models, and the states of your circuit. The report is saved in a file with the rep extension.

Dialog Boxes and Parameters



Hide messages during analysis

If selected, the echo messages of Power System Blockset are disabled during the analysis and simulation of the model.

Phasor simulation

If selected, Power System Blockset performs a phasor simulation of the model, at the frequency specified by the **Frequency** parameter.

Frequency (Hz)

Specify the frequency used by Power System Blockset to perform the phasor simulation of the model. The **Frequency** parameter is not available if the **Phasor simulation** parameter is not selected.

Discretize electrical model

If selected, Power System Blockset performs a discretization of the model. The sample time is specified by the **Sample time** parameter.

Sample time (s)

Specify the sample time used to discretize the state-space matrices of the linear part of the circuit. Set the **Sample time** parameter to a value greater than 0. The icon displays the value of the sample time.

Steady State Voltages and Currents

Open a window that displays the steady state voltages and currents of the model.

Initial states Setting

Open a window that allows you to display and modify initial voltages and currents of the model.

Load Flow and Machine Initializations

Open a window to perform load flows and machine initialization.

Use LTI Viewer

Open a window to use the LTI Viewer of the Control System Toolbox.

Impedance vs Frequency Measurements

Open a window that allows you to display the impedance vs frequency measurements of Impedance Measurement blocks of the model.

FFT Analysis

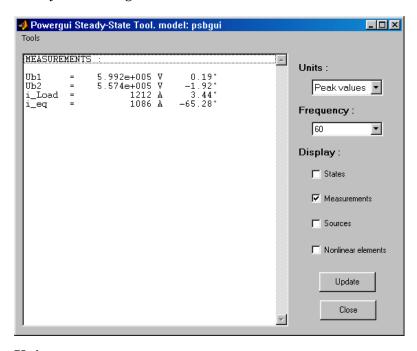
Open a window to use the FFT analysis tool of the Powergui block.

Generate Report

Open a window to generate a report of the steady state calculations.

Hysteresis Design Tool

Open a tool that allow you to design an hysteresis characteristic for the saturable core of the Saturable Transformer block and the Three-Phase Transformer blocks (two- and three-windings).



Steady state voltages and currents window:

Units

Set the **Units** parameter to **Peak values** to display the peak values of the selected values. Set the **Units** parameter to **RMS** to display the root-mean-square (RMS) values of the selected values.

Frequency

Allows you to choose the frequency, in hertz (Hz), that you are interested in to display the phasors. The **Frequency** parameter lists all the different frequencies of the electrical sources of the model.

States

If selected, the window displays the steady state phasors of the capacitor voltages and inductor currents of the circuit.

Measurements

If selected, the window displays the steady state phasor voltages and currents of the measurement blocks of the circuit.

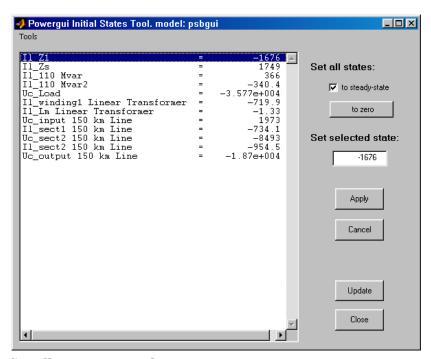
Sources

If selected, the window displays the steady state phasor voltages of the electrical sources of the circuit.

Nonlinear elements

If selected, the window displays the steady state voltages and currents of the nonlinear blocks of the circuit. Note that there is no nonlinear element in the model given in the example.

Initial Values of State Variables Window



Set all states to steady state

If selected, specify that Power System Blockset is to use the initial conditions for a steady state simulation. If you previously changed initial conditions, you must select the **Steady State** parameter in order to return to steady state initial conditions.

Set all states to 0

Sets the initial states to 0.

Set selected state

Sets the selected initial state to a specific value.

Apply

Apply the settings to the simulation.

Cancel

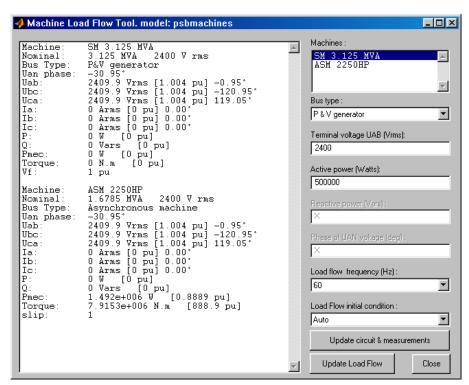
Cancel the last modifications.

Update

Update the list of initial state values.

Close

Close the window.



Load Flow and Machine Initialization Window

Machines

The Machines list box displays the names of the Simplified Synchronous Machines, the Synchronous Machines, the Asynchronous Machine, and the 3-Phase Dynamic Load blocks of your model. Select a machine or a load in the list box in order to set its parameters for the load flow.

Bus type

If the **Bus type** parameter is set to **PV Generator**, you can set the desired terminal voltage and active power of the machine. If the **Bus type** parameter is set to **Swing Bus**, you can set the desired terminal voltage, enter an active power guess, and specify the phase of the UAN terminal voltage of the machine.

If you select a machine that is an Asynchronous Machine block, you only have to enter the desired mechanical power delivered by the machine.

Terminal voltage Uab

Specifies the terminal voltage of the selected machine.

Active power

Specifies the active power of the selected machine.

Reactive power

Specifies the reactive power of the selected machine.

Phase of UAN voltage

Specifies the phase of the phase-to-ground voltage of phase A of the selected machine.

Load flow frequency

Specifies the frequency to be used in the load flow calculations (normally 60 Hz or 50 Hz).

Load Flow initial condition

If selected, the load flow starts with initial conditions corresponding to the previous load flow solution. Try this option if the load flow fails to converge.

Update circuit & measurements

Update the list of machines and the current values for the load flow.

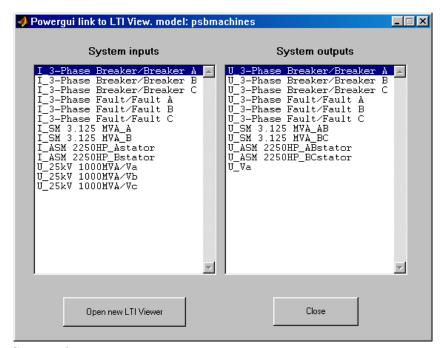
Update load flow

Executes the load flow calculations for the given load flow parameters

Close

Close the window

Link to the LTI Viewer Window



System inputs

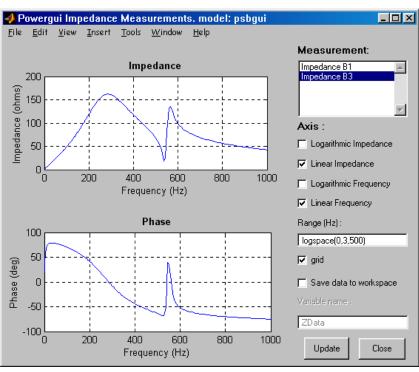
Lists the inputs of the state-space equivalent system of your circuit. Select the inputs to be used by the LTI Viewer.

System outputs

Lists the outputs of the state-space equivalent system of your circuit. Select the outputs to be used by the LTI Viewer.

Open new LTI Viewer

Generates the state-space model of the circuit and opens the LTI viewer for the selected system inputs and outputs.



Impedance vs. Frequency Measurement Window

Measurement

Lists the Impedance Measurement blocks of the model. Select the blocks you want to obtain the frequency response.

Axis

Defines the units of the impedance and the frequency axis.

Frequency range (Hz)

Specifies the frequency vector, in hertz (Hz). You can specify in that field any valid MATLAB expression defining a vector of frequencies, for example 0:2:1000 or linspace(0,1000,500). The default is logspace(0,3,50).

grid

If selected, a grid is displayed for the two graphs.

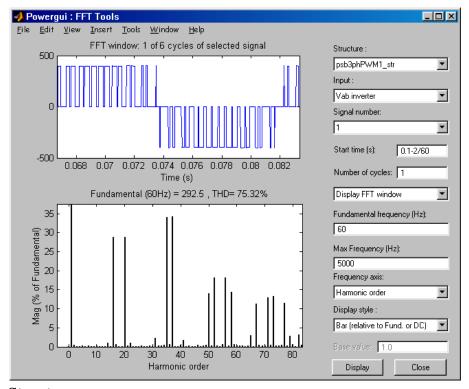
Save data to workspace

Data can be saved in a variable in the workspace. The name of the variable is defined by the **Variable name** parameter.

Update

Click the button to start the impedance versus frequency measurement and display results.

FFT Analysis Tool



Structure

List the structure with time variables that are present in your workspace. Structures with time variables are generated by the Scope blocks in your model.

Input

Select the input signal of the selected structure with time variables specified in the **Structure** parameter. Structures with time variables with multiple inputs can be generated by a Scope block that has multiple input ports.

Signal Number

Specify the indices of the selected input signal specified by the **Input** parameter. For example, the **Signal Number** parameter allows you to analyze the phase A signal of a three-phase signal connected to input 2 of a Scope block.

Start Time

Specify the start time for the FFT analysis.

Number of cycles

Specify the number of cycles for the FFT analysis.

Display FFT window

Select **Display entire signal** to display the entire selected signal in the upper graph of the window. Select **Display FFT window** to display only the portion of the signal where the FFT analysis applies.

Fundamental frequency

Specify the fundamental frequency for the FFT analysis.

Max Frequency

Specify the maximum frequency for the FFT analysis.

Frequency Axis

Select **Hertz** to displays the FFT analysis in hertz. Select **Harmonic order** to display the FFT analysis in harmonic order relative to the fundamental frequency.

Display style

Select **bar** (**relative to Fund. or DC**) to display the FFT analysis as a bar graph relative to the fundamental frequency. Select **bar** (**relative to specified base**) to display the FFT analysis as a bar graph relative to the base defined by the **Base value** parameter.

Powergui

Select **list** (**relative to Fund. or DC**) to display the FFT analysis as a list relative to the fundamental frequency. Select **list** (**relative to specified base**) to display the FFT analysis as a list relative to the base defined by the **Base value** parameter.

Base value

Specify a base value for the display of harmonics.

Display

Display the FFT analysis results for the selected measurement.

🐠 Hysteresis Transformer Design Tool _ 🗆 × Characteristics Special Help Segments: Hysteresis curve for file: hysteresis.mat 256 \blacksquare Remanent flux Fr (pu): 1.05 Saturation flux Fs (pu): 0.5 Saturation current Is (pu): 0.03 Coercive current lc (pu): 0.00499999 Flux dF/dLat coercive current: 2000 Saturation region currents (pu): -0.5 [0.03 0.1] Saturation region fluxes (pu): [1.1 1.5] Transfo Nominal Parameters [P(VA),V(Vrms),f(Hz)]: [150e6 500e3/sqrt(3) 60] -0.01 -0.02 0.01 0.02 0.03 -0.03Parameter units: Current pu Display Close Zoom around hysteresis

Hysteresis Design Tool

Segments

Specify the number of linear segments the characteristic uses to define the curve.

Remanent flux Fr

Specify the remanent flux point of the hysteresis characteristic.

Saturation Flux

Specify the saturation flux point of the hysteresis characteristic.

Saturation current Is

Specify the saturation current point where the saturation characteristic enters in action.

Coercive current Ic

Specify the coercive current point of the hysteresis characteristic.

dF/dl at coercive current

Set the slope of the flux at the coercive current point.

Saturation region currents

Specify the current points that define the saturation characteristic. The number of specified points must be the same as for the **Saturation region fluxes** parameter. You only need to specify the positive part of the characteristic.

Saturation region fluxes

Specify the flux points that define the saturation characteristic. The number of specified points must be the same as for the **Saturation region currents** parameter. You only need to specify the positive part of the characteristic.

Transfo Nominal Parameters

Specify the nominal parameters used in the conversion of the hysteresis parameters.

Parameter units

Convert from SI to p.u., or from p.u. to SI, the parameters that define the hysteresis characteristic.

Example

You can open the demos of Power System Blockset and double-click the Powergui block in the model. For each demo you can use the tools of the Powergui block and look at the initial values and steady state values of the inductor currents and capacitor voltages. For demos containing machines, you can edit and perform a Machine load flow analysis.

Purpose

Generate pulses for a carrier-based pulse width modulator (PWM)

Library

Extras/Control Blocks

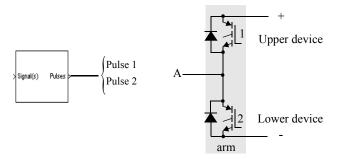
Description



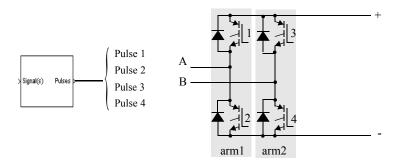
The PWM Generator block generates pulses for carrier-based pulse width modulation (PWM) systems. The block can be used to fire the self-commuted devices (FETs, GTOs, or IGBTs) of single-phase, two-phase, three-phase, or a combination of two three-phase bridges.

The number of pulses generated by the PWM Generator block is determined by the number of bridge arms you have to control:

• Two pulses are generated for a one-arm bridge. Pulse 1 fires the upper device and pulse 2 fires the lower device (shown for the IGBT device).

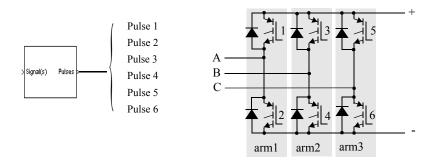


• Four pulses are generated for a two-arm bridge. Pulses 1 and 3 fire the upper devices of the first and second arm. Pulses 2 and 4 fire the lower devices.



PWM Generator

• Six pulses are generated for a three-arm bridge. Pulses 1, 3, and 5 fire the upper devices of the first, second, and third arms. Pulses 2, 4, and 6 fire the lower devices.



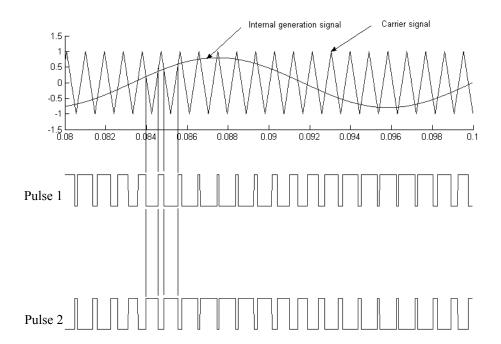
• Twelve pulses are generated for a double three-arm bridge. The first six pulses (1 to 6) fire the six devices of the first three-arm bridge and the last six pulses (7 to 12) fire the six devices of the second three-arm bridge.

The pulses are generated by comparing a triangular carrier waveform to a reference sinusoidal signal. The reference signal can be generated by the PWM generator itself, or it can be generated from a signal connected at the input of the block. In the second option, the PWM Generator needs one reference signal to generate the pulses for a single- or a two-arm bridge, or it needs a three-phase reference signal to generate the pulses for a three-phase bridge (single or double bridge).

The amplitude (modulation), phase, and frequency of the reference signals are set to control the output voltage (on the AC terminals) of the bridge connected to the PWM Generator block.

The pulses that fire the two devices of an arm bridge are complementary one to the other; for example, the pulse 4 is low (0) when the pulse 3 is high (1). This is illustrated in the next two figures.

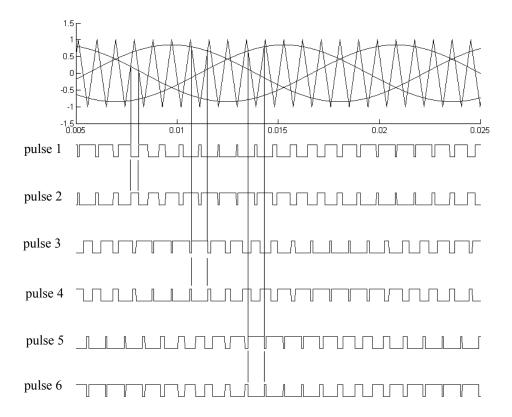
The following figure displays the two pulses generated by the PWM Generator block when programmed to control a one-arm bridge.



The triangular carrier signal is compared to the sinusoidal reference signal. Each time the two signals become equal (at each crossing point), the value of the pulses passes from 0 to 1, or 1 to 0, depending on their previous value.

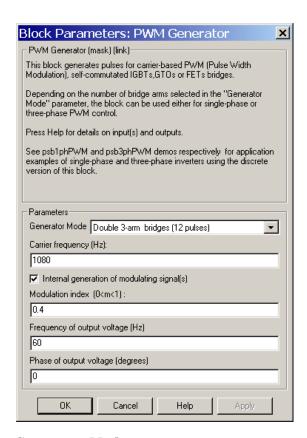
PWM Generator

The following figure displays the six pulses generated by the PWM Generator block when programmed to control a three-arm bridge.



Pulse 2 is the complement of pulse 1, pulse 4 the complement of pulse 3, and pulse 6 the complement of pulse 5. Note that, unlike the pulses generated by the Synchronized 6-Pulse Generator block, the pulses generated by the PWM Generator block are of variable width.

Dialog Box and Parameters



Generator Mode

Specify the number of pulses to generate. The number of pulses is proportional to the number of bridge arms to fire. Select for example **Double 3-arm bridges (12 pulses)** to fire the self-commuted devices of two six-pulse bridges connected in a twelve-pulse bridge configuration.

Carrier frequency (Hz)

The frequency, in hertz, of the carrier triangular signal.

Internal generation of modulating signal (s)

If selected, the modulating signal is generated by the block. Otherwise, external signals are used for pulse generation.

Modulation index (0 < m < 1)

The amplitude of the reference internal signal. The **Modulation index** must be greater than 0, and lower than or equal to 1. This parameter is used to control the amplitude of the output voltage of the controlled bridge.

The **Modulation index** parameter is visible only if the **Internal generation of modulating signal (s)** parameter is selected.

Frequency of output voltage (Hz)

The frequency, in hertz, of the reference internal signal. This parameter is used to control the frequency of the output voltage of the controlled bridge. The **Frequency of output voltage** (**Hz**) parameter is visible only if the **Internal generation of modulating signal** (s) parameter is selected.

Phase of output voltage (degrees)

The phase, in degrees, of the reference internal signal. This parameter is used to control the phase of the output voltage of the controlled bridge. The **Phase of output voltage parameter** is visible only if the **Internal generation of modulating signal (s)** parameter is selected.

Inputs and Outputs

signal(s)

The input is the reference sinusoidal voltage when **Internal generation of modulating signal** is not selected. Connect this input to a single-phase sinusoidal signal when the block is used to control a single- or a two-arm bridge, or to a three-phase sinusoidal signal when the PWM Generator block is controlling one or two three-phase bridges. The input can be left unconnected when **Internal generation of modulating signal (s)** is selected.

Pulses

The output contains the two, four, six, or twelve pulse signals used to fire the self-commuted devices (MOSFETs, GTOs, or IGBTs) of single-phase, two-phase, three-phase, or a combination of two three-phase bridges.

Example

See psb1phPWM and psb3phPWM demos for examples of single-phase and three-phase inverters.

Purpose

Measure the root mean square (RMS) value of a signal

Library

Extras/Measurements

Description

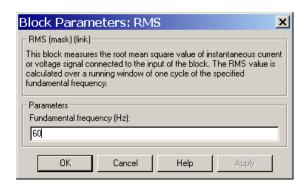


This block measures the root mean square value of an instantaneous current or voltage signal connected to the input of the block. The RMS value of the input signal is calculated over a running window of one cycle of the specified fundamental frequency.

$$rms(f(t)) = \sqrt{abs \left(\frac{1}{T} \int_{t}^{(t+T)} f(t)^{2}\right)}$$

f(t): input signal, T=1/fundamental frequency

Dialog Box and Parameters

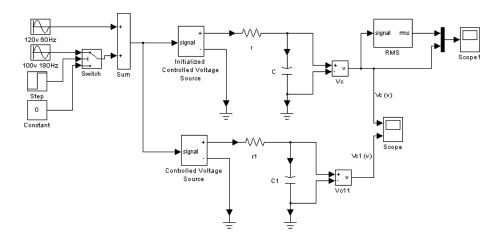


Fundamental frequency (Hz)

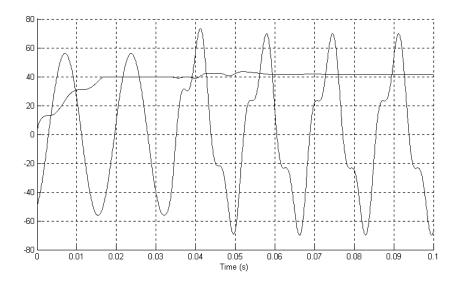
The fundamental frequency, in hertz, of the input signal.

Example

In the psbcontrolvolt demo, the rms value of the voltage of the C capacitor is measured with a Voltage Measurement block and a RMS block. The Controlled Voltage Source block introduces a third harmonic (180 Hz) in the voltage at t=0.4 seconds.



At the beginning of the simulation, the RMS block needs one cycle of the fundamental frequency (60Hz) to calculate the rms value of the voltage. At t=0.4 seconds the rms value slightly increases because of the addition of the third harmonic in the signal. Again, the RMS block needs one cycle of the fundamental signal to stabilize and give the correct result.



Saturable Transformer

Purpose

Implement a two- or three-winding saturable transformer

Library

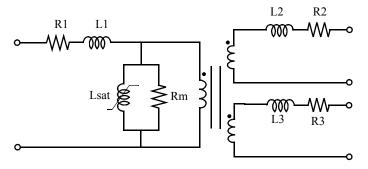
Elements

Description

The Saturable Transformer block model shown consists of three coupled windings wound on the same core.







The model takes into account the winding resistances (R1 R2 R3) and the leakage inductances (L1 L2 L3) as well as the magnetizing characteristics of the core, which is modeled by a resistance Rm simulating the core active losses and a saturable inductance Lsat. The saturation characteristic is specified as a piecewise linear characteristic.

The Per Unit Conversion

In order to comply with industry practice, you must specify the resistance and inductance of the windings in per unit (p.u.). The values are based on the transformer rated power Pn in VA, nominal frequency fn in Hz, and nominal voltage Vn, in Vrms, of the corresponding winding. For each winding the per unit resistance and inductance are defined as

$$R(\text{p.u.}) = \frac{R(\Omega)}{R_{base}}$$

$$L(\text{p.u.}) = \frac{L(H)}{L_{base}}$$

The base resistance and base inductance used for each winding are

Saturable Transformer

$$egin{aligned} R_{base} &= rac{\left(Vn
ight)^2}{Pn} \ L_{base} &= rac{Rbase}{2\pi f_n} \end{aligned}$$

For the magnetization resistance Rm, the p.u. values are based on the transformer rated power and on the nominal voltage of winding 1.

The default parameters of winding 1 specified in the dialog box section give the following bases:

$$Rbase = \frac{(735e3/sqrt(3))^2}{250e6} = 720.3\Omega$$
 $Lbase = \frac{720.3}{2\pi60} = 1.91H$

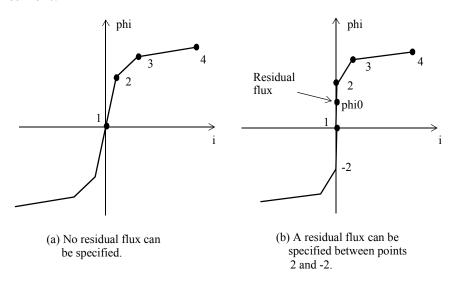
For example, the winding 1 parameters are R1 = 1.44 Ω and L1 = 0.1528 H. The corresponding values to enter in the dialog box are

$$R_1 = \frac{1.44\Omega}{720.3\Omega} = 0.002 \text{ p.u.}$$

$$L_1 = \frac{0.1528H}{1.91H} = 0.08 \text{ p.u.}$$

Saturation Characteristic

The saturation characteristic of the Saturable Transformer block is defined by a piecewise linear relationship between the flux and the magnetization current.



Therefore, if you want to specify a residual flux phi0, the second point of the saturation characteristic should correspond to a null current, as shown in the figure (b).

The saturation characteristic is entered as (i, phi) pair values in per units, starting with pair (0, 0). Power System Blockset converts the vector of fluxes Φ pu and the vector of currents Ipu into standard units to be used in the saturation model of the Saturable Transformer block

$$\Phi = \Phi_{pu} \Phi_{base}$$

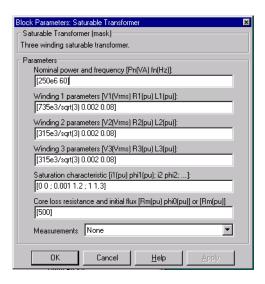
$$I = I_{pu} I_{base}$$

where the base flux (Φ base) and base current (Ibase) are the peak values obtained at nominal voltage power and frequency:

$$I_{base} = rac{Pn}{V_1}\sqrt{2} \qquad \Phi_{base} = rac{V_1}{2\pi f_n}\sqrt{2}$$

Saturable Transformer

Dialog Box and Parameters



Nominal power and frequency

The nominal power rating, Pn, in volt amperes (VA), and frequency, in hertz (Hz), of the transformer.

Winding 1 parameters

The nominal voltage in volts rms, resistance, and leakage inductance in p.u. for winding 1.

Winding 2 parameters

The nominal voltage in volts rms, resistance, and leakage inductance in p.u. for winding 2.

Winding 3 parameters

The nominal voltage in volts rms, resistance, and leakage inductance in p.u. for winding 3. Setting the **Winding 3 parameters** parameter to 0 implements a Saturable Transformer block with only two windings.

Saturation characteristic

Specify a series of current (p.u.) - flux (p.u.) pairs starting with (0,0).

Core loss resistance and initial flux

Specify the active power dissipated in the core by entering the equivalent resistance Rm in p.u. For example, to specify a 0.2% of active power core loss at nominal voltage, use Rm = 500 p.u. You can also specify the initial flux phi0 (p.u). This initial flux becomes particularly important when the transformer is energized. If phi0 is not specified, the initial flux is automatically adjusted so that the simulation starts in steady state.

Measurements

Select **Winding voltages** to measure the voltage across the winding terminals of the Linear Transformer block.

Select **Winding currents** to measure the current flowing through the windings of the Linear Transformer block.

Select **Flux and magnetization current** to measure the magnetization current of the Linear Transformer block.

Select **All Measurements (V, I, Flux)** to measure the winding voltages and currents plus the magnetization current.

Place a Multimeter block in your model to display the selected measurements during the simulation.

In the **Available Measurements** list box of the Multimeter block, the measurements are identified by a label followed by the block name.

Measurement	Label
Winding voltages	Uw1:, Uw2:, Uw3:
Winding currents	Iw1:, Iw2:, Iw3:
Magnetization current	Imag:
Flux	Flux:

Inputs and Outputs

Input one, output one, and output three (if it exists) are at the same instantaneous polarity.

Saturable Transformer

If you set the entry for the third winding to 0, the blockset implements a transformer with two windings and a new icon is displayed:



Limitations

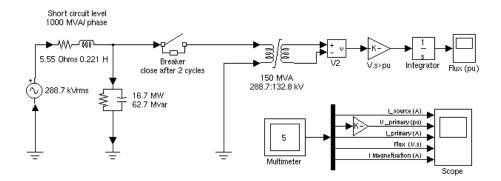
Windings can be left floating (that is, not connected by an impedance to the rest of the circuit). However, the floating winding is connected internally to the main circuit through a resistor. This invisible connection does not affect voltage and current measurements.

The flux saturation model does not include hysteresis.

Example

Energization of one phase of a three-phase 450~MVA, 500/230~kV transformer on a 3000~MVA source. The transformer parameters are

Nominal power: 150e6, 60 Hz, Winding 1 parameters (primary): 500e3 Vrms/sqrt(3), R = 0.002 p.u. X = 0.08 p.u., winding 2 parameters (secondary): 230e3 Vrms/sqrt(3), R = 0.002 p.u. X = 0.08 p.u., Core loss resistance: 1000 p.u., Saturation characteristic: [0 0; 0 1.2; 1.0 1.52], residual flux = 0.8 p.u.



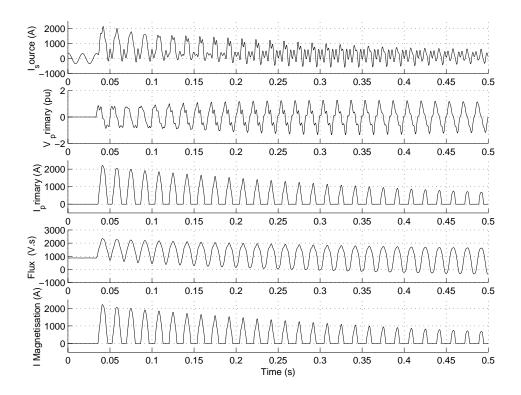
This circuit is available in the psbxfosaturable.mdl file.

As the source is resonant at the fourth harmonic, you can observe a high fourth harmonic content in the secondary voltage. In this circuit the flux is calculated in two ways:

Saturable Transformer

- By integrating the secondary voltage
- By using the Multimeter block

Simulation of this circuit illustrates the saturation effect on the transformer current and voltage:



See Also Linear Transformer, Mutual Inductance

Series RLC Branch

Purpose

Implement a series RLC branch

Library

Elements

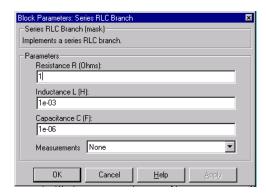
Description

The Series RLC Branch block implements a single resistor, inductor, or capacitor, or a series combination of these. To eliminate either the resistance, inductance, or capacitance of the branch, the R, L, and C values must be set respectively to 0, 0, and infinity (inf). Only existing elements are displayed in the block icon.

>-W-**m**--|->

Negative values are allowed for resistance, inductance, and capacitance.

Dialog Box and Parameters



Resistance R

The branch resistance, in ohms (Ω) .

Inductance L

The branch inductance, in henries (H).

Capacitance C

The branch capacitance, in farads (F).

Measurements

Select **Branch voltage** to measure the voltage across the Series RLC Branch block terminals.

Select **Branch current** to measure the current flowing through the Series RLC Branch block.

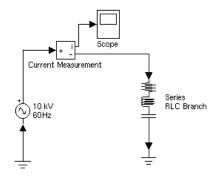
Select **Branch voltage and current** to measure the voltage and the current of the Parallel RLC Branch block.

Place a Multimeter block in your model to display the selected measurements during the simulation. In the **Available Measurements** list box of the Multimeter block, the measurement is identified by a label followed by the block name.

Measurement	Label
Branch voltage	Ub:
Branch current	Ib:

Example

Obtain the frequency response of a fifth-harmonic filter (tuned frequency = 300 Hz) connected on a 60 Hz power system. This example is available in the psbseriesbranch.mdl file.



The network impedance in Laplace domain is

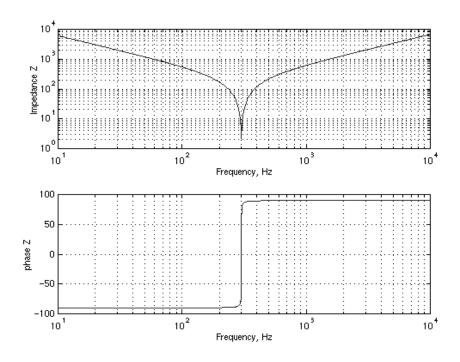
$$Z(s) = \frac{V(s)}{I(s)} = \frac{LCs^2 + RCs + 1}{Cs}$$

To obtain the frequency response of the impedance you have to get the state-space model (A B C D matrices) of the system.

This system is a one-input (Vsource) and one-output (Current Measurement block) system. If you own the Control System Toolbox, you can get the transfer function Z(s) from the state-space matrices and the bode function as follows:

```
[A,B,C,D] = power2sys( psbseriesbranch );
freq = logspace(1,4,500);
w = 2\pi * freq;
[Ymag, Yphase
] = bode(A,B,C,D,1,w);
% invert Y(s) to get Z(s)
Zmag = 1./Ymag;
Zphase = -Yphase;
subplot(2,1,1)
loglog(freq,Zphase)
grid
title(5th harmonic filter)
xlabel('Frequency, Hz')
ylabel('Impedance Zmag')
subplot(2,1,2)
semilogx(freq,Zphase)
xlabel('Frequency, Hz')
ylabel('phase Z')
grid
```

You can also use the Impedance Measurement block and the Powergui block to plot the impedance as a function of frequency. In order to measure the impedance you must disconnect the voltage source.



See Also

Parallel RLC Branch, Parallel RLC Load, Series RLC Load

Series RLC Load

Purpose Implement a linear series RLC load

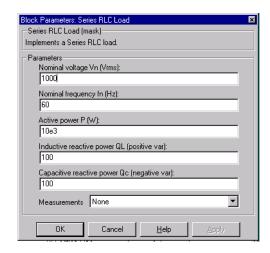
Library Elements

Description The Series RLC Load block implements a linear load as a series combination of

R L C elements. At the specified frequency, the load exhibits constant

impedance and its power is proportional to the square of the applied voltage. Only elements associated with nonzero powers are displayed in the block icon.

Dialog Box and Parameters



Nominal voltage Vn

The nominal voltage of the load, in volts rms.

$Nominal\ frequency\ fn$

The nominal frequency, in hertz.

Active power P

The active power of the load, in watts.

Inductive reactive power QL

The inductive reactive power QL, in vars. Specify a positive value, or 0.

Capacitive reactive power QC

The capacitive reactive power QC, in vars. Specify a positive value, or 0.

Measurements

Select **Branch voltage** to measure the voltage across the Series RLC Load block terminals.

Select **Branch current** to measure the current flowing through the Series RLC Load block.

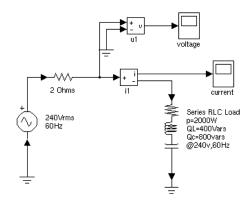
Select **Branch voltage and current** to measure the voltage and the current of the Series RLC Load block.

Place a Multimeter block in your model to display the selected measurements during the simulation. In the **Available Measurements** list box of the Multimeter block, the measurement is identified by a label followed by the block name:

Measurement	Label
Branch voltage	Ub:
Branch current	Ib:

Example

The psbseriesload.mdl demo uses a Series RLC Load block to implement a simple load.



See Also

Parallel RLC Branch, Parallel RLC Load, Series RLC Branch

Purpose

Model the dynamics of a simplified three-phase synchronous machine

Library

Machines

Description

The Simplified Synchronous Machine block models both the electrical and mechanical characteristics of a simple synchronous machine.

The electrical system for each phase consists of a voltage source in series with an RL impedance, which implements the internal impedance of the machine. The value of R can be 0 but the value of L must be positive.

The Simplified Synchronous Machine block implements the mechanical system described by

$$\Delta\omega(t) = \frac{1}{2H} \int_{0}^{t} (Tm - Te)dt - Kd\Delta\omega(t)$$
$$\omega(t) = \Delta\omega(t) + \omega_{0}$$

where

 $\Delta \omega = S$ peed variation with respect to speed of operation

H = Constant of inertia

Tm = Mechanical torque

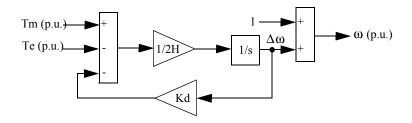
Te = Electromagnetic torque

Kd = Damping factor

 $\omega(t) = M$ echanical speed of the rotor

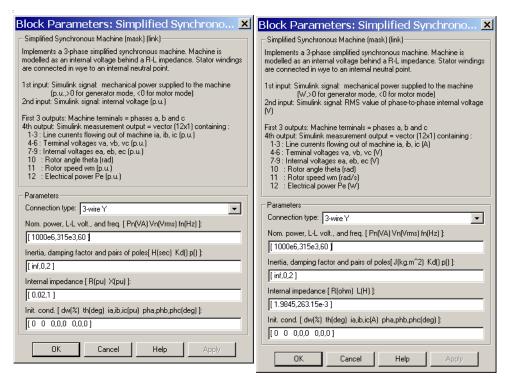
 $\omega_0 = S$ peed of operation (1 p.u.)

Although the parameters can be entered in either SI units or per unit in the dialog box, the internal calculations are done in per unit. The following block diagram illustrates how the mechanical part of the model is implemented. Notice that the model computes a deviation with respect to the speed of operation, and not the absolute speed itself.



Dialog Box and Parameters

In the **powerlib** library you can choose between two Simplified Synchronous Machine blocks to specify the electrical and mechanical parameters of the model.



Per Unit (p.u.) Dialog Box

SI Units Dialog Box

Connection type

Specifies number of wires used in three-phase Y connection: either three-wire (neutral not accessible) or four-wire (neutral is accessible).

Nominal

The nominal apparent power Pn (VA), frequency fn (Hz), and rms line-to-line voltage Vn (V). Used to compute nominal torque and convert SI units to p.u.

Mechanical

The moment of inertia (N.m or p.u.) and damping factor. The damping factor has been scaled to act like the damping factor of a second-order system. This means, for example, that for no overshoot and minimum settling time, a damping factor of 0.9 is used.

Internal impedance

The resistance R (Ω or p.u.) and reactance L (H or p.u.) for each phase.

Initial conditions

The initial speed deviation (% of nominal), rotor angle (deg), line current magnitudes (A or p.u.), and phase angles (deg). These values can be computed by the load flow utility of the Powergui block.

Note These two blocks simulate exactly the same simplified synchronous machine model; the only difference is the way of entering the parameter units.

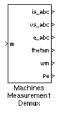
Inputs and Outputs

The first input of the Simplified Synchronous Machine block is the mechanical power supplied to the machine. This input can be a constant or the output of the Hydraulic Turbine and Governor block. The frequency of the voltage sources depends on the mechanical speed of the machine. The amplitude of these voltages is given by the second input of the block, which can be a constant or the output of a voltage regulator. If you use SI units these two inputs should be in watts and volts phase-to-phase rms. If you use p.u. both inputs should be in p.u.

The first three outputs are the electrical terminals of the stator. The last output of the block is a vector containing the following 12 variables:

- 1-3: Line currents (flowing out of the machine) ia, ib, ic
- 4-6: Terminal voltages va, vb, vc
- 7-9: Internal voltages ea, eb, ec
- 10: Mechanical angle θ
- 11: Rotor speed ω
- 12: Electrical power Pe

These variables can be demultiplexed by using the special Machines Measurement Demux block provided in the Machines library.

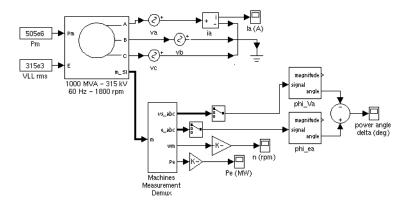


Assumptions

The electrical system of the Simplified Synchronous Machine block consists solely of a voltage source behind a synchronous reactance and resistance. All the other self and magnetizing inductances of the armature, field, and damping windings are neglected. The three voltage sources and RL impedance branches are Y-connected (three wires). The load might or might not be balanced.

Example

The psbsimplealt.mdl demo uses the Simplified Synchronous Machine block to represent a 1000 MVA 315 kV equivalent source connected to an infinite bus (three AC Voltage Source blocks). The Synchronous Machine block is used as a synchronous generator. The internal resistance of the machine is set to 0.02 p.u., or 1.9845 ohms. Its inductance is set in such a way that the total impedance is 1 p.u. (L = 263.15 mH). The inertia of the machine is 56290 kg.m 2 .



In this example, the machine has an initial speed deviation of 0.5%. The initial mechanical angle and phase currents i_a , i_b , and i_c are set to 0. The power transfer between the machine and the bus is given by the following relation:

$$P_T = \frac{V_1 \times V_2}{X} \times \sin \delta$$

 P_T = power transfer (500 MW)

 V_1 = machine voltage (315 kV)

 V_2 = bus voltage (315 kV)

 $X = \text{total reactance} (263.15 \text{ mH x } 120 \text{ x } \pi)$

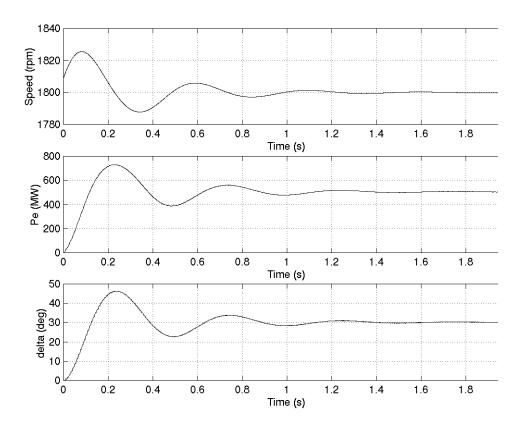
 δ = electrical angle difference (power angle δ) between machine internal voltage and terminal voltage

With the preceding parameters, the steady state internal voltage is 30° ahead of the terminal voltage (δ = +30°). The machine is supplied with 505 MW of mechanical power in order to compensate for its resistive losses. The electrical angle δ is displayed as the phase difference between the internal and terminal voltages of phase A. With simulation parameters set as follows, the results shown below are obtained:

• Integrator type: Stiff, ode15s

• Stop time: 2.0

• Integration options: Use default settings



The speed vs. time graph clearly shows that the machine is initially running at a speed of 1.005 p.u. (1809 rpm) and that speed stabilizes itself at its nominal value of 1800 rpm. As expected, the electrical power supplied by the machine stabilizes at 500 MW. The power angle δ also settles at its expected value of 30°. The mechanical system is clearly underdamped, the damping factor being set to 0.3.

See Also

Excitation System, Hydraulic Turbine and Governor, Powergui, Steam Turbine and Governor, Synchronous Machine

Purpose

Model the dynamics of a speed governing system, steam turbine, and multimass shaft

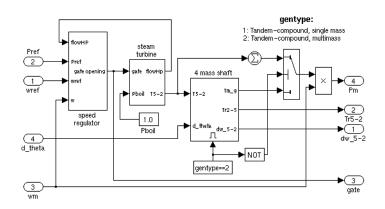
Library

Machines

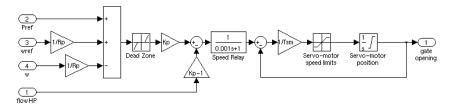
Description



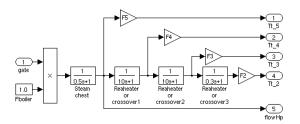
The Steam Turbine and Governor block implements a complete tandem-compound steam prime mover, including a speed governing system, a four-stage steam turbine, and a shaft with up to four masses.



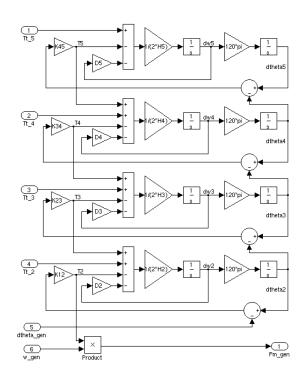
The speed governing system consists of a proportional regulator, a speed relay and a servomotor controlling the gate opening. It is similar to one of the models proposed in [1].



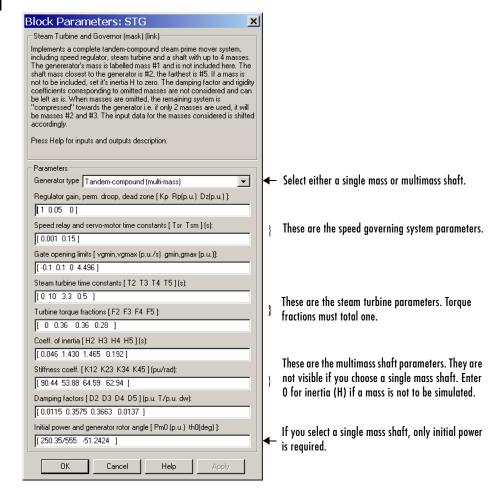
The steam turbine has four stages, each modeled by a first-order transfer function. The first stage represents the steam chest while the three other stages represent either reheaters or crossover piping. The boiler is not modeled and boiler pressure is constant at 1.0 p.u. Fractions F2 to F5 are used to distribute the turbine power to the various shaft stages.



The shaft models a four-mass system, which is coupled to the mass in the Synchronous Machine model for a total of five masses. The machine's mass is labeled mass #1. The mass in the Steam Turbine and Governor block, which is closest to the machine's mass, is mass #2, while the mass farthest from the machine is mass #5. The shaft is characterized by mass inertias H, damping factors D, and rigidity coefficients K. If you choose to simulate a single-mass shaft, the entire four-mass shaft subsystem in the Steam Turbine and Governor block is disabled and all the torque from the turbine is added together and applied to the machine's mass.



Dialog Box and Parameters



Generator type

Specifies rotor type: single mass or multimass tandem-compound. If you choose a single-mass system, the multimass shaft subsystem in the Steam Turbine and Governor block is disabled and the turbine's output torques are summed together and applied to the single mass in the Synchronous Machine block.

Regulator

The gain Kp, permanent droop Rp(p.u.), and dead-zone width Dz(p.u.). Set gain to 3 if you want to use the steam flow feedback loop. Otherwise, set gain to 1.

Time constants

The speed relay and gate servomotor time constants Tsr (s) and Tsm (s).

Gate Limits

The minimum and maximum gate opening speed vgmin and vgmax (both in p.u./s), and minimum and maximum gate opening gmin and gmax (both in p.u.).

Turbine time constants

The turbine time constants T2 to T5 (s). Numbered consistently with turbine torque fractions and mass numbers, i.e., T5 is the time constant of the first turbine stage, which models the steam chest.

Turbine torque fractions

The turbine torque fractions F2 to F5. Must total 1, otherwise an error message appears. Fraction numbers correspond to mass numbers, i.e., F2 is the fraction of torque to be applied to mass #2 of the multimass shaft.

Multi-mass shaft

Only visible if generator type is multimass. Coefficients of inertia H2 to H5 (s), stiffness coefficients K12 to K45 (p.u./rad), and damping factors D2 to D5 (p.u. torque / p.u. speed deviation) associated with the masses of the multimass shaft. K12 corresponds to the rigidity coefficient between masses #1 and #2, and so on.

Note If you do not want to simulate all four masses in the multimass shaft, simply set the inertia of unwanted masses to 0. The rigidity coefficient and damping factor corresponding to omitted masses are not considered. When masses are not simulated, the remaining system is "compressed" toward the generator, i.e., if only two masses are used (excluding the generator), they are masses #2 and #3. The input data for the masses considered are shifted accordingly. In any case, inertias must be consistent with torque fractions. You cannot set an inertia to 0 and set the corresponding torque fraction to a

nonzero value. However, you can set a torque fraction to 0 and set the corresponding mass inertia to a nonzero value.

Initial conditions

If the shaft is multimass, enter the initial mechanical power Pm0 (p.u.) and initial generator angle θ e0 (deg). If the shaft is single mass, enter only initial mechanical power.

Initial mechanical power can be computed by the load flow utility of the Powergui block. Initial angle is also computed by the load flow utility and is written in the associated Synchronous Machine block dialog box.

Inputs and Outputs

The first input is the speed reference, in p.u. It is normally connected to a Constant block with the value set to 1.0 p.u.

The second input is the electrical power reference, in p.u. It is set to a constant value corresponding to the initial active power drawn from the Synchronous Machine block connected to the Steam Turbine and Governor block.

The third input is the generator's speed, in p.u. This is one of the signals in the last output of the Synchronous Machine model (internal variables).

The fourth input is the generator's power angle deviation. It is also one of the signals in the last output of the Synchronous Machine model (internal variables).

The first output is a vector containing the speed deviations, in p.u., of masses #5 to #2, in that order.

The second output is also a vector containing the torques, in p.u., transmitted by masses #5 to #2.

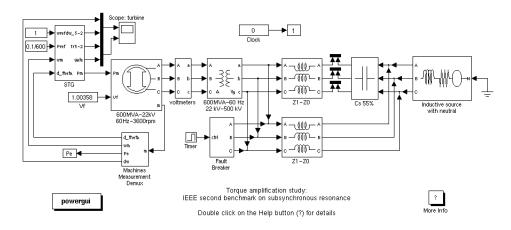
The third output is the mechanical power, in p.u., that you must connect to the first input of a Synchronous Machine block.

Example

The psbthermal.mdl demo illustrates the use of the Steam Turbine and Governor block. This system is an IEEE benchmark used to study subsynchronous resonance and particularly torque amplification after a fault on a series-compensated power system [2]. It consists in a single generator connected to an infinite bus via two transmission lines, one of which is series

compensated. The subsynchronous mode introduced by the compensation capacitor after a fault has been applied and cleared excites the oscillatory torsional modes of the multimass shaft and the torque amplification phenomenon can be observed. Open the Simulink diagram by typing psbthermal.

This system is slightly different from the one presented in [2]. Since we are using the Synchronous Machine mass as the first mass, we cannot model the exciter's mass as is done in [2]. Therefore, our system has only three masses, representing the generator's rotor (mass #1), and the turbine's low and high pressure stages (masses #2 and #3, respectively).



In order to start the simulation in steady state, a load flow was performed on the system, setting the generator as a **PV generator** with initial power of 100 kW (1e5 W). This is done to simulate an initially unloaded generator. The load flow returns initial mechanical power of 100 010 W. This value was converted into p.u. by dividing it by the generator's nominal VA rating (600e6 VA) and the result was entered as the first initial condition in the Steam Turbine and Governor block. The second initial condition is the generator's initial angle. This value is computed by the load flow and is written in the initial conditions vector of the generator. After the load flow is finished, you can open the Synchronous Machine block dialog box and copy the initial angle in the Steam Turbine and Governor block dialog box. The Steam Turbine and Governor block is now correctly initialized. The electrical power (load) reference, the

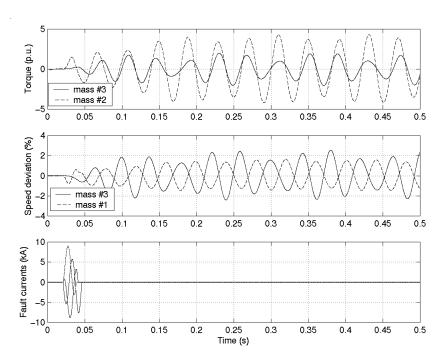
second input of the Steam Turbine and Governor block, is set to the desired electrical power supplied by the generator, in p.u. (1e5/600e6, or 0.1/600).

This test is performed without regulators. The speed governing system is forced to output a constant value by setting the gate opening limits very close to each other, around the initial gate opening, which is also the initial mechanical power in p.u. (100 010/600e6, or 0.00016668 p.u.). The machine's excitation voltage is also set to a constant value (1.00358 p.u.), which is computed by the load flow.

Set the simulation parameters as follows:

- Integrator type: Variable-step, ode23tb
- Stop time: 0.5 s
- Integration options: Use default options, except for Max step size, which you set to 50e-6. This is not absolutely required, but the simulation runs faster if this value is set as prescribed.

Run the simulation by choosing **Start** from the **Simulation** menu. Once the simulation is completed, observe the mass speed deviations and torques and the fault current.



The peak values of all these signals correspond within 3% to those given in Table 5, case 1A, of [2]. The torque amplification is clearly observed on all masses of the shaft system. The high-pressure mass (#3) transmits a peak torque of 1.91 p.u. to the low-pressure mass (#2), while the low-pressure mass transmits a peak torque of 4.05 p.u. to the generator's rotor (mass #1).

References

[1] IEEE committee report, "Dynamic models for steam and hydro turbines in power system studies," *IEEE Transactions on Power Apparatus and Systems*, Vol. PAS-92, No. 6, 1973, pp. 1904-1915.

[2] IEEE Subsynchronous resonance working group, "Second benchmark model for computer simulation of subsynchronous resonance," *IEEE Transactions on Power Apparatus and Systems*, Vol. PAS-104, No. 5, 1985, pp. 1057-1066.

See Also

Excitation System, Hydraulic Turbine and Governor, Powergui, Synchronous Machine

Purpose

Implement a metal-oxide surge arrester

Library

Elements

Description

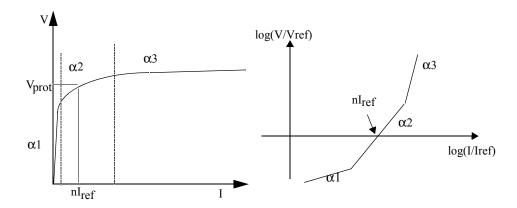
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The Surge Arrester block implements a highly nonlinear resistor used to protect power equipment against overvoltages. For applications requiring high power dissipation, several columns of metal-oxide discs are connected in parallel inside the same porcelain housing. The nonlinear V-I characteristic of each column of the surge arrester is modeled by a combination of three exponential functions of the form

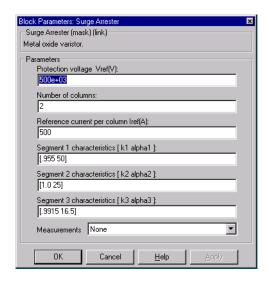
$$\frac{V}{V_{ref}} = K_i \left(\frac{I}{I_{ref}}\right)^{1/\alpha_i}$$

The protection voltage obtained with a single column is specified at a reference current (usually 500 A or 1 kA). Default parameters k and α given in the dialog box fit the average V-I characteristic provided by the main metal-oxide arrester manufacturers and they do not change with the protection voltage. The required protection voltage is obtained by adding discs of zinc oxide in series in each column.

This V-I characteristic is graphically represented as follows (on a linear scale and on a logarithmic scale).



Dialog Box and Parameters



Protection voltage Vref

The protection voltage of the Surge Arrester block, in volts (V).

Number of columns

The number of metal-oxide disc columns. The minimum is one.

Reference current per column Iref

The reference current of one column used to specify the protection voltage, in amperes (A).

Segment 1 characteristic

The K and $\boldsymbol{\alpha}$ parameters of segment 1.

Segment 2 characteristic

The K and α parameters of segment 2.

Segment 3 characteristic

The K and α characteristics of segment 3.

Measurements

Select **Branch voltage** to measure the voltage across the Surge Arrester block terminals.

Select **Branch current** to measure the current flowing through the Surge Arrester block.

Select **Branch voltage and current** to measure the surge arrester voltage and current.

Place a Multimeter block in your model to display the selected measurements during the simulation. In the **Available Measurements** list box of the Multimeter block, the measurement is identified by a label followed by the block name.

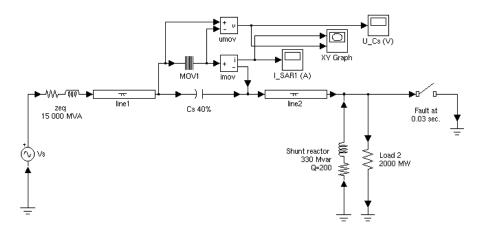
Measurement	Label
Branch voltage	Ub:
Branch current	Ib:

Limitations

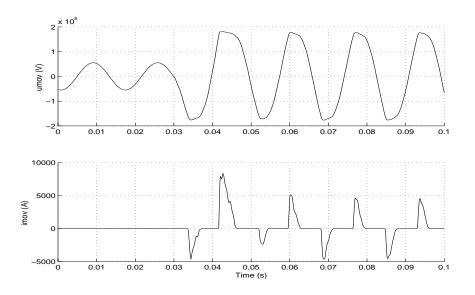
The Surge Arrester block is modeled as a current source driven by the voltage appearing across its terminals. Therefore, it cannot be connected in series with an inductor or another current source. As the Surge Arrester block is highly nonlinear, a stiff integrator algorithm must be used to simulate the circuit. ode15s or ode23tb with default parameters usually gives the best simulation speed. For continuous simulation, in order to avoid an algebraic loop, the voltage applied to the nonlinear resistance is filtered by a first-order filter with a time constant of 0.01 microseconds. This very fast time constant does not significantly affect the result accuracy. When the Surge Arrester block is used in a discrete system, a time delay of one simulation step is used. This delay can cause numerical oscillations if the sample time is too large.

Example

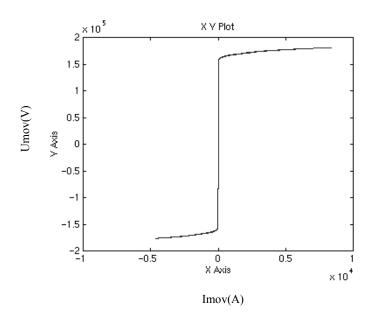
The psbarrester.mdl demo illustrates the use of metal-oxide varistors (MOV) on a 735 kV series-compensated network. Only one phase of the network is represented. The capacitor connected in series with the line is protected by a 30 column arrester. At t=0.03 seconds, a fault is applied at the load terminals. The current increases in the series capacitor and produces an overvoltage that is limited by the Surge Arrester block. Then the fault is cleared at t=0.3 seconds.



At fault application, the resulting overvoltage makes the MOV conduct. The waveforms displayed by Umov and Imov measurements as well as the V-I characteristic plotted by the X-Y scope are shown below:



Surge Arrester



Purpose

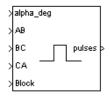
Implement a synchronized pulse generator to fire the thyristors of a six-pulse

converter

Library

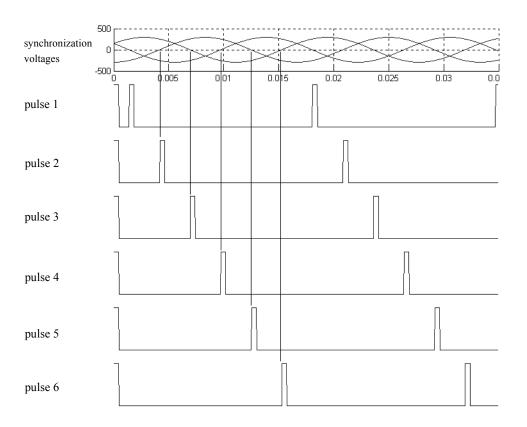
Extras/Control Blocks

Description



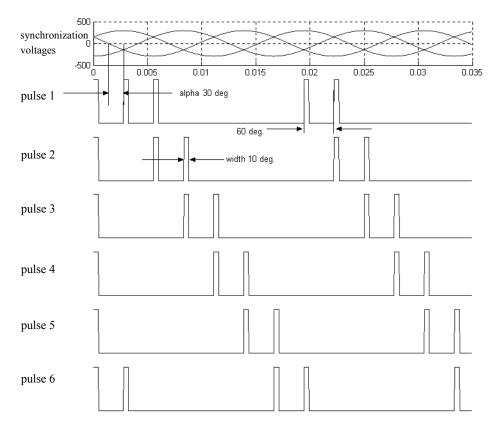
The Synchronized 6-Pulse Generator block can be used to fire the six thyristors of a six-pulse converter. The output of the block is a vector of six pulses individually synchronized on a three-phase commutation voltage. The pulses are generated alpha degrees after the increasing zero crossings of the commutation voltages.

The figures below display the synchronization of the six pulses for an alpha angle of 0 degrees. The pulses are generated exactly at the zero crossings of the synchronization voltages.

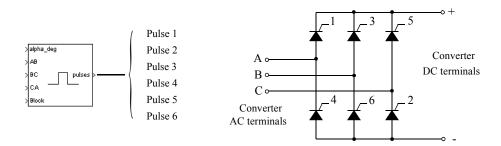


The Synchronized 6-Pulse Generator block can be configured to work in double-pulsing mode. In this mode two pulses are sent to each thyristor: a first pulse when the alpha angle is reached, then a second pulse 60 degrees later, when the next thyristor is fired.

The figures below display the synchronization of the six pulses for an alpha angle of 30 degrees and with double-pulsing mode. Notice that the pulses are generated 30 degrees after the zero crossings.

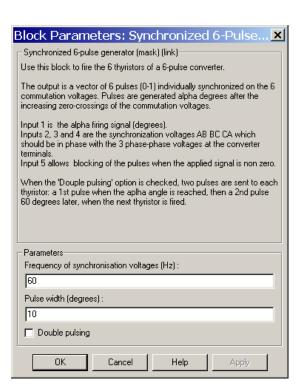


The pulse ordering at the output of the block corresponds to the natural order of commutation of a three-phase thyristor bridge. When you connect the Synchronized 6-Pulse Generator block to the pulses input of the Universal Bridge block (with the thyristors as the power electronic device), the pulses are sent to the thyristors in the following order:



When you build your own three-phase thyristor bridge with single thyristor blocks, you need to connect the pulse signals of the Synchronized 6-Pulse Generator block to the gate inputs of the corresponding thyristors.

Dialog Box and Parameters



Frequency of synchronization voltages (Hz)

The frequency, in hertz, of the synchronization voltages. It usually corresponds to the frequency of the network.

Pulse width (degrees)

The width of the pulses, in degrees.

Double pulsing

If selected, the generator sends to each thyristor a first pulse when the alpha angle is reached, and then a second pulse 60 degrees later when the next thyristor in the sequence is fired.

Inputs and Outputs

alpha_deg

Input 1 is the alpha firing signal, in degrees. This input can be connected to a Constant block, or it can be connected to a controller system to control the pulses of the generator.

AB, BC, CA

Inputs 2, 3, and 4 are the phase-to-phase synchronization voltages Vab, Vbc, and Vca. The synchronization voltages should be in phase with the three phase-phase voltages at the converter AC terminals.

block

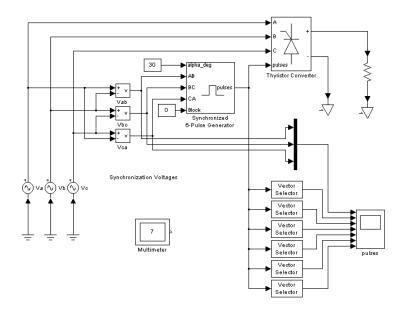
Input 5 allows you to block the operation of the generator. The pulses are disabled when the applied signal is greater than zero.

pulses

The output contains the six pulses signals.

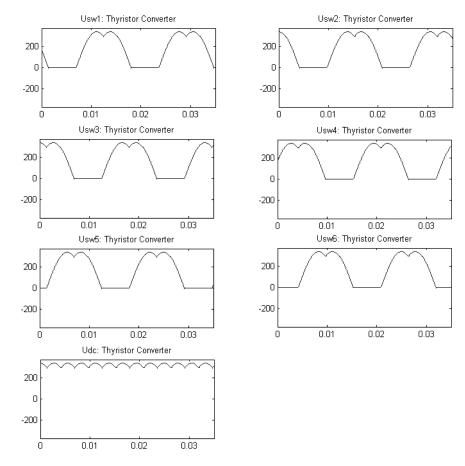
Example

The psbsixpulses.mdl demo uses a synchronized 6-Pulse Generator block to fire the thyristors of a six-pulse thyristor bridge. The bridge is fed by a three-phase voltage source and it is connected to a resistive load.



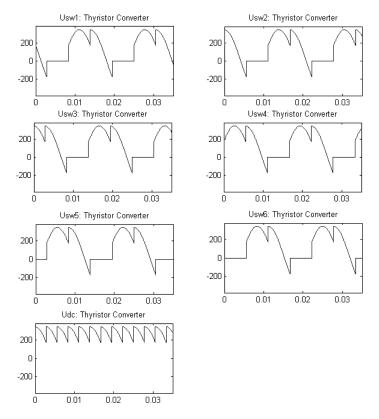
A first simulation is performed with an alpha angle of 0 degrees. Open the Constant block connected at input 1 of the Synchronized 6-Pulse Generator block and set its value to 0. Start the simulation. The voltages of the six thyristors are displayed in the next figure. The resulting DC voltage at the output of the rectifier is also displayed.

The thyristor voltages for alpha = 0 degrees are:



Now change the value of the alpha angle to 30 degrees and start the simulation. Notice that the waveforms of the thyristor voltages look different from the previous case. The thyristors begin to conduct with a lag of 30 degrees and the resulting DC voltage at the output of the rectifier has changed.

The thyristor voltages for alpha = 0 degrees are:



The figures show that the rms value of the DC voltage can be controlled by the alpha angle applied to the Synchronized 6-Pulse Generator block.

Purpose

Implement a synchronized pulse generator to fire the thyristors of a twelve-pulse converter

Library

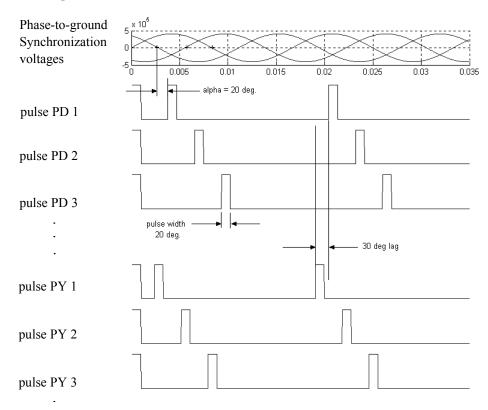
Extras / Control Blocks

Description



The Synchronized 12-Pulse Generator block generates two vectors of six pulses synchronized on a three-phase commutation voltage. The first set of pulses, denoted PY, is generated alpha degrees after the zero crossing of the phase-to-phase synchronization voltages. The second set of pulses, denoted PD, lags the PY pulses by 30 degrees.

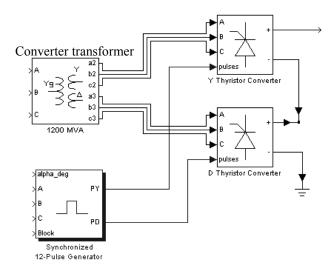
The figures below display the synchronization for the three first pulses of the two output vectors.



The phase-to-ground A,B, and C voltages are provided to the generator, and the phase-to-phase synchronization voltages are generated internally.

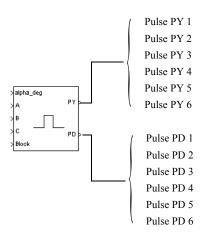
The Synchronized 12-Pulse Generator block can be used to control two six-pulse thyristor converters connected in series on the secondary terminals of a Y-Y-Delta transformer.

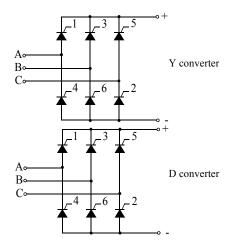
The PY pulses are used to fire the six thyristors of the converter connected to the Y secondary terminal and the PD pulses are used to fire the thyristors of the converter connected at the Delta terminal of the transformer.



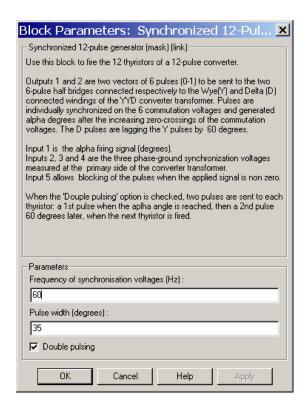
The ordering of the pulses in the two outputs of the block corresponds to the natural order of commutation of a three-phase thyristor bridge. When you connect the Synchronized 12-Pulse Generator block outputs to the pulse inputs

of the Universal Bridge blocks (with the thyristor device), the pulses are sent to the thyristors in the following way:





Dialog Box and Parameters



Frequency of synchronization voltages (Hz)

The frequency, in hertz, of the synchronization voltages. It usually corresponds to the frequency of the network.

Pulse width (degrees)

The width of the pulses, in degrees.

Double pulsing

If selected, the generator sends to each thyristor a first pulse when the alpha angle is reached, and then a second pulse 60 degrees later when the next thyristor in the sequence is fired. The double pulsing is applied separately on the two vectors of pulses.

Inputs and Outputs

alpha_deg

Input 1 is the alpha firing signal, in degrees. This input can be connected to a Constant block, or it can be connected to a controller system to control the pulses of the generator.

A, B, C

Inputs 2, 3, and 4 are the phase-to-ground synchronization voltages Va, Vb, and Vc. The synchronization voltages should be measured at the primary side of the converter transformer.

block

Input 5 allows you to block the operation of the generator. The pulses are disabled when the applied signal is greater than zero.

PΥ

Output 1 contains the six-pulse signals to be sent to the six-pulse thyristor converter connected to the Y secondary winding of the converter transformer.

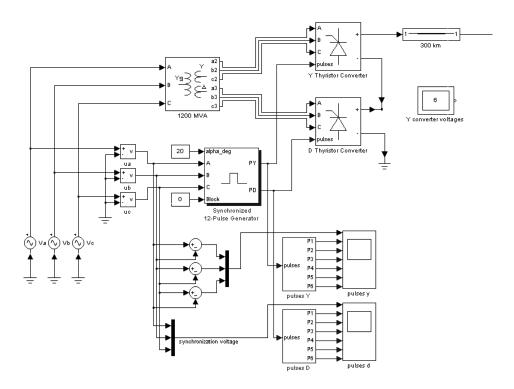
PD

Output 2 contains the six-pulse signals to be sent to the six-pulse thyristor converter connected to the Delta (D) secondary winding of the converter transformer.

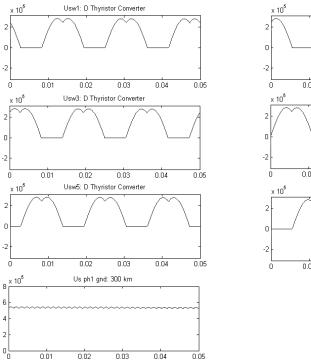
Example

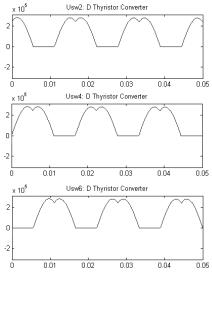
In the psbtwelvepulses.mdl demo a Synchronized 12-Pulse Generator block is used to fire the thyristors of a twelve-pulse thyristor bridge built with two six-pulse bridges. The bridge is fed by a three-winding three-phase transformer. The Y-connected secondary fed the first six-pulse bridge. The Delta secondary fed the second bridge.

The two bridge rectifiers are connected in series and a 300 Km line is connected to the DC side of the rectifiers.



A first simulation is performed with an alpha angle of 0 degrees. Open the Constant block connected at input 1 of the Synchronized 12-Pulse Generator block and set its value to 0. Start the simulation. The voltages of the thyristors of the D thyristor Converter block are displayed in the next figure. The resulting DC voltage at the input terminal of the transmission line is also displayed.





Compare the DC voltage generated by the Synchronized 12-Pulse Generator and its associated rectifier bridges to the results you obtained with the Synchronized 6-Pulse Generator. Notice that the ripple in the DC waveform is softer when 12 thyristors are used to rectify the three-phase AC voltage.

Purpose

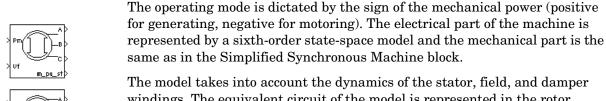
Model the dynamics of a three-phase round rotor or salient-pole synchronous machine

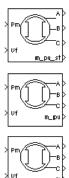
The Synchronous Machine block operates in generating or motoring modes.

Library

Machines

Description

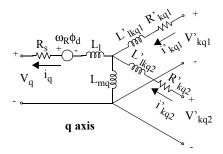


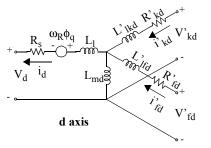


The model takes into account the dynamics of the stator, field, and damper windings. The equivalent circuit of the model is represented in the rotor reference frame (qd frame). All rotor parameters and electrical quantities are viewed from the stator. They are identified by primed variables. The subscripts used are defined as follows:

- d,q: d and q axis quantity
- *R*,*s*: Rotor and stator quantity
- *l,m*: Leakage and magnetizing inductance
- f,k: Field and damper winding quantity

The electrical model of the machine is





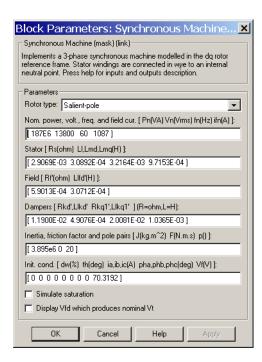
with the following equations.

$$\begin{split} V_{d} &= R_{s}i_{d} + \frac{d}{dt}\phi_{d} - \omega_{R}\phi_{q} \\ V_{q} &= R_{s}i_{q} + \frac{d}{dt}\phi_{q} + \omega_{R}\phi_{d} \\ V'_{fd} &= R'_{fd}i'_{fd} + \frac{d}{dt}\phi'_{fd} \\ V'_{kd} &= R'_{kd}i'_{kd} + \frac{d}{dt}\phi'_{kd} \\ V'_{kq1} &= R'_{kq1}i'_{kq1} + \frac{d}{dt}\phi'_{kq1} \\ \end{split} \qquad \begin{array}{l} \phi_{d} &= L_{d}i_{d} + L_{md}(i'_{fd} + i'_{kd}) \\ \phi_{q} &= L_{q}i_{q} + L_{mq}i'_{kq} \\ \phi'_{fd} &= L'_{fd}i'_{fd} + L_{md}(i_{d} + i'_{kd}) \\ \phi'_{kd} &= L'_{kd}i'_{kd} + L_{md}(i_{d} + i'_{fd}) \\ \phi'_{kq1} &= L'_{kq1}i'_{kq1} + L_{mq}i_{q} \\ \phi'_{kq2} &= L'_{kq2}i'_{kq2} + L_{mq}i_{q} \\ \end{split}$$

Dialog Box and Parameters

In the **powerlib** library you can choose among three Synchronous Machine blocks to specify the parameters of the model.

Fundamental Parameters in SI Units



Rotor type

Specifies rotor type: salient-pole or round (cylindrical). This choice affects the number of rotor circuits in the q-axis (damper windings).

Nominal

The total three-phase apparent power Pn (VA), rms line-to-line voltage Vn (V), frequency fn (Hz), and field current ifn (A).

The nominal field current is the current that produces nominal terminal voltage under no-load conditions. This model was developed with all quantities viewed from the stator. The nominal field current makes it possible to compute the transformation ratio of the machine, which allows you to apply the field voltage viewed from the rotor, as in real life. This also allows the field current, which is a variable in the output vector of the model, to be viewed from the rotor. If the value of the nominal field current is not known, you must enter 0. Since the transformation ratio cannot be

determined in this case, you have to apply the field voltage as viewed from the stator and the field current in the output vector is also viewed from the stator.

Stator

The resistance Rs (Ω) , leakage inductance Lls (H), and d-axis and q-axis magnetizing inductances Lmd (H) and Lmq (H).

Field

The field resistance Rf (Ω) and leakage inductance Llfd' (H), both referred to the stator.

Dampers

The d-axis resistance Rkd' (Ω) and leakage inductance Llkd' (H), the q-axis resistance Rkq1' (Ω) and leakage inductance Llkq1' (H), and (if round rotor only) the q-axis resistance Rkq2' (Ω) and leakage inductance Llkq2' (H). All these values are referred to the stator.

Mechanical

The inertia coefficient J (kg.m²), damping coefficient D (N.m.s./rad), and number of pole pairs p.

Initial conditions

The initial speed deviation $\Delta\omega$ (% of nominal speed), electrical angle of the rotor θe (deg), line current magnitudes ia, ib, ic (A) and phase angles pha, phb, phc (deg), and the initial field voltage Vf (V).

You can specify the initial field voltage in one of two ways. If you know the nominal field current (first line, last parameter) enter in the dialog box the initial field voltage in volts DC referred to the rotor. Otherwise, enter a 0 as nominal field current, as explained earlier, and specify the initial field voltage in volts DC referred to the stator. You can easily determine the nominal field voltage viewed from the stator by selecting the **Display Vfd which produces a nominal Vt** check box at the bottom of the dialog box.

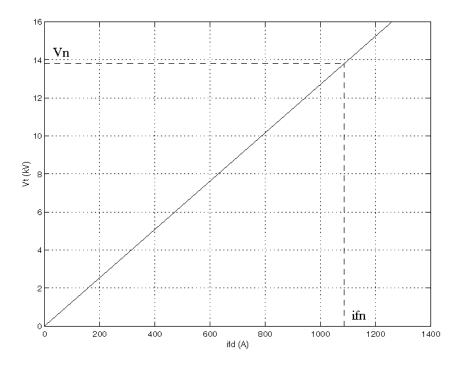
Simulate saturation

Specifies whether magnetic saturation of rotor and stator iron is to be simulated or not.

Saturation

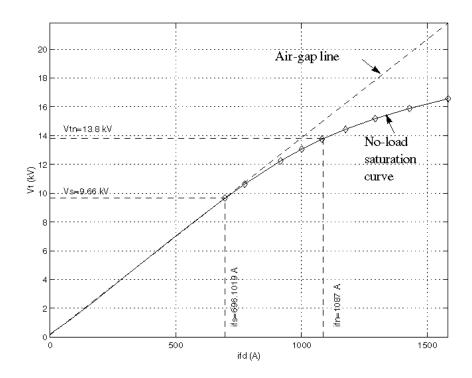
The no-load saturation curve parameters. Magnetic saturation of stator and rotor iron is modeled by a nonlinear function, in this case a polynomial, using points on the no-load saturation curve. You must enter a 2-by-n matrix, where n is the number of points taken from the saturation curve. The first row of this matrix contains the values of field currents, while the second row contains values of corresponding terminal voltages. The first point (first column of the matrix) must correspond to the point where the effect of saturation begins. You must select the **Simulate saturation** check box to simulate saturation. This check box allows you to enter the matrix of parameters for simulating the saturation. If you do not want to model saturation in your simulation, simply do not select the **Simulate saturation** check box. In this case the relationship between ifd and Vt obtained is linear (no saturation).

As an example, without saturation, a typical curve might be as shown below. Here ifn is 1087 A and Vn is 13800 V rms line-to-line, which is also 11268 V peak line-to-neutral.



When saturation is modeled, a polynomial is fitted to the curve corresponding to the matrix of points you enter. The more points you enter, the better the fit to the original curve.

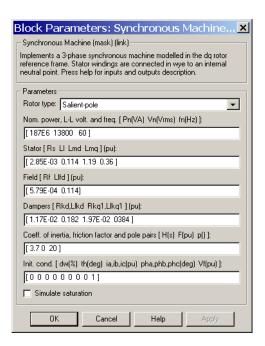
The next figure illustrates this graphically (the diamonds are the actual points entered in the dialog box).



In this particular case, the following values were used:

- ifn = 1087 A
- ifd = [695.64, 774.7, 917.5, 1001.6, 1082.2, 1175.9, 1293.6, 1430.2, 1583.7] A
- Vt = [9660, 10623, 12243, 13063, 13757, 14437, 15180, 15890, 16567] V

Fundamental Parameters in p.u.



Rotor type

Specifies rotor type: salient-pole or round (cylindrical).

Nominal

Total three-phase apparent power (VA), rms line-to-line voltage (V), frequency (Hz), and field current (A).

This line is identical to the first line of the fundamental parameters in SI dialog box, except that you do not specify a nominal field current. This value is not required here because we do not need the transformation ratio. Since rotor quantities are viewed from the stator, they are converted to p.u. using the stator base quantities derived from the preceding three nominal parameters.

Stator, field and dampers

Contain exactly the same parameters as in the previous dialog box, but they are expressed here in p.u. instead of SI units.

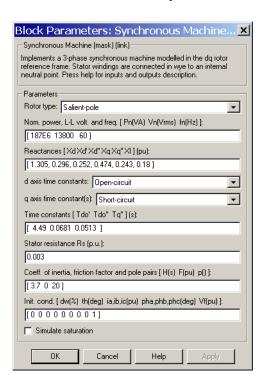
Mechanical

The inertia constant H(s), where H is the ratio of energy stored in the rotor at nominal speed over the nominal power of the machine, the damping coefficient D (p.u. torque/p.u. speed deviation), and the number of pole pairs p.

Initial conditions, Simulate saturation, Saturation

The same initial conditions and saturation parameters as in the S.I. units dialog box, but all values are expressed in p.u. instead of SI units. For saturation, the nominal field current multiplied by the d-axis magnetizing inductance and nominal rms line-to-line voltage are the base values for the field current and terminal voltage, respectively.

Standard Parameters in p.u.



Rotor type, Nominal

The same parameters as the fundamental p.u. dialog box.

Reactances

The d-axis synchronous reactance Xd, transient reactance Xd', and subtransient reactance Xd", the q-axis synchronous reactance Xq, transient reactance Xq' (only if round rotor), and subtransient reactance Xq", and finally the leakage reactance Xl (all in p.u.).

d-axis time constants, q-axis time constant(s)

Specify which time constants you supply for each axis: either open-circuit or short-circuit.

Time constants

The d-axis and q-axis time constants (all in s). These values must be consistent with choices made on the two previous lines: d-axis transient open-circuit (Tdo') or short-circuit (Td') time constant, d-axis subtransient open-circuit (Tdo") or short-circuit (Td") time constant, q-axis transient open-circuit (Tqo') or short-circuit (Tq') time constant (only if round rotor), q-axis subtransient open-circuit (Tqo") or short-circuit (Tq") time constant.

Stator resistance

The stator resistance Rs (p.u.).

Mechanical, Initial conditions, Simulate saturation, Saturation

The same parameters as the fundamental parameters in p.u. dialog box.

Note These three blocks simulate exactly the same Synchronous machine model; the only difference is the way of entering the parameter units.

Inputs and Outputs

The units of inputs and outputs vary according to which dialog box was used to enter the block parameters. For the nonelectrical connections, there are two possibilities. If the first dialog box (fundamental parameters in SI units) is used, the inputs and outputs are in SI units (except for dw in the vector of internal variables, which is always in p.u., and angle θ , which is always in rad). If the second or third dialog boxes is used, the inputs and outputs are in p.u.

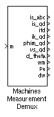
The first input is the mechanical power at the machine's shaft. In generating mode, this input can be a positive constant or function or the output of a prime mover block (see the Hydraulic Turbine and Governor or Steam Turbine and Governor blocks). In motoring mode, this input is usually a negative constant or function.

The second input of the block is the field voltage and can be supplied by a voltage regulator (see the Excitation System block) in generating mode and is usually a constant in motoring mode.

The first three outputs are the electrical terminals of the stator. The last output of the block is a vector containing 16 variables. They are, in order:

- 1-3: Stator currents (flowing out of machine) i_{sa} , i_{sb} , and i_{sc}
- 4-5: q- and d-axis stator currents (flowing out of machine) iq, id
- 6-8: Field and damper winding currents (flowing into machine)
 ifd, ikq, and ikd
- 9-10: q- and d-axis magnetizing fluxes ϕ_{mq} , ϕ_{md}
- 11-12: q- and d-axis stator voltages v_q,v_d
- 13: Rotor angle deviation $\Delta\theta$, also known as power angle δ
- 14: Rotor speed ω_r
- 15: Electrical power Pe
- 16: Rotor speed deviation dw

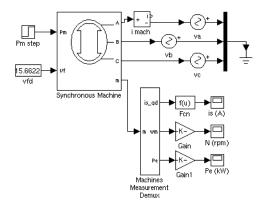
You can demultiplex these variables by using the special Machines Measurement Demux block provided in the Machines library.



Example

The psbsyncmachine.mdl demoillustrates the use of the Synchronous Machine block in motoring mode. The simulated system consists of an industrial grade synchronous motor (150 HP, 440V) connected to an infinite bus. After the

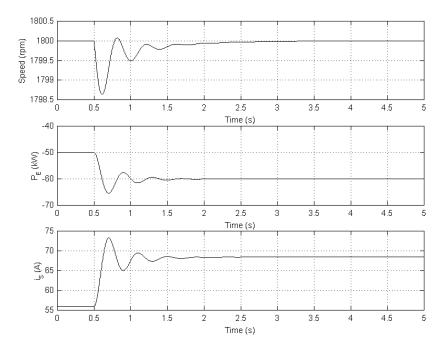
machine reaches a stable speed, the load (mechanical power) is changed from $50\,kW$ to $60\,kW$. The initial conditions are set in such a way that the simulation starts in steady state.



Set the simulation parameters as follows:

- Integrator type: Stiff, ode15s
- Stop time: 5 s
- \bullet Integration options: Use default settings, except for Max. step size, which you must set to 0.005 s

Run the simulation and observe the speed, power, and current of the motor.



Since this is a four pole machine, the nominal speed is 1800 rpm. The initial speed is 1800 rpm as prescribed (top graph). The load passes from 50 kW to 60 kW at t=0.5 s. The machine then oscillates before stabilizing to 1800 rpm.

Now, look at the electrical power (middle graph). Since we are in motoring mode, the machine absorbs power and Pe is negative. As expected, the power starts at -50 kW until the load is changed at t=0.5 seconds, at which point the power oscillates before settling at -60 kW.

Finally, look at the stator current i_s . As expected, the current starts with the value corresponding to a three-phase power of 50 kW (56 A), before oscillating and settling to the value corresponding to a 60 kW load (68.5 A).

References

[1] Krause P.C., *Analysis of Electric Machinery*, McGraw-Hill, 1986, Section 12.5.

[2] Kamwa I. et al., "Experience with Computer-Aided Graphical Analysis of Sudden-Short-Circuit Oscillograms of Large Synchronous Machines," *IEEE Transactions on Energy Conversion*, Vol.10, No.3, September 1995.

See Also

Excitation System, Hydraulic Turbine and Governor, Powergui, Simplified Synchronous Machine, Steam Turbine and Governor

Purpose

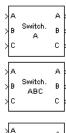
Implement a three-phase circuit breaker opening at the current zero crossing

Library

Elements

Description

The 3-Phase Breaker block implements a three-phase circuit breaker where the opening and closing times can be controlled either from an external Simulink signal (external control mode), or from an internal control timer (internal control mode).



The 3-Phase Breaker block uses three Breaker blocks connected between the inputs and the outputs of the block. You can use this block in series with the three-phase element you want to switch. See the Breaker block reference pages for details on the modeling of the single-phase breakers.

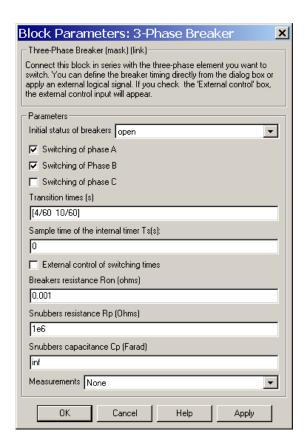


If the 3-Phase Breaker block is set in external control mode, a control input appears in the block icon. The control signal connected to this input must be either 0 or 1, 0 to open the breakers, 1 to close them. If the 3-Phase Breaker block is set in internal control mode, the switching times are specified in the dialog box of the block. The three individual breakers are controlled with the same signal.

Series Rs-Cs snubber circuit are included in the model. They can be optionally connected to the phase breakers. If the 3-Phase Breaker block happens to be in series with an inductive circuit, an open circuit or a current source, you must use the snubbers.

3-Phase Breaker

Dialog Box and Parameters



Initial status of breakers

The initial status of the breakers. The initial status is the same for the three breakers.

Switching of phase A

If selected, the switching of phase A is activated. If not selected, the breaker of phase A stays in its initial status specified in the **Initial status** of breakers parameter.

Switching of Phase B

If selected, the switching of phase B is activated. If not selected, the breaker of phase B stays in its initial status specified in the **Initial status** of breakers parameter.

Switching of phase C

If selected, the switching of phase C is activated. If not selected, the breaker of phase C stays in its initial status specified in the **Initial status** of breakers parameter.

Transition times (s)

Specify the vector of switching times when using the 3-Phase Breaker block in internal control mode. At each transition time the selected breakers opens or closes depending to their initial state. The **Transition times** (s) parameter is not visible in the dialog box if the **External control of switching times** parameter is selected.

Sample time of the internal timer Ts (s):

The sample time of the internal breakers. The default is 0, corresponding to continuous breaker models.

External control of switching times

If selected, adds a fourth input port to the 3-Phase Breaker block for an external control of the switching times of the breakers. The switching times are defined by a Simulink signal (0-1 sequence).

Breakers resistance Ron (ohms)

The internal breaker resistances, in ohms (Ω) . The **Breaker resistance Ron** parameter cannot be set to 0.

Snubbers resistance Rp (ohms)

The snubber resistances, in ohms (Ω) . Set the **Snubber resistance Rp** parameter to inf to eliminate the snubbers from the model.

Snubbers capacitance Cp (Farad)

The snubber capacitances, in farads (F). Set the **Snubber capacitance Cp** parameter to 0 to eliminate the snubbers, or to inf to get resistive snubbers.

3-Phase Breaker

Measurements

Select **Breaker voltages** to measure the voltage across the three internal breaker terminals.

Select **Breaker currents** to measure the current flowing through the three internal breakers. If the snubber devices are connected, the measured currents are the ones flowing through the breakers contacts only.

Select **Breaker voltages and currents** to measure the breaker voltages and the breaker currents.

Place a Multimeter block in your model to display the selected measurements during the simulation. In the **Available Measurements** list box of the Multimeter block, the measurements is identified by a label followed by the block name and the phase:

Measurement	Label
Breaker voltages	Ub <block name=""> /Breaker A: Ub <block name=""> /Breaker B:</block></block>
	Ub <block name=""> /Breaker C.</block>
Breaker currents	<pre>Ib <block name=""> /Breaker A: Ib <block name=""> /Breaker B:</block></block></pre>
	Ib <block name=""> Breaker C.</block>

Inputs and Outputs

The inputs 1,2 and 3 and the three outputs are the breaker terminals. The breaker A is connected between the input and output 1, the breaker B is connected between input and output 2, and the breaker C is connected between input and output 3. If the 3-Phase Breaker block is set in external control mode, the input 4 appears and it is used to control the opening and closing of the three internal breakers.

Example

See the psb3phlinereclose.mdl and psb3phseriescomp.mdl circuits for demos using the 3-Phase Breaker block.

See also

Breaker, 3-Phase Fault

Purpose

Implements a three-phase dynamic load with programmable active power and reactive power

Library

Elements

Description



The 3-Phase Dynamic Load block implements a three-phase, three-wire dynamic load whose active power P and reactive power Q vary as function of positive-sequence voltage. Negative- and zero-sequence currents are not simulated. The three load currents are therefore balanced, even under unbalanced load voltage conditions.

The load impedance is kept constant if the terminal voltage V of the load is lower than a specified value Vmin. When the terminal voltage is greater than the Vmin value, the active power P and reactive power Q of the load vary as follows:

$$P(s) = P_o \left(\frac{V}{V_o}\right)^{n_p} \frac{(1 + T_{p1}s)}{(1 + T_{p2}s)}$$

$$Q(s) = Q_o \left(\frac{V}{V_o}\right)^{n_q} \frac{(1 + T_{q1}s)}{(1 + T_{q2}s)}$$

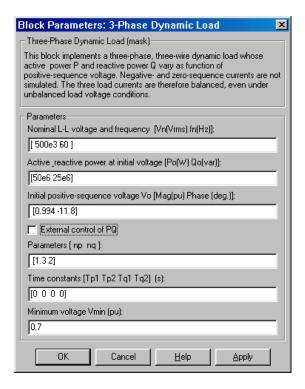
where

- V₀ is the initial positive sequence voltage.
- \bullet P_o and Q_o are the initial active and reactive powers at the initial voltage $V_o.$
- \bullet V is the positive-sequence voltage.
- \bullet n_p and n_q are exponents (usually between 1 and 3) controlling the nature of the load.
- T_{p1} and T_{p2} are time constants controlling the dynamics of the active power P.
- \bullet T_{q1} and T_{q2} are time constants controlling the dynamics of the reactive power Q.

For a constant current load, for example, you set n_p to 1 and n_q to 1, and for constant impedance load you set n_p to 2 and n_q to 2.

3-Phase Dynamic Load

Dialog Box



Nominal L-L voltage and frequency

Specifies the nominal phase to phase voltage and nominal frequency of the load.

Active and reactive power at initial voltage

Specifies the initial active power and initial reactive power at the initial voltage.

Initial positive-sequence voltage Vo

Specifies the magnitude and phase of the initial positive-sequence voltage of the load.

External control of PQ

If, selected, the active power and reactive power of the load are defined by an external simulink signal.

Parameters [np nq]

Specifies the np and nq parameters that define the nature of the load.

Time constants [Tp1 Tp2 Tq1 Tq2]

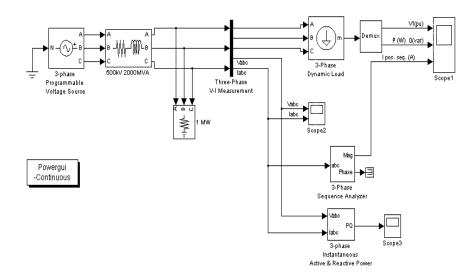
Specifies the time constants controlling the dynamics of the active power and the reactive power.

minimum voltage Vmin

Specifies the minimum voltage where the load dynamics enter in action. The load impedance is constant below this value.

Example

The psbdynamicload.mdl model uses a 3-Phase Dynamic Load block connected on a 500 kV, 60 Hz power network. The network is simulated by its Thevenin equivalent (voltage source behind a R-L impedance corresponding to a three-phase short-circuit level of 2000 MVA). The source internal voltage is modulated in order to simulate voltage variation during a power swing. As the dynamic load is a nonlinear model simulated by current sources, it cannot be connected to an inductive network (R-L in series). Therefore, a small resistive load (1 MW) has been added in parallel with the dynamic load.



3-Phase Fault

Purpose

Implement a programmable phase-to-phase and phase-to-ground fault breaker system

Library

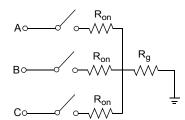
Extras/Three-Phase Library

Description

The 3-Phase Fault block implements a three-phase circuit breaker where the opening and closing times can be controlled either from an external Simulink signal (external control mode), or from an internal control timer (internal control mode).

The 3-Phase Fault block uses three Breaker blocks that can be individually switched on and off to program phase-to-phase faults, phase-to-ground faults, or a combination of phase-to-phase and ground faults.





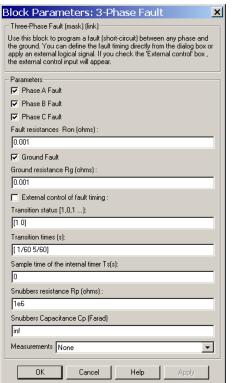
A B Fault C ABC

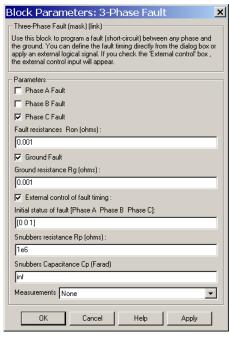
The ground resistance Rg is automatically set to 1e6 ohms when the ground fault option is not programmed. For example, to program a fault between the phases A and B you need to check the **Phase A Fault** and **Phase B Fault** block parameters only. To program a fault between the phase A and the ground, you need to check the **Phase A Fault** and **Ground Fault** parameters and specify a small value for the ground resistance.

If the 3-Phase Fault block is set in external control mode, a control input appears in the block icon. The control signal connected to the fourth input must be either 0 or 1, 0 to open the breakers, 1 to close them. If the 3-Phase Fault block is set in internal control mode, the switching times and status are specified in the dialog box of the block.

Series Rs-Cs snubber circuits are included in the model. They can be optionally connected to the fault breakers. If the 3-Phase Fault block happen to be in series with an inductive circuit, an open circuit or a current source, you must use the snubbers.

Dialog Box and Parameters





Phase A Fault

If selected, the fault switching of phase A is activated. If not selected, the breaker of phase A stays in its initial status. The initial status of the phase A fault breaker is specified by the **Transition status** parameter when the 3-Phase Fault block is used in internal control mode. It is specified by the **Initial status of fault** parameter when the block is used in external control mode.

Phase B Fault

If selected, the fault switching of phase B is activated. If not selected, the breaker of phase B stays in its initial status. The initial status of the phase B fault breaker is specified by the **Transition status** parameter when the 3-Phase Fault block is used in internal control mode. It is specified by the

Initial status of fault parameter when the block is used in external control mode.

Phase C Fault

If selected, the fault switching of phase C is activated. If not selected, the breaker of phase C stays in its initial status. The initial status of the phase C fault breaker is specified by the **Transition status** parameter when the 3-Phase Fault block is used in internal control mode. It is specified by the **Initial status of fault** parameter when the block is used in external control mode.

Fault resistances Ron (ohms):

The internal resistance, in ohms (Ω) , of the phase fault breakers. The **Fault resistance Ron** parameter cannot be set to 0.

Ground Fault

If selected, the fault switching to the ground is activated. A fault to the ground can be programed for the activated phases. For example if the **Phase C Fault** and **Ground Fault** parameters are selected, a fault to the ground is applied to the phase C. The ground resistance is set internally to 1e6 ohms when the **Ground Fault** parameter is not selected.

Ground resistance Rg (ohms):

The ground resistance, in ohms (Ω) . The **Ground resistance Rg (ohms)** parameter cannot be set to 0. The **Ground resistance Rg (ohms)** parameter is not visible if the **Ground Fault** parameter is not selected.

External control of fault timing:

If selected, adds a fourth input port to the 3-Phase Fault block for an external control of the switching times of the fault breakers. The switching times are defined by a Simulink signal (0 or 1) connected to the fourth input port of the block.

Transition status:

Specify the vector of switching status when using the 3-Phase Breaker block in internal control mode. The selected fault breakers opens (0) or closes (1) at each transition time according to the **Transition status** parameter values.

If the first value specified in the **Transition times** parameter is 0, the initial status of the fault breakers corresponds to the first value specified

in the vector of switching status (0 for open, 1 for closed). Otherwise the initial status of the breakers corresponds to the complement of the first value specified in the vector of switching status.

Transition times (s)

Specify the vector of switching times when using the 3-Phase Breaker block in internal control mode. At each transition time the selected fault breakers opens or closes depending to the initial state. The **Transition times** (s) parameter is not visible in the dialog box if the **External control of switching times** parameter is selected.

Sample time of the internal timer Ts (s)

The sample time of the internal breakers. The default is 0, corresponding to continuous breaker models.

Initial status of fault [Phase A Phase B Phase C]

The initial status of the fault breakers when the 3-Phase Fault block is used in external control mode.

Snubbers resistance Rp (ohms)

The snubber resistances, in ohms (Ω) . Set the **Snubber resistance Rp** parameter to inf to eliminate the snubbers from the model.

Snubbers Capacitance Cp (Farad)

The snubber capacitances, in farads (F). Set the **Snubber capacitance Cp** parameter to 0 to eliminate the snubbers, or to inf to get resistive snubbers.

Measurements

Select **Fault voltages** to measure the voltage across the three internal fault breaker terminals.

Select **Fault currents** to measure the current flowing through the three internal breakers. If the snubber devices are connected, the measured currents are the ones flowing through the breakers contacts only.

Select **Fault voltages and currents** to measure the breaker voltages and the breaker currents.

Place a Multimeter block in your model to display the selected measurements during the simulation. In the **Available Measurements** list

3-Phase Fault

box of the Multimeter block, the measurements is identified by a label followed by the block name and the phase:

Measurement	Label
Fault voltages	Ub <block name=""> /Fault A: Ub <block name=""> /Fault B: Ub <block name=""> /Fault C.</block></block></block>
Fault currents	<pre>Ib <block name=""> /Fault A: Ib <block name=""> /Fault B: Ib <block name=""> /Fault C.</block></block></block></pre>

Inputs and Outputs

The inputs 1, 2, and 3 are the breaker terminals. The breakers are connected between inputs 1, 2, and 3 and the internal ground resistor. If the 3-Phase Fault block is set in external control mode, input 4 appears and is used to control the opening and closing of the three internal breakers.

Example

See the psb3phseriescomp.mdl circuit for a demo using the 3-Phase Fault block.

See also

Breaker, 3-Phase Breaker

Purpose

Implement a three-phase source signal with programmable time variation of amplitude, phase, frequency, and harmonics

Library

Electrical Sources

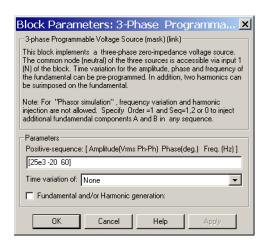
Description



Use this block to generate a three-phase sinusoidal signal with time-varying parameters. You can program the time variation for the amplitude, phase, or frequency of the fundamental component of the source. In addition, two harmonics can be programmed and superimposed on the fundamental signal.

The 3-Phase Programmable Source block can be used to control the voltage of three Controlled Voltage Source blocks or the current of three Controlled Current Source blocks.

Dialog Box and Parameters



Positive-sequence: [Amplitude Phase (degrees) Freq. (Hz)]

The amplitude in volts or amperes, the phase in degrees, and the frequency in hertz of the positive-sequence component of the source.

Time variation of

Specify the parameter for which you want to program the time variation. Select **None** if you do not want to program the time variation of the source parameters. Select **Amplitude** if you want to program the time variation of the amplitude. Select **Phase** if you want to program the time variation of

the phase. Select **Frequency** if you want to program the time variation of the frequency.

Note that the time variation applies on the three phases of the source.

Type of variation

Specify the type of variation that is applied on the parameter specified by the **Time variation of** parameter. Select **Step** to program a step variation. Select **Ramp** to program a ramp variation. Select **Modulation** to program a modulated variation.

Step magnitude

Specify the amplitude of the step change. This parameter is only visible if the **Type of Variation** parameter is set to **Step**.

Rate of change (value/s)

Specify the rate of change, in volt/seconds or ampere/second. This parameter is only visible if the **Type of Variation** parameter is set to **Ramp**.

Amplitude of the modulation

Specify the amplitude of the modulation for the source parameter specified in the **Time variation of** parameter. This parameter is only visible if the **Type of variation** parameter is set to **Modulation**.

Frequency of the modulation (Hz)

Specify the frequency of the modulation for the source parameter specified in the **Time variation of** parameter. This parameter is only visible if the **Type of variation** parameter is set to **Modulation**.

Variation timing (s): [Start End]

Specify the time, in seconds, when the programmed time variation takes effect and the time when it stops.

Harmonic generation

If selected, two harmonics can be programmed to be superimposed on the fundamental signal of the source.

A: [Order(n) Amplitude Phase(degrees) Seq(0, 1 or 2)]

Specify the order, amplitude, phase, and the type of sequence of the first harmonic to be superimposed on the fundamental signal. This parameter is only visible if the **Harmonic generation** parameter is selected.

B: [Order(n) Amplitude Phase(degrees) Seq(0, 1 or 2)]

Specify the order, amplitude, phase, and the type of sequence of the second harmonic to be superimposed on the fundamental signal. This parameter is only visible if the **Harmonic generation** parameter is selected.

Harmonic timing (s): [Start End]

Specify the time, in seconds, when the harmonic generation is superimposed on the fundamental signal and the time when it stops. This parameter is only visible if the **Harmonic generation** parameter is selected.

Inputs and Outputs

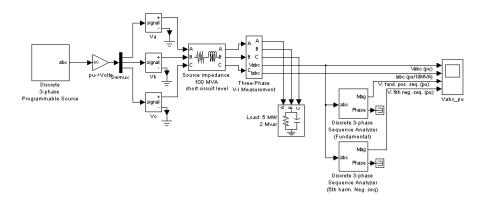
The output of the block is a vectorized signal containing a three-phase signal [a b c] of the programmed source. Use a Demux block and three Controlled Voltage Source blocks to generate a three-phase voltage source or use three Controlled Current Source blocks to generate a three-phase current source.

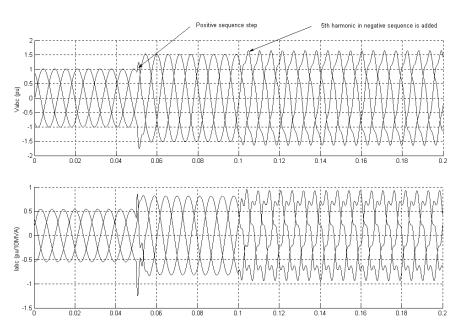
Example

The psb3phsignalseq.mdl circuit illustrates the use of the discrete version of the 3-Phase Programmable Source block to create a programmable voltage source.

A positive-sequence of 1.0 p.u., 0 degrees is specified for the fundamental signal. At $t=0.05\,\mathrm{s}$ a step of 0.5 p.u. is applied on the positive-sequence voltage magnitude, then at $t=0.1\,\mathrm{s}$, 0.1 p.u. of fifth harmonic in negative sequence is added to the 1.5 p.u. voltage.

In order to start simulation in steady state, the three Controlled Voltage Source blocks are initialized with a positive-sequence voltage of $25~\rm kV$, $0~\rm degree$, $60~\rm Hz$. The three-phase voltage and current are measured at the output of the source impedance.





Purpose

Measure the positive, negative, and zero sequence components of a three-phase signal $\,$

Library

Extras/Measurements

Description



The 3-Phase Sequence Analyzer block outputs the magnitude and phase of the positive (denoted by the index 1), negative (index 2), and zero sequence (index 0) components of a set of three balanced or unbalanced signals. The signals can contain harmonics or not. The three sequence components of a three-phase signal (voltages V1 V2 V0 or currents I1 I2 I0) are computed as follows:

$$\begin{split} V_1 &= \frac{1}{3}(V_a + a \cdot V_b + a^2 \cdot V_c) \\ V_2 &= \frac{1}{3}(V_a + a^2 \cdot V_b + a \cdot V_c) \\ V_0 &= \frac{1}{3}(V_a + V_b + V_c) \end{split}$$

where

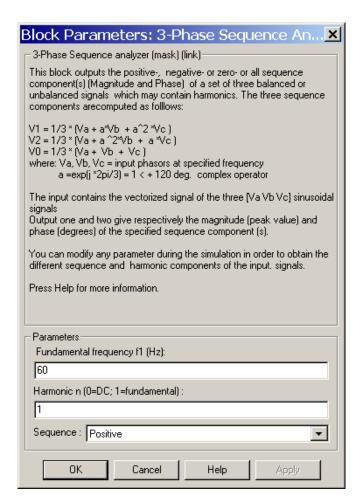
$$V_a, V_b, V_c$$
 = three-phase voltage at specified frequency $a=e^{j2\pi/3}=1\angle 120\deg$ complex operator

A Fourier analysis over a sliding window of one cycle of the specified frequency is first applied to the three input signals. It evaluate the phasor values Va, Vb, and Vc at the specified fundamental or harmonic frequency. Then the transformation is applied to obtain the positive sequence, negative sequence, and zero sequence.

The 3-Phase Sequence Analyzer block is not sensitive to harmonics or unbalances. However, as with any filtered system, it introduces some delay. For example, its response to a step change of V1 is a one-cycle ramp.

You can modify any parameter during the simulation in order to obtain the different sequence and harmonic components of the input signals.

Dialog Box and Parameters



Fundamental frequency f1 (Hz)

The fundamental frequency, in hertz, of the three-phase input signal.

Harmonic n (0=DC; 1=fundamental)

Specify the harmonic component from which you want to evaluate the sequences.

Sequence

Specify which sequence component the block outputs. Select **Positive** to calculate the Positive sequence, select **Negative** to calculate the negative sequence, or select **0** to compute the zero sequence of the fundamental or specified harmonic of the three-phase input signal

Inputs and Outputs

abc

Connect to the input the vectorized signal of the three [Va Vb Vc] sinusoidal signals.

Mag

The first output give the magnitude (peak value) of the specified sequence component.

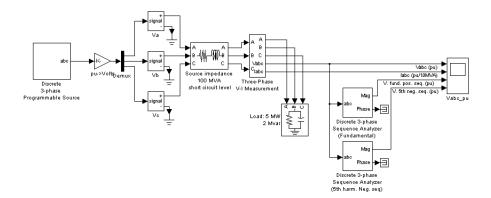
Phase

The second output gives the phase in degrees of the specified component.

Example

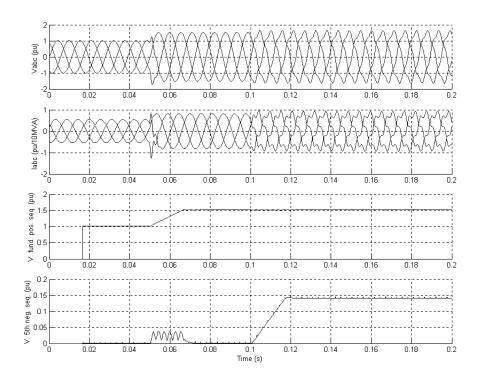
The psb3phsignalseq.mdl demo illustrates the use of the Sequence Analyzer blocks to observe and calculate the fundamental and harmonic components of a measured signal.

The internal voltage of the voltage source is controlled through the Discrete 3-Phase Programmable Source block. The three voltage outputs are first converted from p.u. to volts and they are connected to three Controlled Voltage Source blocks. Two 3-Phase Sequence Analyzer blocks are used to monitor the positive sequence of the three fundamental voltages Va,Vb, Vc and the negative-sequence component of the fifth harmonics.



Start the simulation and observe the positive sequence of the fundamental frequency and the negative sequence of the fifth harmonic component. The fifth harmonic internal voltage is amplified from 0.08 p.u. to 0.14 p.u. at the load terminals.

As the 3-Phase Sequence Analyzer blocks use Fourier analysis, their response time is delayed by one cycle of the fundamental frequency.



Purpose

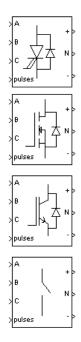
Implement a three-level neutral point clamped (NPC) power converter with selectable topologies and power switching devices

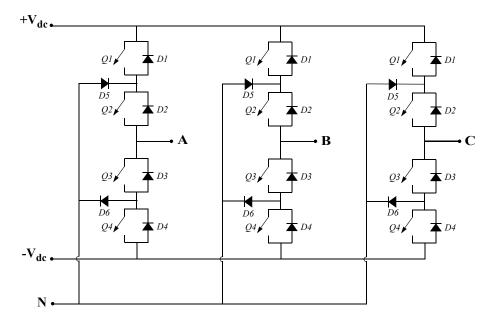
Library

Power Electronics

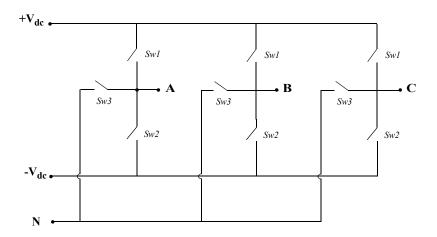
Description

The Three Level Bridge block implements a three-level power converter that consists of one, two, or three arms of power switching devices. Each arm consists of four switching devices (Q1 to Q4) along with their antiparallel diodes (D1 to D4) and two neutral clamping diodes (D5 and D6) as shown.

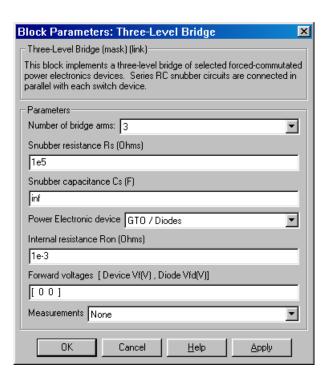




The type of power switching devices (IGBT, GTO, MOSFET, or ideal switch) and the number of arms (one, two, or three) are selectable from the dialog box. When the ideal switch is used as the switching device, the Three-Level Bridge block implements an ideal switch bridge having a three-level topology as shown.



Dialog Box and Parameters



Number of bridge arms

Determine the bridge topology: One, two, or three arms.

Snubber resistance Rs

The snubber resistance, in ohms (Ω) . Set the **Snubber resistance Rs** parameter to inf to eliminate the snubbers from the model.

Snubber capacitance Cs

The snubber capacitance, in farads (F). Set the **Snubber capacitance Cs** parameter to 0 to eliminate the snubbers, or to inf to get a resistive snubber.

For forced-commutated devices (GTO, IGBT, or MOSFET) the Three-Level Bridge block operates satisfactorily with resistive snubbers as long as the firing pulses are sent to the switching devices.

If the firing pulses to forced-commutated devices are blocked, the bridge operates as a diode rectifier. In this condition, you must use appropriate values of Rs and Cs. If the model is discretized, you can use the following formulas to compute approximate values of Rs and Cs:

$$Rs > 2\frac{Ts}{Cs}$$

$$Cs < \frac{1000Pn}{2\pi f Vn^2}$$

The Pn is the nominal power of the single-phase or three-phase converter (VA), Vn is the nominal line-to-line AC voltage (V rms), f is the fundamental frequency (Hz), and Ts is the Sample time (s).

These Rs and Cs values are derived from these two criteria:

- The snubber leakage current at fundamental frequency is less than 0.1% of nominal current when power electronic devices are not conducting
- The RC time constant of snubbers is higher than two times the sample time Ts.

Note that the Rs and Cs values that guarantee numerical stability of the discretized bridge can be different from actual values used in the physical circuit.

Power electronic device

Select the type of power electronic device to use in the bridge.

Internal resistance Ron (ohms)

Internal resistance of the selected devices and diodes, in ohms (Ω) .

Forward voltages [Device Vf (V), Diode Vfd (V)]

The forward voltage of the selected devices (for GTO or IGBT only) and of the antiparallel and clamping diodes, in volts.

Measurements

Select **Device currents** to measure the current flowing through all the components (Q1 to Q4 and D1 to D6). If the snubber devices are defined, the measured currents are those flowing through the power electronic devices only.

Select **UAN UBN UCN UDCP(DC+) UDCM(DC-) voltages** to measure the terminal voltages (AC and DC) of the Three Level Bridge block.

Select **All voltages and currents** to measure all voltages and currents defined for the Three Level Bridge block.

Place a Multimeter block in your model to display the selected measurements during the simulation. In the **Available Measurement** list box of the Multimeter block, the measurement is identified by a label followed by the block name.

Measurement	Label
device currents for MOSFET, IGBT and GTO	<pre>I_Q1_X:, I_Q2_X:, I_Q3_X:, I_Q4_X:, I_D1_X:, I_D2_X:, I_D3_X:, I_D4_X:, I_D5_X:, I_D6_X:</pre>
device currents for Ideal Switch, where X=A, B, or C	or I_Sw1_X:, I_Sw2_X:, I_Sw3_X:
Terminal voltages	Uan:, Ubn:, Ucn:, Udep:, Udem:

Inputs and Outputs

The bridge configuration is selectable, and the inputs and outputs depend on the configuration chosen:

- When A, B, C are selected as inputs, the DC terminals are the outputs.
- When A, B, C are selected as outputs, the DC terminals are the inputs.

The Pulses input accepts a Simulink-compatible vectorized gating signal containing four pulses (Q1 to Q4) for each leg of the converter. For instance, if a three-leg topology is selected, the input vector must contain twelve pulses and the ordering must be as follows: Q1 of leg A, Q2 of leg A, ..., Q4 of leg C.

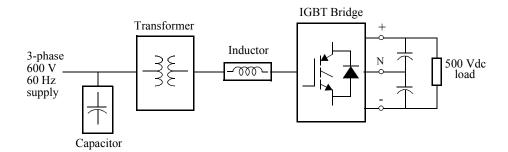
Note In the case of the ideal switch converter, Q1 pulse is sent to Sw1, Q4 pulse to Sw2, and a logical AND operation is performed on Q2 and Q3 pulses and the result sent to Sw3.

Assumptions and Limitations

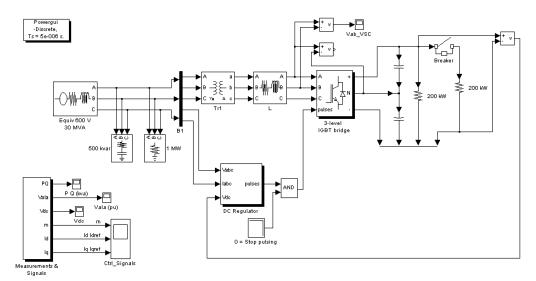
Turn-on and turnoff times (Fall time, Tail time) of power switching devices are not modeled in the Three-Level Bridge block.

Example

The psb3levelVSC.mdl demo illustrates the use of the Three Level Bridge block in an AC-DC converter consisting of a three-phase IGBT-based voltage sourced converter (VSC). The converter is pulse-width modulated (PWM) to produce a 500 DC voltage (+/- 250V). In this example the converter chopping frequency is 1620 Hz and the power system frequency is 60 Hz.



The VSC is controlled in a closed loop by two PI regulators in order to maintain a DC voltage of 500 V at the load while maintaining a unity input power factor for the AC supply.



The initial conditions for a steady state simulation are generated by running an initial simulation to steady-state for an integer number of cycles of 60 Hz. The final states (both Power System Blockset and Simulink controller states) are saved in a vector called xInitial. This vector, as well as the sample times (Ts Power and Ts Control) are saved in the psb3levelVSC xinit.mat file.

Load the initial condition MAT file and start the simulation. Observe the following signals:

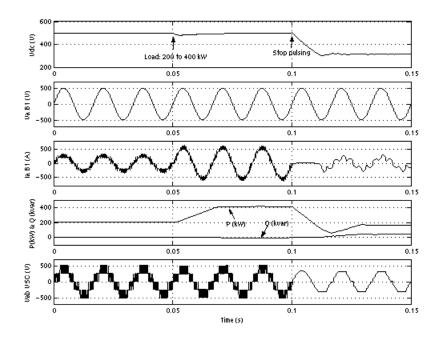
- The DC voltage (Vdc Scope block)
- The primary voltage and current of phase A of the AC supply (VaIa Scope block)
- The device currents of leg A of the IGBT bridge (Ia_Devices Scope block inside the Measurements & Signals subsystem)
- The line-to-line terminal voltage of the VSC (Vab_VSC Scope block)

At 50 ms, a 200-kW load is switched in. You can see that the dynamic response of the DC regulator to the sudden load variation from 200 kW to 400 kW is

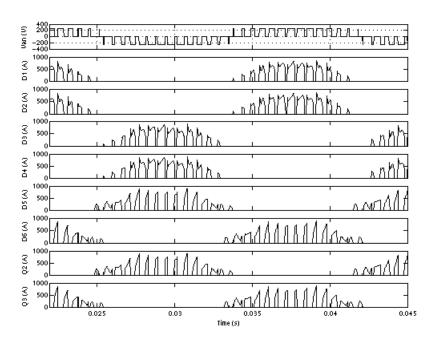
satisfactory. The DC voltage reverts to 500 V within 2 cycles and the unity power factor on the AC side is maintained.

At 100 ms, a *Stop Pulsing* signal is activated and the pulses normally sent to the converter are blocked. You can see that the DC voltage drops to 315 V. A drastic change in the primary current waveform can also be observed. When the pulses are blocked, the Three-Level Bridge block operation becomes similar to a three-phase diode bridge.

The following two figures summarize the results of the simulation. The first figure shows the operation of the AC-DC converter during the load variation and when the pulses are blocked.



The second figure shows the current flowing in the various devices of the IGBT bridge when the converter is feeding 500 Vdc to a 200-kW load.



Purpose

Implement a three-phase transformer with configurable winding connections

Library

Elements

Description



The Three-Phase Transformer (Two Windings) block implements a three-phase transformer using three single-phase transformers. You can simulate the saturable core or not simply by setting the appropriate check box in the parameter menu of the block. See the Linear Transformer block and Saturable Transformer block sections for a detailed description of the electrical model of single-phase transformers.

The two windings of the transformer can be connected in the following manner:

- Y
- Y with accessible neutral
- Grounded Y
- Delta (D11), delta leading Y by 30 degrees
- Delta (D1), delta lagging Y by 30 degrees

Note The D11 and D1 convention assumes that the Y voltage phase angle is at noon (12) on a clock display. D1 and D11 refer respectively to 11 AM (-30 degrees) and 1 PM (+30 degrees).

The block takes into account the connection type you have selected, and the icon of the block is automatically updated. An input port labeled N is added to the block if you select the Y connection with accessible neutral for winding 1. If you ask for an accessible neutral on winding 2, an extra outport port labeled n is generated.

The following icons are displayed for four arbitrary settings





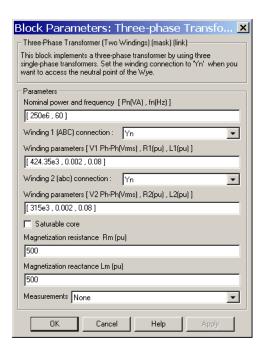




The saturation characteristic, when activated, is the same as the one described for the Saturable Transformer block, and the icon of the block is automatically updated. If the fluxes are not specified, the initial values are automatically adjusted so that the simulation starts in steady state.

The leakage inductance and resistance of each winding are given in p.u. based on the transformer nominal power Pn and on the nominal voltage of the winding (V1 or V2). Refer to the per unit explanations given in the Linear Transformer and Saturable Transformer blocks reference sections.

Dialog Box and Parameters



Nominal power and frequency

The nominal power rating, in volt amperes (VA), and nominal frequency, in hertz (Hz), of the transformer.

Winding 1 (ABC) connection

The phase connection for winding 1.

Winding parameters

The phase-to-phase nominal voltage in volts rms, resistance, and leakage inductance in p.u. for winding 1.

Winding 2 (abc) connection

The phase connection for winding 2.

Winding parameters

The phase-to-phase nominal voltage in volts rms, resistance, and leakage inductance in p.u. for winding 2.

Saturable core

If selected, implements a saturable three-phase transformer.

Magnetization resistance Rm

The magnetization resistance Rm, in p.u.

Magnetization reactance Lm

The magnetization inductance Lm, in p.u., for a nonsaturable core. The **Magnetization reactance Lm** parameter is not visible in the dialog box if the **Saturable core** parameter is selected.

Saturation characteristic

The saturation characteristic for the saturable core. Specify a series of current/ flux pairs (in p.u.) starting with the pair (0,0). This parameter is visible only if the **Saturable core** parameter is selected.

Specify initial fluxes

If selected, the initial fluxes are defined by the [phiOA phiOB phiOC] parameter.

[phi0A, phi0B, phi0C]

Specifies initial fluxes for each phase of the transformer. This parameter is visible only if the **Specify initial fluxes** and **Saturable core** parameters are selected.

Measurements

Select **Winding voltages** to measure the voltage across the winding terminals of the Three-Phase Transformer block.

Select **Winding currents** to measure the current flowing through the windings of the Three-Phase Transformer block.

Select **Fluxes and magnetization currents** to measure the flux linkage, in volt seconds (V.s), and the magnetization current (for saturable transformers only).

Select **All measurement (V, I, Flux)** to measure the winding voltages, currents, magnetization currents, and the fluxes.

Place a Multimeter block in your model to display the selected measurements during the simulation. In the **Available Measurements** list box of the Multimeter block, the measurements are identified by a label followed by the block name.

If the **Winding 1 ABC connection** parameter is set to **Y**, **Yn**, or **Yg**, the labels are as follows.

Measurement	Label
Winding voltages	Uan_w1:, Ubn_w1:, Ucn_w1: or: Uag_w1:, Ubg_w1:, Ucg_w1:
Winding currents	Ian_w1:, Ibn_w1:, Icn_w1: or: Iag_w1:, Ibg_w1:, Icg_w1:
Fluxes	Flux_A:, Flux_B:, Flux_C:
Magnetization currents	Imag_A:, Imag_B:, Imag_C:

If the **Winding 1 ABC connection** parameter is set to **Delta (D11)** or **Delta (D1)**, the labels are as follows.

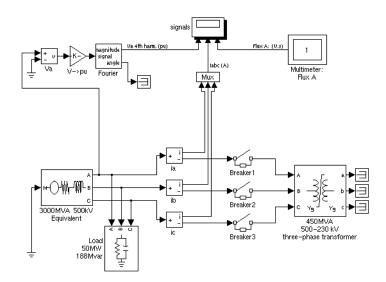
Measurement	Label
Winding voltages	Uab_w1:, Ubc_w1:, Uca_w1:
Winding currents	Iab_w1:, Ibc_w1:, Ica_w1:

Measurement	Label
Fluxes	Flux_A:, Flux_B:, Flux_C:
Magnetization currents	Imag_A:, Imag_B:, Imag_C:

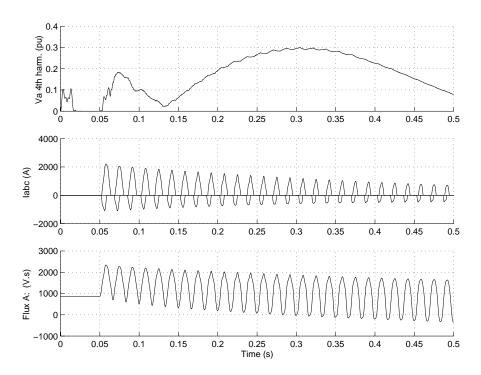
The same labels apply for the **Winding 2** (abc) connection parameter, except that 1 is replaced by 2 in the labels.

Example

The psbtransfo3ph.mdl circuit is using the Three-Phase Transformer block where the saturable core is simulated. Both windings are connected in a Y grounded configuration. Note that the neutral points of the two windings are internally connected to the ground.



Run the simulation and observe the simulation results.



See Also Three-Phase Transformer (Three Windings)

Purpose

Implement a three-phase transformer with configurable phase connection

Library

Elements

Description



This block implements a three-phase transformer by using three single-phase transformers with three windings. You can simulate the saturable core or not simply by setting the appropriate check box in the parameter menu of the block. See the Linear Transformer and Saturable Transformer block sections for a detailed description of the electrical model of single-phase transformers.

The three windings of the transformer can be connected in the following manner:

- Y
- Y with accessible neutral (for windings 1 and 3 only)
- Grounded Y
- Delta (D11), delta lagging Y by 30 degrees
- Delta (D1), delta leading Y by 30 degrees

Note The D11 and D1 convention assumes that the Y voltage phase angle is at noon (12) on a clock display. D1 and D11 refer respectively to 11 AM (-30 degrees) and 1 PM (+30 degrees).

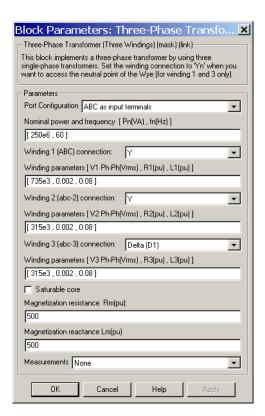
The block takes into count the connection type you have selected, and the icon of the block is automatically updated. An input port labeled N is added to the block if you select the Y connection with accessible neutral for winding 1. If you ask for an accessible neutral on winding 3, an extra outport port labeled n3 is generated.

The following icons are displayed for three arbitrary settings.

The saturation characteristic, when activated, is the same as the one described for the Saturable Transformer block, and the icon of the block is automatically updated. If the fluxes are not specified, the initial values are automatically adjusted so that the simulation starts in steady state.

The leakage inductances and resistance of each winding are given in p.u. based on the transformer nominal power Pn and on the nominal voltage of the winding (V1, V2, or V3). Refer to the per unit explanations given in the Linear Transformer and Saturable Transformer blocks reference sections.

Dialog Box and Parameters



Port configuration

Specify the ABC port of winding 1 as input terminals or as output terminals. The terminals of windings 2 and 3 are reconfigured consequently.

Nominal power and frequency

The nominal power rating, in volt amperes (VA), and nominal frequency, in hertz (Hz), of the transformer.

Winding 1 (ABC) connection

The phase connection for winding 1.

Winding parameters

The phase-to-phase nominal voltage in volts rms, resistance, and leakage inductance in p.u. for winding 1.

Winding 2 (abc2) connection

The phase connection for winding 2.

Winding parameters

The phase-to-phase nominal voltage in volts rms, resistance, and leakage inductance in p.u. for winding 2.

Winding 3 (abc3) connection

The phase connection for winding 3.

Winding parameters

The phase-to-phase nominal voltage in volts rms, resistance, and leakage inductance in p.u. for winding 3.

Saturable core

If selected, implements a saturable three-phase transformer.

Magnetization resistance Rm

The magnetization resistance Rm, in p.u.

Magnetization reactance Lm

The magnetization inductance Lm, in p.u., for a nonsaturable core. The **Magnetization reactance Lm** parameter is not visible in the dialog box if the **Saturable core** parameter is selected.

Saturation characteristic

The saturation characteristic for the saturable core. Specify a series of current/ flux pairs (in p.u.) starting with the pair (0,0). This parameter is visible only if the **Saturable core** parameter is selected.

Specify initial fluxes

If selected, the initial fluxes are defined by the [phiOA phiOB phiOC] parameter.

[phi0A phi0B phi0C] (p.u.):

Specifies initial fluxes for each phase of the transformer. This parameter is visible only if the **Specify initial fluxes** and **Saturable core** parameters are selected.

Measurements

Select **Winding voltages** to measure the voltage across the winding terminals of the Three-Phase Transformer block.

Select **Winding currents** to measure the current flowing through the windings of the Three-Phase Transformer block.

Select **Fluxes and magnetization currents** to measure the winding voltages, currents, magnetization currents, and the fluxes.

Select **All measurements** (**V**, **I**, **Flux**) to measure the winding voltages, winding currents, magnetization currents, and fluxes.

Place a Multimeter block in your model to display the selected measurements during the simulation. In the **Available Measurements** list box of the Multimeter block, the measurements are identified by a label followed by the block name.

If the **Winding 1 ABC connection** parameter is set to **Y**, **Yn**, or **Yg**, the labels are as follows.

Measurement	Label
Winding voltages	Uan_w1:, Ubn_w1:, Ucn_w1: or: Uag_w1:, Ubg_w1:, Ucg_w1:
Winding currents	Ian_w1:, Ibn_w1:, Icn_w1: or: Iag_w1:, Ibg_w1:, Icg_w1:

Measurement	Label
Fluxes	Flux_A:, Flux_B:, Flux_C:
Magnetization currents	Imag_A:, Imag_B:, Imag_C:

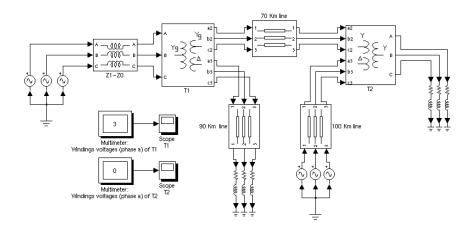
If the **Winding 1 ABC connection** parameter is set to **Delta (D11)** or **Delta (D1)**, the labels is as follows.

Measurement	Label
Winding voltages	Uab_w1:, Ubc_w1:, Uca_w1:
Winding currents	Iab_w1:, Ibc_w1:, Ica_w1:
Fluxes	Flux_A:, Flux_B:, Flux_C:
Magnetization currents	Imag_A:, Imag_B:, Imag_C:

The same labels apply for the **Winding 2** (abc2) connection and **Winding 3** (abc3) connection parameters, except that the 1 is replaced by 2 or by 3 in the labels.

Example

The psbtransfo3wdn.mdl circuit is using two Three-Phase Transformer blocks. In this example the ABC terminals of winding 1 of the T1 transformer are configured as inputs and the ABC terminals of winding 1 of the T2 transformer are configured as outputs.



See Also Three-Phase Transformer (Two Windings)

Three-Phase V-I Measurement

Purpose

Measure three-phase currents and voltages in a circuit

Library

Extras/Measurements

Description



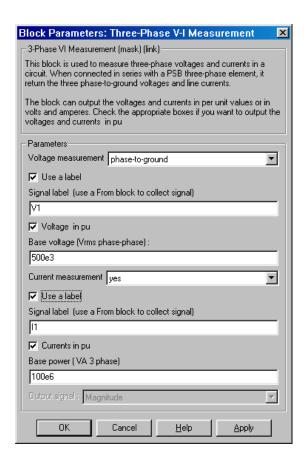
The Three-Phase V-I Measurement block is used to measure three-phase voltages and currents in a circuit. When connected in series with three-phase elements, it returns the three phase-to-ground voltages and the three line currents.

The block can output the voltages and currents in per unit (p.u.) values or in volts and amperes. If you choose to measure the voltages and currents in p.u., the Three-Phase V-I Measurement block does the following conversions:

$$V_{abc}(\text{p.u.}) = \frac{V_{abc}(\text{volts})}{(V_{base} \cdot \sqrt{2} / \sqrt{3})}$$

$$I_{abc}(\text{p.u.}) = \frac{I_{abc}(\text{amperes})}{P_{base}/(V_{base} \cdot \sqrt{2}/\sqrt{3})}$$

Dialog Box and Parameters



Voltage measurement

Select **no** if you do not want to measure three-phase voltage. Select **phase-to-ground** if you want to measure the phase-to-ground voltages. Select **phase-to-phase** if you want to measure the phase-phase voltages.

Use a label

If selected, the voltage measurements are sent to a labeled signal. Use a From block to read the voltages. The goto tag of the From block must correspond to the label specified by the **Signal label** parameter. If not selected, the voltage measurements are available via the Vabc output of the block.

Three-Phase V-I Measurement

Signal label

Specifies a label tag for the voltage measurements.

Voltages in p.u.

If selected, the three-phase voltages are measured in p.u. Otherwise they are measured in volts.

Base voltage (Vrms phase-phase)

The base voltage, in volts rms, used to convert the measured voltages in p.u. The **Base voltage (Vrms phase-phase)** parameter is not visible in the dialog box if the **Measure voltages Vabc in p.u./Vbase** parameter is not selected.

Current measurement

Select **yes** if you want to measure the three-phase currents that flow through the block.

Use a label

If selected, the current measurements are sent to a labeled signal. Use a From block to read the currents. The goto tag of the From block must correspond to the label specified by the **Signal label** parameter. If not selected, the current measurements are available via the Iabc output of the block.

Signal label

Specifies a label tag for the current measurements.

Currents in p.u.

If selected, the three-phase currents are measured in p.u. Otherwise they are measured in amperes.

Base power (VA 3 phase)

The base power, in volt-ampere (VA), used to convert the measured currents in p.u. The **Base power (VA 3 phase)** parameter is not visible in the dialog box if the **Measure currents Iabc in p.u./Pbase** parameter is not selected.

Output signal

Specifies the format of the measurement signal when the block is used in a phasor simulation. The **Output signal** parameter is disabled when the

Three-Phase V-I Measurement

block is not used in a phasor simulation. The phasor simulation is activated by a Powergui block placed in the model.

Set to **Complex** to output the measured voltages and currents as complex values. The outputs are complex signals.

Set to **Real-Imag** to output the real and imaginary parts of the measured voltages and currents.

Set to **Magnitude-Angle** to output the magnitudes and angles of the measured voltages and currents.

Set to **Magnitude** to output the magnitudes of the measured voltages and currents. The output is a scalar value.

Inputs and Outputs

A, B, C

Inputs 1, 2, 3 and outputs 1, 2, 3 are the phase connectors of the measurement block. Connect the Three-Phase V-I Measurement block in series with other three-phase electrical blocks.

Vabc

Output 4 is a vector containing the three measured phase-to-ground voltages.

Iabc

Output 5 is a vector containing the three measured line currents.

Example

See the psb3phseriescomp.mdl for a demo using the Three-Phase V-I Measurement block.

Thyristor

Purpose

Implement a thyristor model

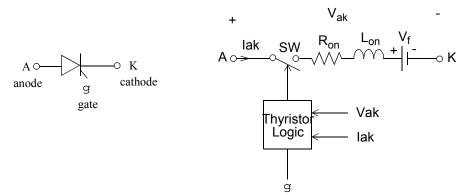
Library

Power Electronics

Description

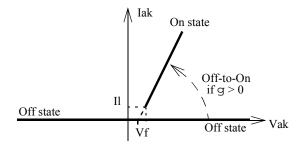
A thyristor is a semiconductor device that can be turned on via a gate signal. The thyristor model is simulated as a resistor Ron, an inductor Lon, and a DC voltage source Vf, connected in series with a switch. The switch is controlled by a logical signal depending on the voltage Vak, the current Iak, and the gate signal g.





The Thyristor block also contains a series Rs-Cs snubber circuit that can be connected in parallel with the thyristor device.

The static VI characteristic of this model is shown.



The thyristor device turns on when the anode-cathode voltage is greater than Vf and a positive pulse signal is applied at the gate input (g>0). The pulse height must be greater than 0 and last long enough to allow the thyristor anode current to become larger than the latching current Il.

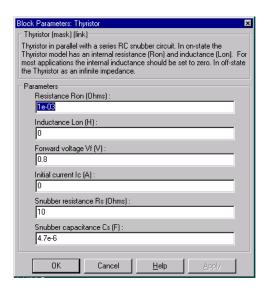
The thyristor device turns off when the current flowing in the device becomes 0 (Iak = 0) and a negative voltage appears across the anode and cathode for at least a period of time equal to the turnoff time Tq. If the voltage across the device becomes positive within a period of time less than Tq, the device turns on automatically even if the gate signal is low (g = 0) and the anode current is less than the latching current. Furthermore, if during turn-on, the device current amplitude stays below the latching current level specified in the dialog box, the device turns off after the gate signal level becomes low (g = 0).

The turnoff time Tq represents the carrier recovery time: it is the time interval between the instant the anode current has decreased to 0 and the instant when the thyristor is capable of withstanding positive voltage Vak without turning on again.

Dialog Boxes and Parameters

Thyristor Model and Detailed Thyristor Model

In order to optimize simulation speed, two models of thyristors are available: the thyristor model and the detailed thyristor model. For the thyristor model, the latching current Il and recovery time Tq are assumed to be 0.



Resistance Ron

The thyristor internal resistance Ron, in ohms (Ω) . The **Resistance Ron** parameter cannot be set to 0 when the **Inductance Lon** parameter is set to 0.

Inductance Lon

The thyristor internal inductance Lon, in henries (H). The **Inductance Lon** parameter cannot be set to 0 when the **Resistance Ron** parameter is set to 0.

Forward voltage Vf

The forward voltage of the thyristor, in volts (V).

Initial current Ic

When the **Inductance Lon** parameter is greater than 0, you can specify an initial current flowing in the thyristor. It is usually set to 0 in order to start the simulation with the thyristor blocked.

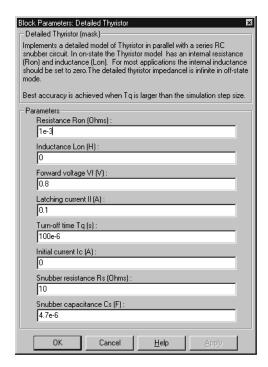
You can specify an **Initial current Ic** value corresponding to a particular state of the circuit. In such a case all states of the linear circuit must be set accordingly. Initializing all states of a power electronic converter is a complex task. Therefore, this option is useful only with simple circuits.

Snubber resistance Rs

The snubber resistance, in ohms (Ω) . Set the **Snubber resistance Rs** parameter to inf to eliminate the snubber from the model.

Snubber capacitance Cs

The snubber capacitance in farads (F). Set the **Snubber capacitance Cs** parameter to 0 to eliminate the snubber, or to inf to get a resistive snubber.



Latching current Il

The latching current of the detailed thyristor model, in amperes (A).

Turn off time Tq

The turnoff time Tq of the detailed thyristor model, in amperes (A).

Inputs and Outputs

The Thyristor block consists of two inputs and two outputs. The first input and output are the thyristor terminals connected respectively to anode (a) and

Thyristor

cathode (k). The second input (g) is a Simulink logical signal applied to the gate. The second output (m) is a Simulink measurement output vector [Iak Vak] returning the thyristor current and voltage.

Assumptions and Limitations

The Thyristor block implements a macromodel of the real thyristor. It does not take into account either the geometry of the device or complex physical processes that model the behavior of the device [1, 2]. The forward breakover voltage and the critical value of the derivative of the reapplied anode-cathode voltage are not considered by the model.

Depending on the value of **Inductance Lon**, the Thyristor block is modeled either as a current source (Lon > 0) or as a variable topology circuit (Lon = 0). See Chapter 3, "Advanced Topics," for more details.

As the Thyristor block is modeled as a current source, it cannot be connected in series with an inductor, a current source, or an open circuit, unless a snubber circuit is used.

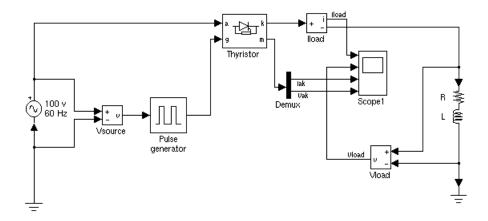
You must use a stiff integrator algorithm to simulate circuits containing thyristors. ode23tb or ode15s with default parameters usually gives the best simulation speed.

The inductance Lon is forced to 0 if you choose to discretize your circuit.

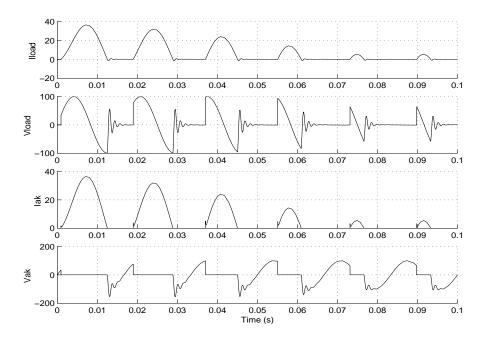
Example

In the psbthyristor.mdl demo a single-pulse thyristor rectifier is used to feed an RL load. The gate pulses are obtained from a pulse generator synchronized on the source voltage. The circuit is available in the file. The following parameters are used:

```
R = 1W; L = 10mH; Thyristor block: Ron = 0.001 \Omega, Lon = 0 H, Vf = 0.8 V, Rs = 20 \Omega, Cs = 4e-6 F.
```



The firing angle is varied by a pulse generator synchronized on the voltage source. Run the simulation and observe the load current and load voltage, as well as the thyristor current and voltage.



Thyristor

References [1] F

 $\label{eq:computer-Aided Analysis of Power Electronic Systems,} \\ [1] \ Rajagopalan, V., Computer-Aided Analysis of Power Electronic Systems, \\ [2]$

Marcel Dekker, Inc., New York, 1987.

[2] Mohan, N., T.M. Undeland, and W.P. Robbins, *Power Electronics: Converters, Applications, and Design*, John Wiley & Sons, Inc., New York,

1995.

See Also

Diode, Universal Bridge

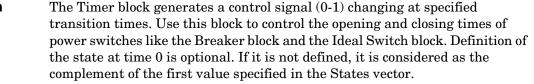
Purpose

Generate a control logical signal changing at specified transition times

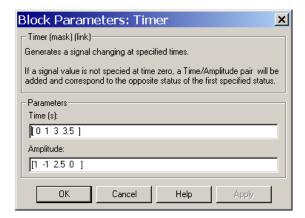
Library

Extras/Control Blocks

Description



Dialog Box and Parameters



Transition times (s)

The transition times, in seconds, when the output of the block changes its value as defined by the **States** (0/1) parameter. The **Transition times** (s) parameter must be a vector of the same length as the vector defined in the **States** (0/1) parameter. The definition of the time 0 is optional.

States (0/1)

The sequence of states to be generated by the Timer block. The **States** (0/1) parameter must be a scalar or a vector of 0 and 1. The **States** (0/1) parameter must be a vector of the same length as the vector defined in the **Transition times** (s) parameter. The definition of the initial state is optional.

Timer

Sample time (s)

Specify the sample time, in seconds, to synchronize the transitions of the Timer block when the simulation is discrete.

Inputs and Outputs

The output is a logical signal that is used to control the switching of any Power Electronics device or Breaker block.

Example

See the Breaker block Example section for a demo using the Timer block. The demo circuit is psbbreaker.mdl.

Purpose

Measure the total harmonic distortion (THD) of a signal

Library

Extras/Measurements, Extras/Discrete Measurements

Description



The Total Harmonic Distortion block measures the total harmonic distortion (THD) of a periodic distorted signal. The signal can be a measured voltage or current.

The THD is defined as the root mean square (RMS) value of the total harmonics of the signal, divided by the RMS value of its fundamental signal. For example, the THD of a measured current is defined as

total harmonic distortion (THD) =
$$\frac{I_H}{I_F}$$

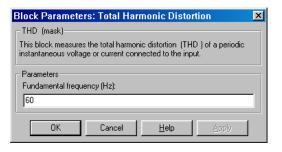
where

$$I_H = \sqrt{{I_2}^2 + {I_3}^2 + \ldots + {I_n}^2}$$
 I_2 : RMS value of the harmonic n

 I_F : RMS value of the fundamental current

It follows that the THD is null for a pure sinusoidal voltage or current.

Dialog Box and Parameters



Fundamental frequency (Hz)

The frequency, in hertz, of the fundamental signal.

Inputs and Outputs

Connect to the first input the voltage or current you want to measure the total harmonic distortion. The output returns the THD of the input signal.

Purpose

Implement a universal three-phase bridge converter with selectable configuration and power switch type

Library

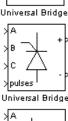
Power Electronics

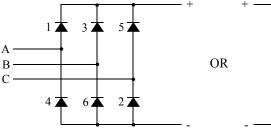
Description

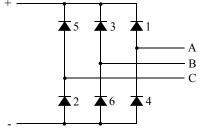
The Universal Bridge block implements a universal three-phase power converter that consists of six power switches connected as a bridge. The type of power switch and converter configuration are selectable from the dialog box.



Diode bridges:



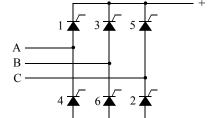


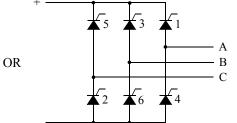




Thyristor bridges:







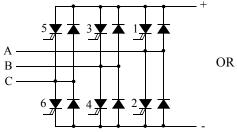


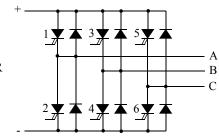
Universal Bridge



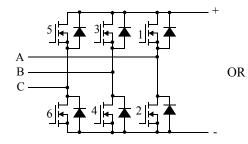
Universal Bridge

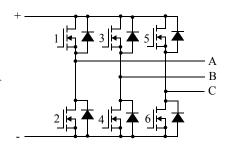
GTO-Diode bridges:



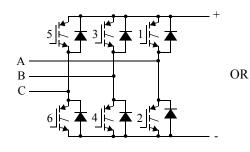


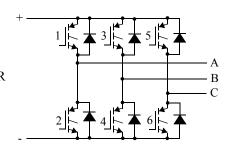
MOSFET-Diode bridges:



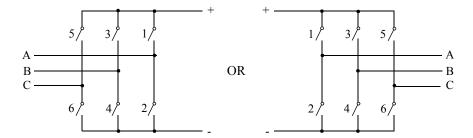


IGBT-Diode bridges:

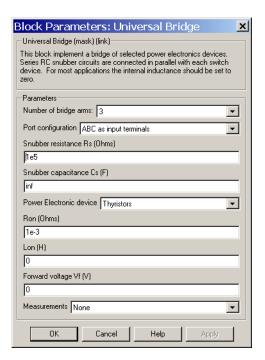




Ideal switch bridge:



Dialog Box and Parameters



Port configuration

Set to **ABC** as input terminals to connect the A, B, and C phases of the bridge to the input ports one, two, and three of the Universal Bridge block. The DC + and - terminals are connected at outputs one and two.

Set to **ABC** as **output terminals** to connect the A, B, and C phases of the bridge to the output ports one, two, and three of the Universal Bridge block. The DC + and - terminals are connected at outputs one and two.

Snubber resistance Rs

The snubber resistance, in ohms (Ω) . Set the **Snubber resistance Rs** parameter to inf to eliminate the snubbers from the model.

Snubber capacitance Cs

The snubber capacitance, in farads (F). Set the **Snubber capacitance Cs** parameter to 0 to eliminate the snubbers, or to inf to get a resistive snubber.

Power electronic device

Select the type of power electronic device to use in the bridge.

Ron (ohms)

Internal resistance of the selected device, in ohms (Ω) .

Lon (H)

Internal inductance, in henries (H), for the Diode, the Thyristor, or the MOSFET device.

[Tf(s) Tt(s)]

Fall time Tf and tail time Tt, in seconds (s), for the GTO or the IGBT devices.

Measurements

Select **Device voltages** to measure the voltage across the six power electronic device terminals.

Select **Device currents** to measure the current flowing through the six power electronic devices. If the snubber devices are defined, the measured currents are the ones flowing through the power electronic devices only.

Select **UAB UBC UCA UDC voltages** to measure the terminal voltages (AC and DC) of the Universal Bridge block.

Select **All voltages and currents** to measure all voltages and currents defined for the Universal Bridge block.

Place a Multimeter block in your model to display the selected measurements during the simulation. In the **Available Measurements** list box of the Multimeter block, the measurement is identified by a label followed by the block name.

Measurement	Label
Device voltages	Usw1:, Usw2:,Usw3:,Usw4:,Usw5:,Usw6:
Branch current	Usw1:, Usw2:, Usw3:, Usw4:, Usw5:, Usw6:
Terminal voltages	Uab:, Ubc), Uca:, Udc:

Inputs and Outputs

The bridge configuration is selectable so that the inputs and outputs depend on the configuration chosen:

- When A, B, C are selected as inputs, the DC terminals are the outputs.
- When A, B, C are selected as outputs, the DC terminals are the inputs.

Except for the case of a diode bridge, the Pulses input accepts a Simulink-compatible vectorized gating signal containing six pulse trains. The gating signals are sent to the power switches according to the number shown in the diagrams above.

Note The pulse ordering in the vector of the gate signals corresponds to the switch number indicated in the six circuits shown in the Description section. For the diode and thyristor bridges, the pulse ordering corresponds to the natural order of commutation. For all other forced-commutated switches, pulses are sent to upper and lower switches of phases A, B, and C with the following order: [A upper A lower B upper B lower C upper C lower].

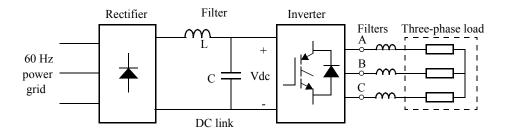
Assumptions and Limitations

Universal Bridge blocks can be discretized to be used in a discrete time step simulation specified by the Discrete System block. In this case, the internal commutation logic of the Universal Bridge takes care of the commutation between the power switches and the diodes in the converter legs.

Note In a converter built with individual forced-commutated power components (GTOs, MOSFETs, IGBTs), discretization of the model is not available. See Chapter 3, "Advanced Topics," for more details.

Example

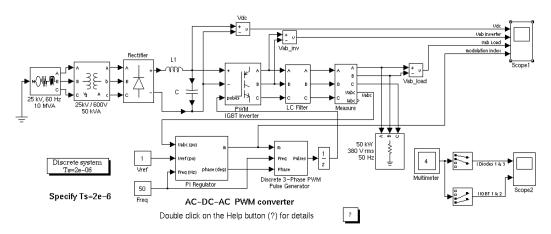
The psbbridges.mdl demo illustrates the use of two Universal Bridge blocks in an AC-AC converter consisting of a rectifier feeding an IGBT inverter through a DC link. The inverter is pulse-width modulated (PWM) to produce a three-phase variable-voltage variable-frequency sinusoidal voltage to the load. In this example the inverter chopping frequency is 2000 Hz and the modulation frequency is 50 Hz.



The IGBT inverter is controlled in a closed loop with a PI regulator in order to maintain a 1 p.u. voltage (380 Vrms, 50 Hz) at the load terminals.

A Multimeter block is used to observe commutation of currents between diodes 1 and 3 in the diode bridge and between IGBT/Diodes switches 1 and 2 in the IGBT bridge.

.The circuit is available in the demo.

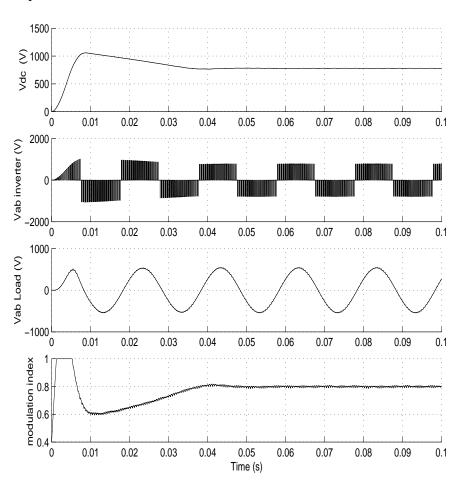


Start simulation. After a transient period of approximately 70 ms, the system reaches a steady state. Observe voltage waveforms at DC bus, inverter output, and load on Scope 1. The harmonics generated by the inverter around multiples of 2 kHz are filtered by the LC filter. As expected the peak value of the load voltage is 537 V (380 V rms).

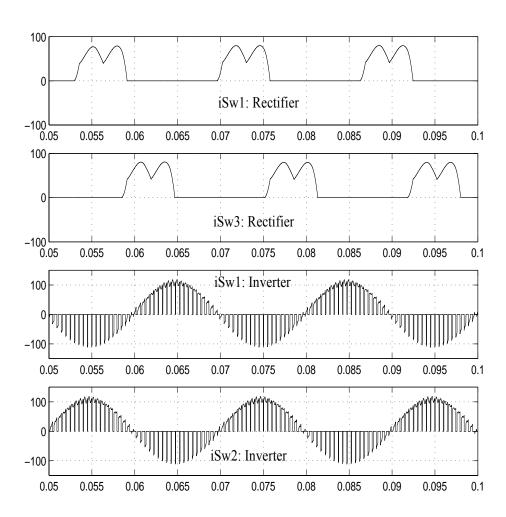
In steady state the mean value of the modulation index is m=0.77, and the mean value of the DC voltage is 780 V. The fundamental component of 50 Hz voltage buried in the chopped inverter voltage is therefore

Observe diode currents on trace 1 of Scope2, showing commutation from diode 1 to diode 3. Also observe on trace 2 currents in switches 1 and 2 of the IGBT/Diode bridge (upper and lower switches connected to phase A). These two currents are complementary. A positive current indicates a current flowing in the IGBT, whereas a negative current indicates a current flowing in the antiparallel diode.





Scope 2



See Also

Diode, GTO, Ideal Switch, IGBT, MOSFET, Thyristor

Voltage Measurement

Purpose Measure a voltage in a circuit

Library Measurements

Description The Voltage Measurement block measures the instantaneous voltage between

two electric nodes. The output provides a Simulink signal that can be used by

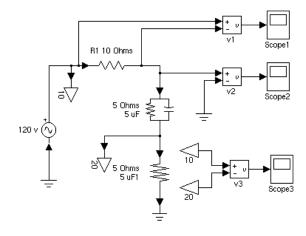
other Simulink blocks.





Example

The psbvoltmeasure.mdl demo uses three Voltage Measurement blocks to read voltages.



See Also Current Measurement

Purpose

Implement a zigzag phase-shifting transformer with a configurable secondary winding connection

Library

Elements

Description



The Zigzag Phase-Shifting Transformer block implements a three-phase transformer with a primary winding connected in a zigzag configuration and a configurable secondary winding. The model uses three single-phase, three-winding transformers. The primary winding connects the windings 1 and 2 of the single-phase transformers in a zigzag configuration. The secondary winding uses the windings 3 of the single phase transformers, and they can be connected in the following manner:

- Y
- Y with accessible neutral
- Grounded Y
- Delta (D11), delta leading Y by 30 degrees
- Delta (D1), delta lagging Y by 30 degrees

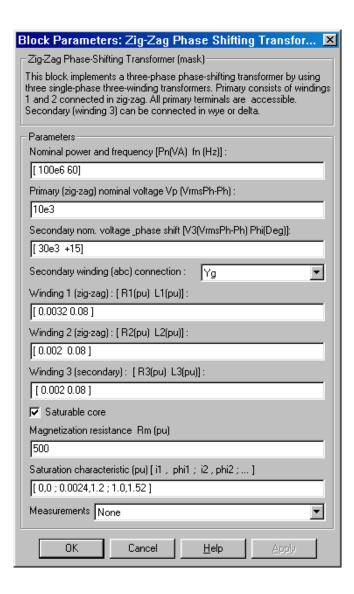
Note The D11 and D1 convention assumes that the Y voltage phase angle is at noon (12) on a clock display. D1 and D11 refer respectively to 11 AM (-30 degrees) and 1 PM (+30 degrees).

If the secondary winding is connected in Y, the secondary phase voltages are leading or lagging the primary voltages by the Phi phase angle in the parameters of the block. If the secondary winding is connected in Delta (D11), an additional phase shift of 30 degrees is added to the phase angle. If the secondary winding is connected in Delta (D1), a phase shift of -30 degrees is added to the phase angle.

The block takes into account the connection type you have selected and the icon of the block is automatically updated. An output port labeled N is added to the block if you select the Y connection with accessible neutral for the secondary winding.

The saturation characteristic, when activated, is the same as the one described for the Saturable Transformer block.

Dialog Box and Parameters



Nominal power and frequency

The nominal power rating, in volt amperes (VA), and nominal frequency, in hertz (Hz), of the transformer.

Primary (zigzag) nominal voltage Vp

The phase-to-phase nominal voltage in volts rms, for the primary winding of the transformer.

Secondary nominal voltage and phase shift

The phase-to-phase nominal voltage, in volts rms, and the phase shift, in degrees, for the secondary winding of the transformer.

Secondary winding (abc) connection

The phase connection for the secondary winding.

Winding 1 (zigzag): [R1(p.u.) L1(p.u.)]

The resistance and leakage inductance of the windings 1 of the single-phase transformers used to implement the primary winding of the Zigzag Phase-Shifting Transformer.

Winding 2 (zigzag): [R2(p.u.) L2(p.u.)]

The resistance and leakage inductance of the windings 2 of the single-phase transformers used to implement the primary winding of the Zigzag Phase-Shifting Transformer.

Winding 3 (secondary): [R1(p.u.) L1(p.u.)]

The resistance and leakage inductance of the windings 3 of the single-phase transformers used to implement the secondary winding of the Zigzag Phase-Shifting Transformer.

Saturable core

If selected, implements a saturable core.

Magnetization resistance Rm

The magnetization resistance Rm, in p.u, when the saturation is simulated. This parameter is visible only if the **Saturable core** parameter is selected.

Magnetizing branch: [Rm(p.u.) Lm(p.u.)]

The magnetization resistance Rm and inductance Lm, in p.u., when the saturation is not simulated. The **Magnetizing branch** parameter is not visible in the dialog box if the **Saturable core** parameter is selected.

Saturation characteristic

The saturation characteristic for the saturable core. Specify a series of current/ flux pairs (in p.u.) starting with the pair (0,0). This parameter is visible only if the **Saturable core** parameter is selected.

Measurements

Select **Phase voltages** to measure the voltage across the primary and secondary winding terminals of the block.

Select **Phase currents** to measure the current flowing through the primary and secondary windings of the block.

Select **Fluxes and magnetization currents** to measure the flux linkage, in volt seconds (V.s), and the magnetization current (when the saturation is simulated).

Select **All measurement (V, I, Flux)** to measure the winding voltages, currents, magnetization currents, and the fluxes.

Place a Multimeter block in your model to display the selected measurements during the simulation. In the **Available Measurement** list box of the Multimeter block, the measurements are identified by a label followed by the block name.

If the **Secondary winding (abc) connection** parameter is set to **Y**, **Yn**, or **Yg**, the labels are as follows.

Measurement	Label
Phase voltages	Uan:, Ubn:, Ucn: or: Uag:, Ubg:, Ucg:
Phase currents	Ian:, Ibn:, Icn: or: Iag:, Ibg:, Icg:

Measurement	Label
Fluxes	Flux_a:, Flux_b:, Flux_c:
Magnetization currents	Imag_a:, Imag_b:, Imag_c:

If the **Secondary winding (abc) connection** parameter is set to **Delta** (**D11**) or **Delta** (**D1**), the labels are as follows.

Measurement	Label
Phase voltages	Uab:, Ubc:, Uca:
Phase currents	Iab:, Ibc:, Ica:
Fluxes	Flux_a:, Flux_b:, Flux_c:
Magnetization currents	Imag_a:, Imag_b:, Imag_c:

The labels of the primary zigzag winding are as follows.

Measurement	Label
Phase voltages	UA:, UB:, UC:
Phase currents	IA:, IB:, IC:

Example

See the help text of the $psb48pulsegtoconverter\ demo.$

See Also

Three-Phase Transformer (Three Windings)

Power System Command Reference

This table indicates the tasks performed by the commands described in this chapter.

Command	Purpose
circ2ss	Compute the linear state-space model of an electrical circuit
power2sys	Analyze an electric circuit built with the Power System Blockset
powerinit	Set the initial states values of an electrical circuit

Purpose

Compute the state space model of a linear electrical circuit

Synopsis

You must call circ2ss with a minimum of seven input arguments.

```
[A,B,C,D,states,x0,x0sw,rlsw,u,x,y,freq,Asw,Bsw,Csw,Dsw,Hlin]=circ2ss(rlc,switches,source,line dist,yout,y type,unit)
```

You can also specify additional arguments. To use options, the number of input arguments must be 12, 13, 14 or 16.

```
[A,B,C,D,states,x0,x0sw,rlsw,u,x,y,freq,Asw,Bsw,Csw,Dsw,Hlin] =
circ2ss(rlc,switches,source,line_dist,yout,y_type,unit,
net_arg1,net_arg2,net_arg3,...,netsim_flag,fid_outfile,
freq_sys,ref_node,vary_name,vary_val)
```

Description

The circles command computes the state space model of a linear electrical circuit expressed as

$$\dot{x} = Ax + Bu$$

$$v = Cx + Du$$

where x is the vector of state space variables (inductor currents and capacitor voltages), u is the vector of voltage and current inputs, and y is the vector of voltage and current outputs.

When you build a circuit from Power System Blockset blocks of the **powerlib** library, circ2ss is automatically called by power2sys. circ2ss is also available as a stand-alone function for expert users. This allows you to generate state space models without using Power System Blockset graphical user interface and to access options that are not available through **powerlib**. For example, you can specify transformers and mutual inductances with more than three windings.

The linear circuit can contain any combination of voltage and current sources, RLC branches, multiwinding transformers, mutually coupled inductances, and switches. The state variables are inductor currents and capacitor voltages.

The state space representation (matrices A,B,C,D, and vector x0) computed by circ2ss can then be used in a Simulink system, via a State-Space block, to perform simulation of the electrical circuit (see Example section below). Nonlinear elements (mechanical or power electronic switches, transformer

saturation, machines, distributed parameter lines, etc.) can be connected to the linear circuit.

These Simulink models are interfaced with the linear circuit through voltage outputs and current inputs of the state space model. You can find the models of the nonlinear elements provided with the Power System Blockset in the **powerlib_models** library (see the "Advanced Topics" chapter).

Input Arguments

The number of input arguments must be 7, 12, 13, 14, or 16. Arguments 8 to 16 are optional. The first seven arguments that must be specified are

- rlc: Branch matrix specifying the network topology as well as the resistance R, inductance L, and capacitance C values. See format below.
- switches: Switch matrix. Specify an empty variable if no switches are used. See format below.
- source: Source matrix specifying the parameters of the electrical voltage and current sources. Specify an empty variable if no sources are used. See format below.
- line_dist: Distributed parameter line matrix. Specify an empty variable if no distributed lines are used. See format below.
- yout: String matrix of output expressions. See format below.
- y_type: Integer vector indicating output types (0 for voltage output, 1 for current output).
- unit: String specifying the units to be used for R, L, and C values in the rlc matrix. If unit = 'OHM', R L C values are specified in ohms Ω at the fundamental frequency specified by freq_sys (default value is 60 Hz). If unit = 'OMU', R L C values are specified in ohms (Ω) , millihenries (mH), and microfarads (μF) .

The last nine arguments are optional. The first three are used to pass arguments from the power2sys function. Hereafter, only the arguments to be specified when circ2ss is used as a stand-alone function are described:

• net_arg1, net_arg2, net_arg3: Used to pass arguments from power2sys. Specify an empty variable [] for each of these variables.

• netsim_flag: Integer controlling the messages displayed during the execution of circ2ss. Default value is 0.

If netsim_flag = 0, the version number, number of states, inputs, outputs, and modes are displayed. Output values are displayed in polar form for each source frequency.

If netsim_flag = 1, only version number, number of states, inputs, and outputs are displayed.

If netsim flag = 2, no message is displayed during execution.

- fid_outfile: File identifier of the circ2ss output file containing parameter values, node numbers, steady state outputs, and special messages. Default value is 0.
- freq_sys: Fundamental frequency (Hz) considered for specification of X_L and X_C reactances if unit is set to 'OHM'. Default value is 60 Hz.
- ref_node: Reference node number used for ground of PI transmission lines. If -1 is specified, the user is prompted to specify a node number.
- vary_name: String matrix containing the symbolic variable names used in output expressions. These variables must be defined in your MATLAB workspace.
- vary_val: Vector containing the values of the variable names specified in vary name.

Output Arguments

• A,B,C,D: State space matrices of the linear circuit with all switches open.

```
A(nstates, nstates), B(nstates, ninput), C(noutput, nstates), D (noutput, ninput),
```

where nstates is the number of state variables, ninput is the number of inputs, and noutput is the number of outputs.

• states: String matrix containing the names of the state variables. Each string has the following format:

```
Inductor currents: I1_bxx_nzz1_zz2
Capacitor voltages: Uc_bxx_nzz1_zz2
where
```

```
xx = branch number
zz1= first node number of the branch
zz2 = second node number of the branch
```

The last lines of the states matrix, which are followed by an asterisk, indicate inductor currents and capacitor voltages that are not considered as state variables. This situation arises when inductor currents or capacitor voltages are not independent (inductors forming a cut set or capacitors forming a loop). The currents and voltages followed by asterisks can be expressed as a linear combination of the other state variables:

- x0: Column vector of initial values of state variables considering the open or closed status of switches.
- x0sw: Vector of initial values of switch currents.
- rlsw: Matrix (nswitch,2) containing the R and L values of series switch impedances in ohms (Ω) and henries (H). nswitch is the number of switches in the circuit.
- u,x,y: Matrices u(ninput,nfreq), x(nstates,nfreq), and y(noutput,nfreq) containing the steady state complex values of inputs, states, and outputs. nfreq is the length of the freq vector. Each column corresponds to a different source frequency, as specified by the next argument, freq.
- freq: Column vector containing the source frequencies ordered by increasing frequencies.
- Asw, Bsw, Csw, Dsw: State space matrices of the circuit including the closed switches. Each closed switch with an internal inductance adds one extra state to the circuit.
- Hlin: Three-dimensional array (nfreq, noutput, ninput) of the nfreq complex transfer impedance matrices of the linear system corresponding to each frequency of the freq vector.

Format of the RLC Input Matrix

Two formats are allowed:

- Six columns: Implicit branch numbering. Branch numbers correspond to the RLC line numbers.
- Seven columns: Explicit branch numbering. Branch number Nobr is assigned by the user.

Each line of the RLC matrix must be specified according to the following format.

[node1, node2, type, R, L, C, Nobr] for RLC branch or line branch
[node1, node2, type, R, L, C, Nobr] for transformer magnetizing branch

[node1, node2, type, R, L, U, Nobr] for Transformer winding
[node1, node2, type, R, L, U, Nobr] for mutual inductances

- node1: First node number of the branch. The node number must be positive or zero. Decimal node numbers are allowed.
- node2: Second node number of the branch. The node number must be positive or zero. Decimal node numbers are allowed.
- type: Integer indicating the type of connection of RLC elements, or the transmission line length (negative value).

```
type = 0: Series RLC element
type = 1: Parallel RLC element
type = 2: Transformer winding
type = 3: Coupled (mutual) winding
```

If type is negative: the transmission line is modeled by a PI section. See details below.

For a mutual inductor or a transformer having N windings, N+1 consecutive lines must be specified in RLC matrix:

- 1 N lines with type = 2 or type = 3; (one line per winding). Each line specifies R/L/U or R/X1/Xc where [R/L, R/X1 = winding resistance and leakage reactance for a transformers or winding resistance and self reactance for mutually coupled windings. U is the nominal voltage of transformer winding (specify 0 if type = 3).
- 2 One extra line with type = 1 for the magnetizing branch of a transformer (parallel Rm/Lm or Rm/Xm) or one line with type = 0 for a mutual impedance (series Rm/Lm or Rm/Xm).

For a transformer magnetizing branch or a mutual impedance, the first node number is an internal node located behind the leakage reactance of the first winding. The second node number must be the same as the second node number of the first winding.

To model a saturable transformer, you must use a nonlinear inductance instead of the linear inductance simulating the reactive losses. Set the Lm/Xm value to 0 (no linear inductance) and use the Transfosat block, set with proper flux-current characteristics.

This block can be found in the **powerlib_models** library. It must be connected to the linear part of the system (State-Space block or S-function) between a voltage output (voltage across the magnetizing branch) and a current input (current source injected into the transformer internal node). See the "Example" at the end of the circ2ss documentation.

If type is negative, its absolute value specifies the length (km) of a transmission line simulated by a PI section. For a transmission line, the R/L/C or R/Xl/Xc values must be specified in Ω /km or Ω , mH, or μ F per km.

Parameter	Description
R	Branch resistance (Ω)
X1	Branch inductive reactance (Ω at freq_sys) or transformer winding leakage reactance (Ω at freq_sys)
L	Branch inductance (mH)
Xc	Branch capacitive reactance (Ω at freq_sys). The negative sign of Xc is optional.
С	Capacitance (µF)
U	Nominal voltage of transformer winding. Same units Volts or kV must be used for each winding. For a mutual inductance (type=3), this value must be set to zero.

Zero value for R, L or X1, C or Xc in a series or parallel branch indicates that the corresponding element does not exist.

The following restrictions apply for transformer winding R-L values. Null values are not allowed for secondary impedances if some transformer secondaries form loops (as in a three-phase delta connection). Specify a very low value for R or L or both (e.g., 1e-6 p.u. based on rated voltage and power) to simulate a quasi-ideal transformer. The resistive and inductive parts of the magnetizing branch can be set to infinite (no losses; specify Xm = Rm = inf).

Format of the Source Input Matrix

Three formats are allowed:

- Five columns: All sources are generating the same frequency specified by freq_sys.
- Six columns: The frequency of each source is specified in column 6.
- Seven columns: The seventh column is used to specify the type of nonlinear element modeled by the current source.

Each line of the source matrix must be specified according to the following format:

```
[ node1, node2, type, amp, phase, freq, model ]
```

- node1, node2: Node numbers corresponding to the source terminals. Polarity conventions:
 - Voltage source: node1 is the positive terminal.
 - Current source: Positive current flowing from node1 to node2 inside the source.
- type: Integer indicating the type of source: 0 for voltage source, 1 for current source.
- amp: Amplitude of the AC or DC voltage or current (V or A).
- phase: Phase of the AC voltage or current (degree).
- freq: Frequency (Hz) of the generated voltage or current. Default value is 60 Hz. For a DC voltage or current source, specify phase = 0 and freq = 0. amp can be set to a negative value. The generated signals are:

```
amp * sin(2\pi * freq * t + phase) for AC, amp for DC.
```

• model: Integer specifying the type of nonlinear element modeled by the current source (saturable inductance, thyristor, switch, ...). Used by power2sys only.

Order in Which Sources Must Be Specified

The functions that compute the state space representation of a system expect the sources in a certain order. This order must be respected in order to obtain correct results. You must be particularly careful if the system contains any switches. The following list gives the proper ordering of sources:

1 The currents form all switches that have a null inductance (Lon = 0), if any

- 2 The currents from all nonlinear models that have a finite inductance (switches with Lon > 0, the magnetizing inductance in saturable transformers, etc.), if any
- 3 All other voltage and current sources in any order, if any

Refer to the Example section below for an example illustrating proper ordering of sources for a system containing nonlinear elements.

Format of the Switches Input Matrix

Switches are nonlinear elements simulating mechanical or electronic devices such as circuit breakers, diodes, or thyristors. Like other nonlinear elements, they are simulated by current sources driven by the voltage appearing across their terminals. Therefore, they cannot have a null impedance. They are simulated as ideal switches in series with a series R-L circuit. Various models of switches (circuit breaker, ideal switch, and power electronic devices) are available in the **powerlib_models** library. They must be interconnected to the linear part of the system through appropriate voltage outputs and current inputs.

The switch parameters must be specified in a line of the switches matrix in seven different columns, according to the following format.

Parameter	Description
node1, node2	Node numbers corresponding to the switch terminals
status	Code indicating the initial status of the switch at t = 0: 0 = open; 1 = closed
R	Resistance of the switch when closed (Ω)
L/X1	Inductance of the switch when closed (mH) or inductive reactance (Ω at freq_sys)

For these last two fields, you must use the same units as those specified for the RLC matrix. Either field can be set to 0, but not both.

The next two fields specify the current input number and the voltage output number to be used for interconnecting the switch model to the state space block. The output number corresponding to the voltage across a particular switch must be the same as the input number corresponding to the current from the same switch (see Example section below):

- no I: Current input number coming from the output of the switch model.
- no U: Voltage output number driving the input of the switch model.

Format of the Line_Dist Matrix

The distributed parameter line model contains two parts:

- 1 A linear part containing current sources and resistances that are connected at the line sending and receiving buses together with the linear circuit.
- 2 A nonlinear part available in the Dist_line block of the **powerlib_models** library. This block performs the phase-to-mode transformations of voltage and currents and simulates the transmission delays for each mode. The Dist_line block must be connected to appropriate voltage outputs and current inputs of the linear part of the system. The line parameters have to be specified in the line dist matrix and also in the Dist_line block.

Each row of the line_dist matrix is used to specify a distributed parameter transmission line. The number of columns of line_dist depends on the number of phases of the transmission line.

For an nphase line, the first $(4 + 3 * nphase + nphase^2)$ columns are used. For example, for a three-phase line, 22 columns are used.

Parameter	Description
nphase	Number of phases of the transmission line
no_I	Input number in the source matrix corresponding to the first current source Is_1 of the line model. Each line model uses 2*nphase current sources specified in the source matrix as follows: Is_1, Is_2,, Is_nphase for the sending end followed by Ir_1, Ir_2,, Ir_nphase for the receiving end.

Parameter	Description (Continued)
nu_U	Output number of the state space corresponding to the first voltage output Vs_1 feeding the line model. Each line model uses 2*nphase voltage outputs in the source matrix as follows: Vs_1, Vs_2,, Vs_nphase for the sending end followed by Vr_1, Vr_2,, Vr_nphase for the receiving end.
length	Length of the line (km)
Zc	Vector of the nphase modal characteristic impedances (Ω)
Rm	Vector of the nphase modal series resistances (Ω/km)
speed	Vector of the nphase modal propagation speeds (km/s)
Ti	Transformation matrix from mode to phase currents such that Iphase = Ti * Imod. The nphase * nphase matrix must be given in vector format, [col_1, col_2, col_nphase]

Format of the Yout Matrix

The desired outputs are specified by a string matrix yout. Each line of the yout matrix must be an algebraic expression containing a linear combination of states and state derivatives, specified according to the following format:

Parameter	Description
Uc_bn	Capacitor voltage of branch n
Il_bn	Inductor current of branch n
dUc_bn	Derivative of Uc_bn or I1_bn
Un, In	Source voltage or current specified by line n of the source matrix
U_nx1_x2	Voltage between nodes x1 and x2

Parameter	Description (Continued)
I_bn	Current in branch n . For a parallel RLC branch, I_bn corresponds to the total current IR + IL + IC
I_bn_nx	Current flowing into node x of a PI transmission line specified by line n of the RLC matrix. This current includes the series inductive branch current and the capacitive shunt current.

Each output expression is built from voltage and current variable names defined above, their derivatives, constants, other variable names, parentheses and operators $(+-*/^{\circ})$, in order to form a valid MATLAB expression. For example:

```
yout =
char(['R1*I_b1+Uc_b3-L2*dI1_b2','U_n10_20','I2+3*I_b5']);
```

If variable names are used (R1 and L2 in the above example), their names and values must be specified by the two input arguments vary name and vary val.

Sign Conventions for Voltages and Currents

Parameter	Sign Convention
I_bn, Il_bn, In	Branch current, inductor current of branch n , or current of source #n is oriented from node1 to node2.
I_bn_nx	Current at one end (node x) of a PI transmission line. If $x = node1$, the current is entering the line. If $x = node2$, the current is leaving the line.
Uc_bn, Un	Voltage across capacitor or source voltage (Unode1 - Unode2).
U_nx1_x2	Voltage between nodes $x1$ and $x2 = Ux1 - Ux2$. Voltage of node $x1$ with respect to node $x2$.

Order in Which Outputs Must Be Specified

The functions that compute the state space representation of a system expect the outputs to be in a certain order. This order must be respected in order to obtain correct results. You must be particularly careful if the system contains any switches. The following list gives the proper ordering of outputs:

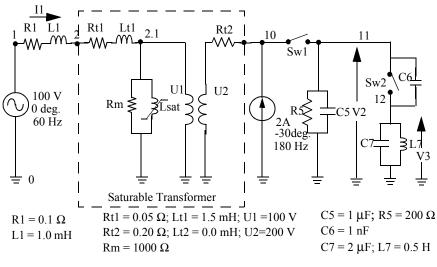
- 1 The voltages across all switches that have a null inductance (Lon = 0), if any
- **2** The currents of all switches that have a null inductance (Lon = 0), if any, in the same order as the voltages above
- **3** The voltages across all nonlinear models that have a finite inductance (switches with Lon > 0, the magnetizing inductance in saturable transformers, etc.)
- 4 All other voltage and current measurements that you request, in any order

Refer to the Example section below for an example illustrating proper ordering of outputs for a system containing nonlinear elements.

Example

The following circuit consists of two sources (one voltage source and one current source), two series RLC branches (R1-L1 and C6), two parallel RLC branches (R5-C5 and L7-C7), one saturable transformer, and two switches (Sw1 and Sw2). Sw1 is initially closed whereas Sw2 is initially open. Three measurement outputs are specified (I1, V2, and V3). This circuit has seven

nodes numbered 0, 1, 2, 2.1, 10, 11, and 12. Node 0 is used for the ground. Node 2.1 is the internal node of the transformer where the magnetization branch is connected.



Sw1: R=0.01 Ω ; L = 0H; initial state = closed Sw2: R=0.1 Ω ; L = 0H; initial state = open

Linear state space. You can use the circ2ss function to find the state space model of the linear part of the circuit. The nonlinear elements Sw1, Sw2, and Lsat must be modeled separately by means of current sources driven by the voltage appearing across their terminals. Therefore you must provide three additional currents sources and three additional voltage outputs for interfacing the nonlinear elements to the linear circuit.

You can find the state space model of the circuit by entering the following commands in a MATLAB script file. The example is available in the psbcirc2ss.m file. Notice that an output text file containing information on the system is requested in the call to circ2ss.

```
unit='OMU'; % Units = Ohms, mH and uF
rlc=[
%N1N2 typeR L C(uF)/U(V)
```

```
1 2
       0
           0.1 1
                   0
                       %R1 L1
2
  0
       2
           0.051.5 100 %transfo Wind.#1
10 0
           0.200
                   200 %transfo Wind.#2
2.10
           10000
                     %transfo mag. branch
11 0
       1
           200 0
                   1
                       %R5 C5
11 12
       0
           0
               0
                   1e-3%C6
12 0
               500 2
                       %L7 C7
];
source=[
%N1N2 typeU/I phasefreq
               0
10 11
           0
                   0
                       %Sw1
11 12
           0
               0
                   0
                       %Sw2
      1
2.10
       1
           0
               0
                   0
                       %Saturation
1 0
       0
         100 0
                   60 %Voltage source
0 10 1
           2 -30 180 %Current source
];
switches=[
%N1N2 statusR(ohm)L(mH)I#U#
10 11
       1
           0.010
                  1
                       1
                           %Sw1
11 12 0
           0.1 0
                   2
                       2
                           %Sw2
];
%outputs
% Both switches have Lon=0, so their voltages must be the first
outputs.
% immediatly followed by their currents (in the same order as the
voltages).
% The voltage across all nonlinear models which don't have L=0
follow (in
% this case the saturable transformer's magnetizing inductor). The
measure-
% ments which you request follow, in any order.
y u1='U n10 11';%U Sw1= Voltage across Sw1
y u2='U n11 12';%U Sw2= Voltage across Sw2
y i3='I1'; %I1= Switch current Sw1
y i4='I2'; %I2= Switch current Sw2
```

```
y u5='U n2.1 0';%U sat= Voltage across saturable reactor
  y i6='I b1';%I1 measurement
  y_u7='U_n11_0';%V2 measurement
  y u8='U n12 0';%V3 measurement
  yout=char(y_u1,y_u2,y_i3,y_i4,y_u5,y_i6,y_u7,y_u8);% outputs
  y_type=[0,0,1,1,0,1,0,0];%output types; 0=voltage 1=current
  % Open file that contains circ2ss output information
  fid=fopen('psbcirc2ss.net','w');
  [A,B,C,D,states,x0,x0sw,rlsw,u,x,y,freq,Asw,Bsw,Csw,Dsw,Hlin]=...
  circ2ss(rlc,switches,source,[],yout,y type,unit,[],[],[],0,fid);
Command line messages. While circ2ss is executing the following messages are
displayed.
  Computing state space representation of linear electrical circuit
  (V2.0)...
  (4 states; 5 inputs; 7 outputs)
  Oscillatory modes and damping factors:
  F=159.115Hz zeta=4.80381e-08
  Steady state outputs @ F=0 Hz :
  y u1= OVolts
  y u2= OVolts
  y i3= OAmperes
  y i4= OAmperes
  y u5= OVolts
  y i6= OAmperes
  y u7= OVolts
  y u8= OVolts
  Steady state outputs @ F=60 Hz :
  y u1 = 0.009999 Volts < 3.168 deg.
  y u2 = 199.4 Volts < -1.148 deg.
  y i3 = 0.9999 Amperes < 3.168 deg.
  y i4 = 0 Amperes < 0 deg.
```

```
y_u5 = 99.81 Volts < -1.144 deg.
y_i6 = 2.099 Amperes < 2.963 deg.
y_u7 = 199.4 Volts < -1.148 deg.
y_u8 = 0.01652 Volts < 178.9 deg.

Steady state outputs @ F=180 Hz :
y_u1 = 0.00117 Volts < 65.23 deg.
y_u2 = 22.78 Volts < 52.47 deg.
y_i3 = 0.117 Amperes < 65.23 deg.
y_i4 = 0 Amperes < 0 deg.
y_u5 = 11.4 Volts < 53.48 deg.
y_i6 = 4.027 Amperes < 146.5 deg.
y_u7 = 22.83 Volts < 52.47 deg.
y u8 = 0.0522 Volts < 52.47 deg.
```

State space output. The names of the state variables are returned in the states string matrix.

```
states
states =
I1_b2_n2_2.1
Uc_b5_n11_0
Uc_b6_n11_12
I1_b7_n12_0
I1_b1_n1_2*
Uc_b7_n12_0*
```

Although this circuit contains a total of six inductors and capacitors, there are only four state variables. The names of the state variables are given by the first four lines of the states matrix. The last two lines are followed by an asterisk indicating that these two variables are a linear combination of the state variables. The dependencies can be viewed in the output file psbcirc2ss.net.

```
The following capacitor voltages are dependent: Uc_b7_n12_0 = + Uc_b5_n11_0 - Uc_b6_n11_12
The following inductor currents are dependent: Il_b1_n1_2 = + Il_b2_n2_0
```

The A,B,C,D matrices contain the state space model of the circuit without nonlinear elements (all switches open). The x0 vector contains the initial state values considering the switch Sw1 closed. The Asw, Bsw, Csw, and Dsw matrices

contain the state space model of the circuit considering the closed switch Sw1. The x0sw vector contains the initial current in the closed switch.

```
A
A =
    -4.0006e+05000
    0 -49950 -499.25
    0 -4992.504.9925e+05
    0 2 -2 0

Asw
Asw =
    -80.999-199.9900
    4.9947e+05-5244.70 -499.25
    4.9922e+05-5242.104.9925e+05
    0 2 -2 0
```

The system source frequencies are returned in the freq vector.

```
freq = 0 60 180
```

The corresponding steady state complex outputs are returned in the (6-by-3) y matrix where each column corresponds to a different source frequency.

For example, you can obtain the magnitude of the six voltage and current outputs at 60 Hz as follows:

```
abs(y(:,2))
ans =
0.0099987
199.42
0.99987
0
99.808
2.0993
199.41
0.016519
```

The initial values of the four state variables are returned in the x0 vector. You must use this vector in the State-Space block to start the simulation in steady state.

```
x0
x0 =
2.3302
14.111
14.07
3.1391e-05
```

The initial values of switch currents are returned in x0sw. To start the simulation in steady state you must use these values as initial currents for the nonlinear model simulating the switches.

```
x0sw
x0sw =
0.16155
```

The Simulink model of the circuit shown in the following figure is available in the psbcirc2ss_slk.mdl file. The linear part of the circuit is simulated by the sfun_psbcontc S-function. Appropriate inputs and outputs are used to connect the switch and saturable reactance models to the linear system. Notice that the status of each switch is fed back from the breaker block to the S-function, after the inputs mentioned earlier. You can find the Breaker and Transfosat blocks in the **powerlib_models**, library containing all the nonlinear models used by the blockset. As the breaker model is vectorized, a single block is used to simulate the two switches Sw1 and Sw2.

If you use the **powerlib** library to build your circuit, the same Simulink system is generated automatically by the power2sys function. The **powerlib** version of this system is also available in the psbcirc2ss_psb.mdl file and is shown below.

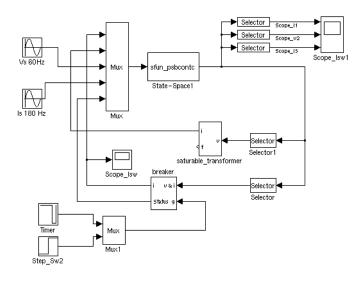


Figure 5-1: psbcirc2ss_slk.mdl Example Diagram

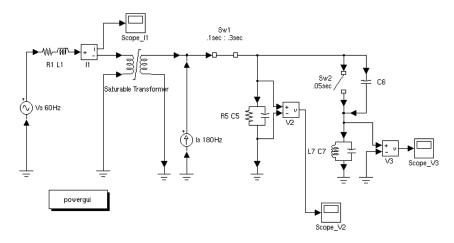


Figure 5-2: psbcirc2ss_psb.mdl Example Diagram

See Also power2sys

Purpose

Analyze an electric circuit built with Power System Blockset

Syntax

```
psb = power2sys('sys','structure')
psb = power2sys('sys','sort')
psb = power2sys('sys','ss')
[A,B,C,D,x0,states,inputs,outputs,uss,xss,yss,freqyss,Hlin]=
    power2sys('simwin');
psb = power2sys('sys','net')
```

Description

The power2sys function computes the equivalent state space model of the specified electrical model built with Power System Blockset. It evaluates the A, B, C, D standard matrices of the state space system described by the equations

```
\dot{x} = Ax + Bu
v = Cx + Du
```

where the state variables contained in the *x* vector are the inductor currents and capacitor voltages. Nonlinear elements are simulated by current sources driven by the voltages across the nonlinear elements.

The inputs of the system contained in the u vector are the voltage and current sources plus the current sources simulating the nonlinear elements. The following conventions are used for inputs:

- Source current flowing in the arrow direction is positive.
- Positive source voltage is indicated by a + sign on the icon.

The outputs of the system contained in the y vector are the voltage and current measurement plus the voltages across the nonlinear elements.

psb = power2sys('sys','structure') creates a structure array with fields and values describing the model 'sys'.

power2sys

The fields are defined in the following order.

Field	Description
circuit	Name of the model
states	char array of state variable names
inputs	char array of system input names
outputs	char array of system output names
A	nstates-by-nstates state space A matrix
В	nstates-by-ninput state space B matrix
С	noutput-by-nstates state space C matrix
D	noutput-by-ninput state space D matrix
x0	nstates-by-1 vector of initial conditions
Aswitch	A matrix including closed switches
Bswitch	B matrix including closed switches
Cswitch	C matrix including closed switches
Dswitch	D matrix including closed switches
x0switch	Vector of initial values of switch currents
uss	ninput-by-nfreq steady state values of inputs
xss	nstates-by-nfreq steady state values of states
yss	noutput-by-nfreq steady state values of outputs
Hlin	nfreq-by-noutput-by-ninput transfer function of impedances
frequencies	1-by-nfreq vector of input source frequencies
LoadFlow	Load flow information for circuits with machines
OscillatoryModes	Oscillatory modes of linear part of the system

The table uses the following conventions:

- nstates is the number of states.
- ninput is the number of inputs.
- noutput is the number of outputs.
- nfreg is the number of input source frequencies.

states is a string matrix containing names of the state variables. Each line of states begins with a prefix Uc_ for capacitor voltages or I1_ for inductor currents, followed by the name of the block in which the element (C or L) is found. Inductor current direction and capacitor voltages polarities are defined by the input and output of the block. The following conventions are used:

- Current flowing in the arrow direction is positive.
- Voltage equals Vinput Voutput.

A suffix is added to the line for blocks containing more than two inductances or capacitors. For example, the Linear Transformer blocks produce three lines in the states matrix, one for each leakage inductance, with the suffix windingx, where x is the winding number of the transformer.

inputs is a string matrix containing names of the inputs of the system. Each line of inputs begins with a prefix U_ for voltage sources or I_ for current sources, followed by the name of the source block.

A suffix can be added to the input for blocks containing more than one source. For example, the Simplified Synchronous Machine block produces two current inputs with suffixes AB and BC.

outputs is a string matrix containing names of the outputs of the state space system (vector y). Each line of outputs begins with a prefix U_ for voltage outputs or I_ for current outputs, followed by the name of the block that produces the output. Sign conventions are indicated by the polarities of the voltage measurement and current measurement blocks.

A,B,C,D are the state space matrices of the linear part of the model.

x0 is a vector containing the initial conditions of the state variables listed in the states variable.

uss, xss, and yss are complex matrices containing the steady state values of inputs, states and outputs. If voltage and current sources all generate the same

power2sys

frequency, these are column vectors. If sources with different frequencies are used, each column of the matrices corresponds to a frequency contained in the frequencies vector.

frequencies is a column vector containing the input source frequencies ordered by increasing values.

Hlin is the complex transfer impedance three-dimensional array (nfreq-by-noutput-by ninput) of the linear system corresponding to the frequencies contained in the frequencies vector. For a particular frequency, Hlin is defined by

```
yss(:,ifreq) = Hlin(ifreq,:,:) * uss(:,ifreq)
```

psb = power2sys('sys','sort') returns a structure array with the following fields related to the interconnection of Power System Blockset blocks in a model. The fields are defined in the following order.

Field	Description
circuit	Name of the model
SampleTime	Sample time for discrete systems
RlcBranch	rlc matrix in the circ2ss format
RlcBranchNames	List of blocks containing the state variable
SourceBranch	Source matrix in the circ2ss format
SourceBranchNames	Names of the blocks defined as sources
InputNames	Names of the inputs of the system
OutputNames	Names of the outputs of the system
OutputExpressions	Output expression in the circ2ss format
OutputMatrix	Output expression in matrix format (internal)
MeasurementBlocks	Names of the voltage and current measurement

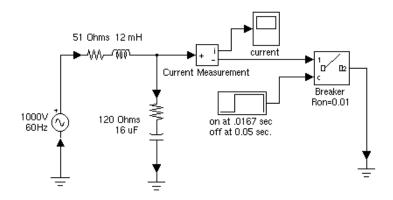
[A,B,C,D,x0,states,inputs,outputs,uss,xss,yss,frequencies,Hlin] = power2sys('sys') returns the state space calculations into separate variables.

If you own the Control System Toolbox, psb = power2sys('sys','ss') creates a continuous state space model of the model sys with matrices *A*, *B*, *C*, *D*. The output is a state space object.

Netlist. When called with an extra argument, 'net', powers2sys generates a netlist stored in a file, sys.net. This file contains the node numbers automatically generated by power2sys, as well as parameter values of all linear elements. See the formats described in the circ2ss reference page.

Example

Obtain the state space matrices and steady state voltages and currents for the psbnetsim2.mdl circuit.



The command

```
psb = power2sys('psbnetsim2','structure')
```

returns the state space model in the psb structure variable.

```
psb.xss =
   1.0e+02 *
   4.8392 - 5.1314i
   0.0310 + 0.0292i
psb.yss =
   1.0e+02 *
   8.5535 - 1.6287i
    0
    0
psb.inputs =
   I Breaker
   U 1000V 60Hz
psb.outputs =
   U Breaker
   I Breaker
   I Current Measurement
```

The inductor of the 51 ohms 12 mH block and the capacitor of the 120 ohms 16 uF block are the two state variables in this circuit. The Breaker block is a nonlinear element that is represented by the first current source driven by the voltage across the breaker (the first output). Note that the current of the Breaker block is also an output of the system (third output).

See Also

circ2ss, powerinit, Powergui

Purpose

Set the initial states values of a model built with Power System Blockset

Syntax

```
powerinit('sys','look')
powerinit('sys','reset')
powerinit('sys','steady')
powerinit('sys','set',X0)
powerinit('sys','setb','StateVariableName',Value)
```

Description

powerinit('sys','look') displays the current initial states for the specified system.

powerinit('sys','reset') resets to zero the initial states of the specified system.

powerinit('sys','steady') sets the initial states of the specified system in order to start the simulation in steady state.

powerinit('sys','set',X0) sets the initial states values of the model sys to the specified vector X0. The ordering of the variables states is given by the powerinit('sys','look') command.

powerinit('sys','setb','StateVariableName',Value) sets the initial state variable specified in 'StateVariableName' to Value. The names of the variables states are given by the powerinit('sys','look') command.

Example

The following commands reset to zero the initial states values of the psbfilter.mdl demo.

```
psbfilter
powerinit('psbfilter','reset')
```

This command returns the names of the states and their current values.

```
powerinit('psbfilter','look')
Initial states for a particular case:
I1_5th Harm. Filter = 0
Uc_5th Harm. Filter = 0
I1 Zsource = 0
```

See Also

power2sys, Powergui

powerinit

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